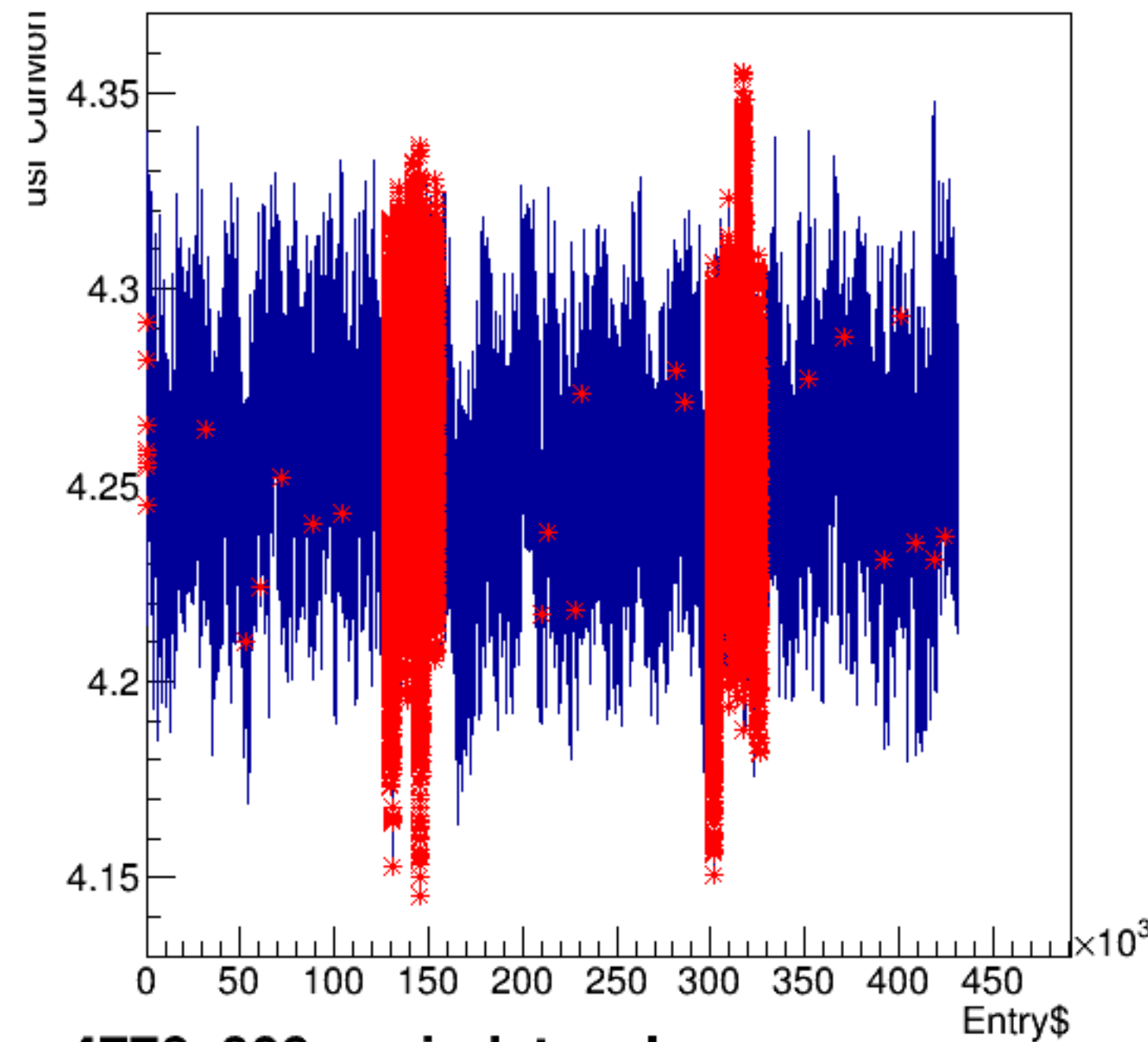
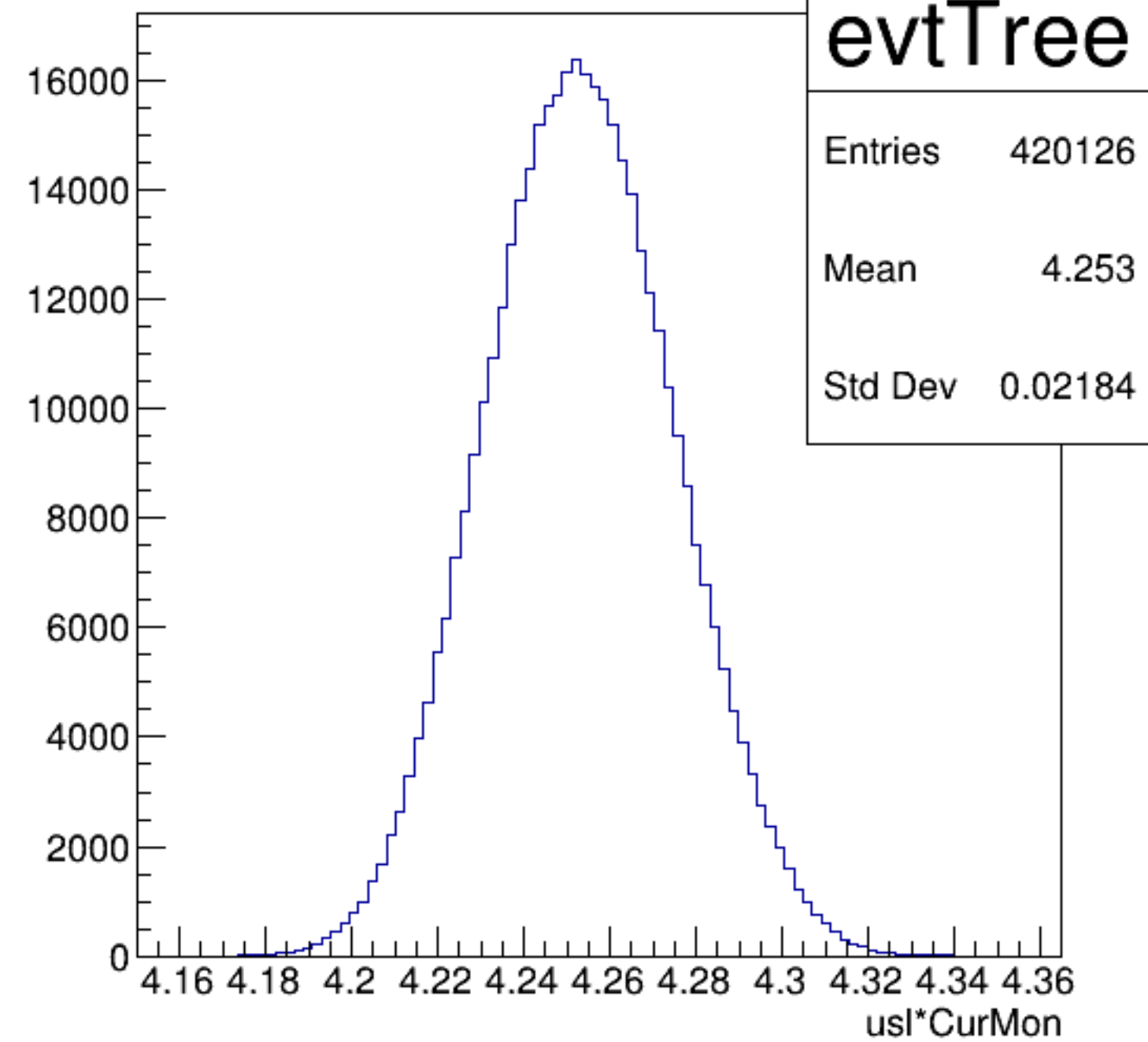


usi*CurMon:Entry\$



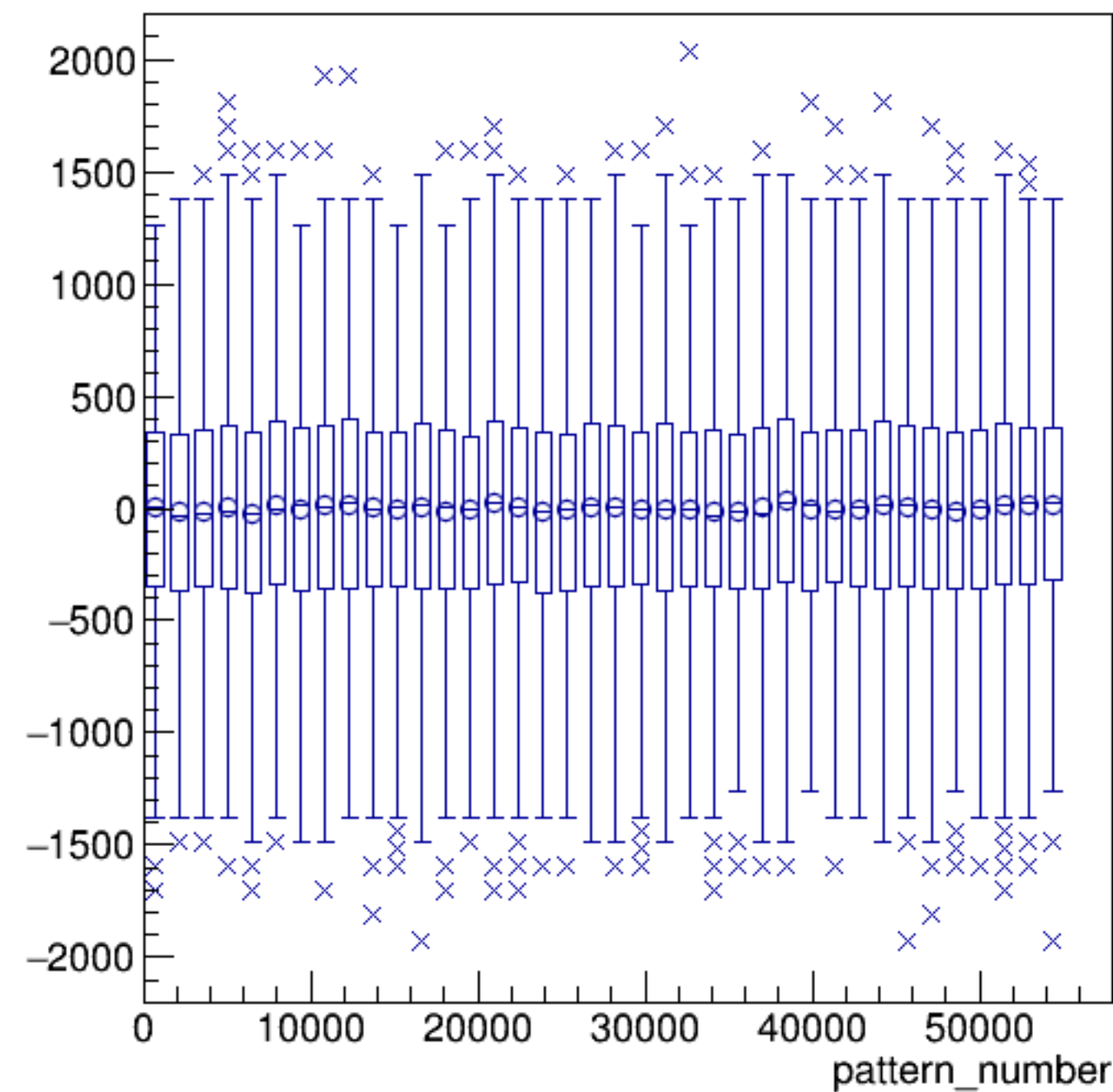
run4776_000_maintet_usl.png

usi*CurMon {ErrorFlag==0}

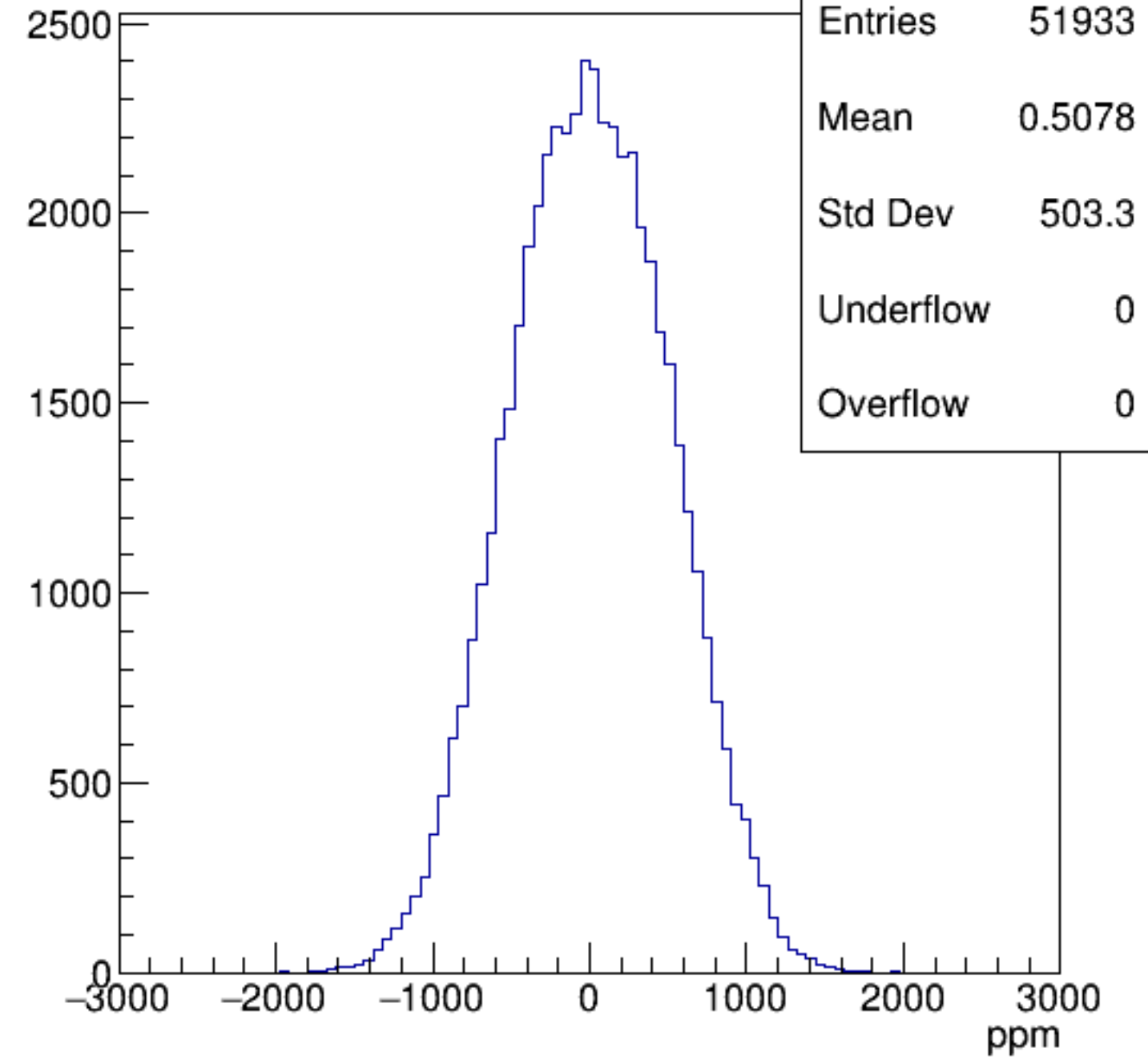


evtTree	
Entries	420126
Mean	4.253
Std Dev	0.02184

asym_usl/ppm:pattern_number {ErrorFlag==0}

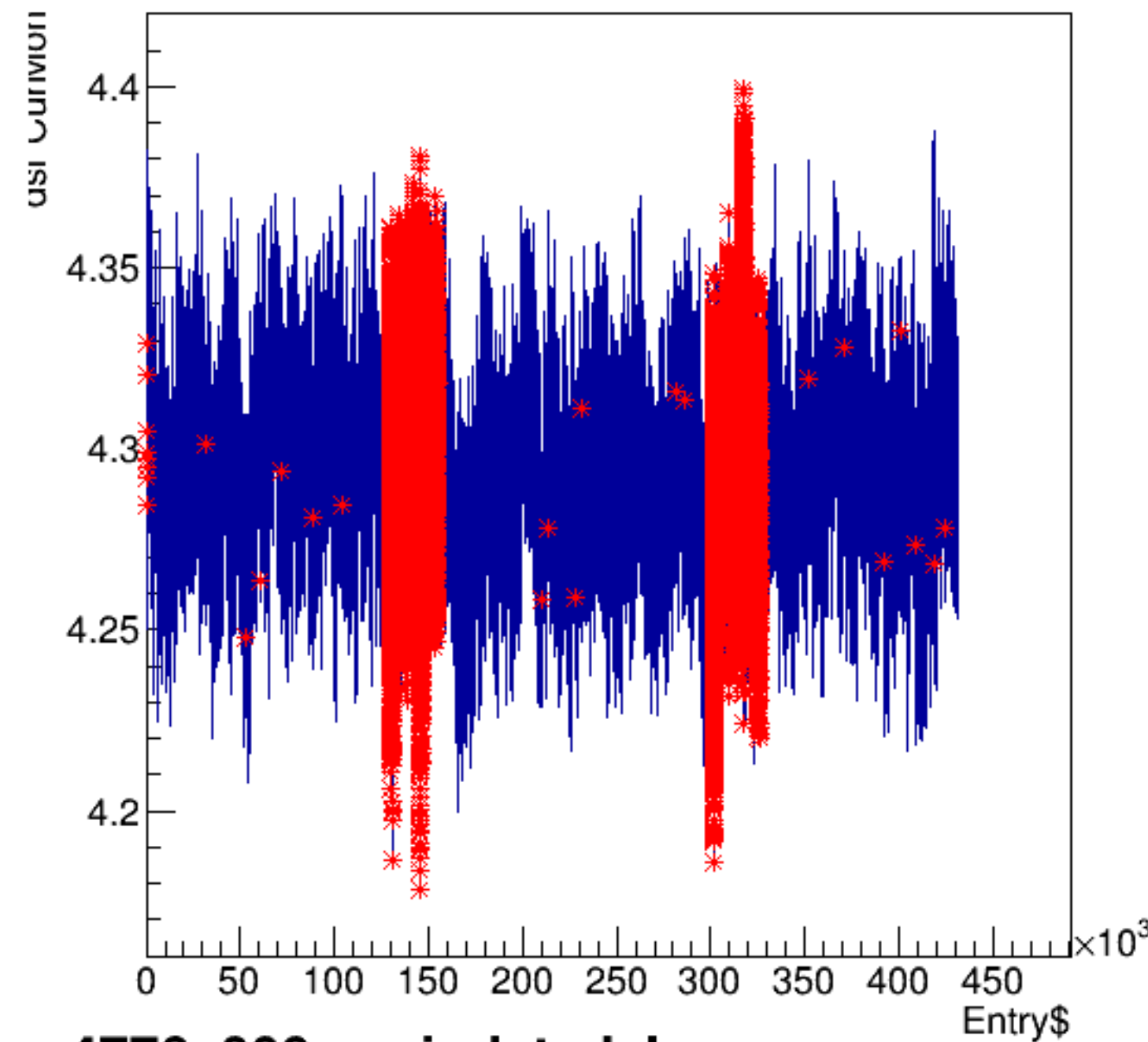


asym_usl



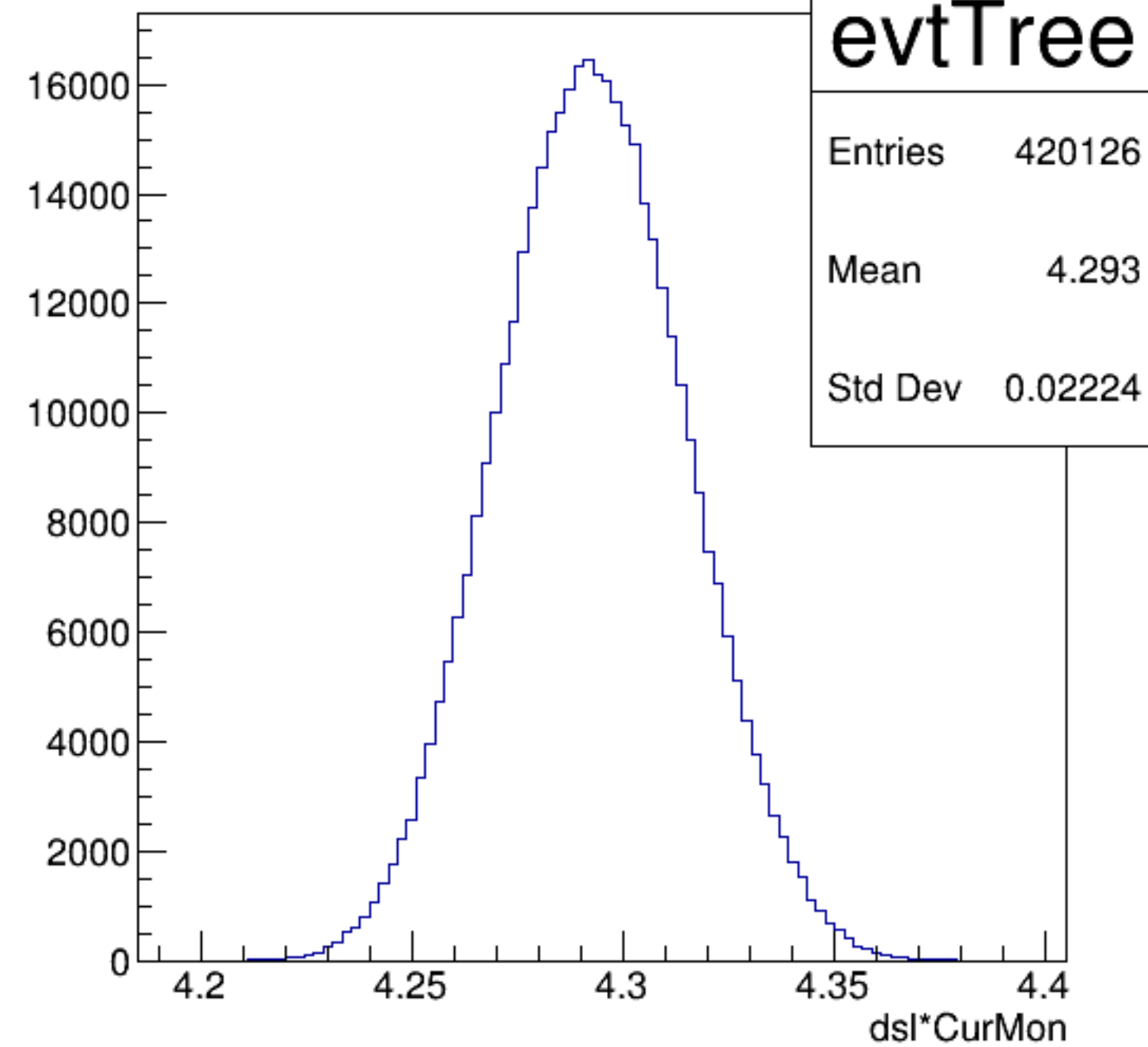
Entries	51933
Mean	0.5078
Std Dev	503.3
Underflow	0
Overflow	0

dsl*CurMon:Entry\$

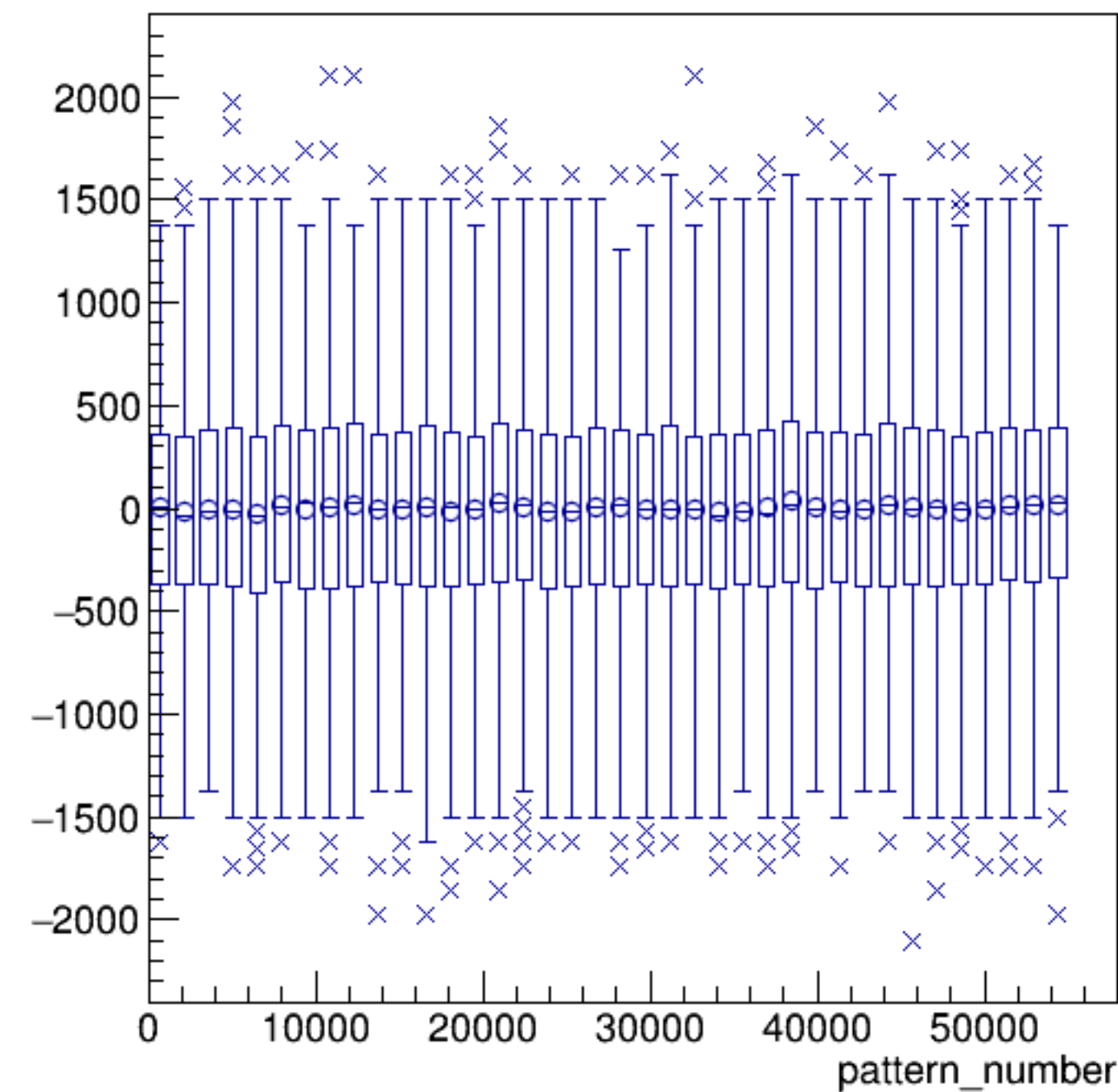


run4776_000_maintet_dsl.png

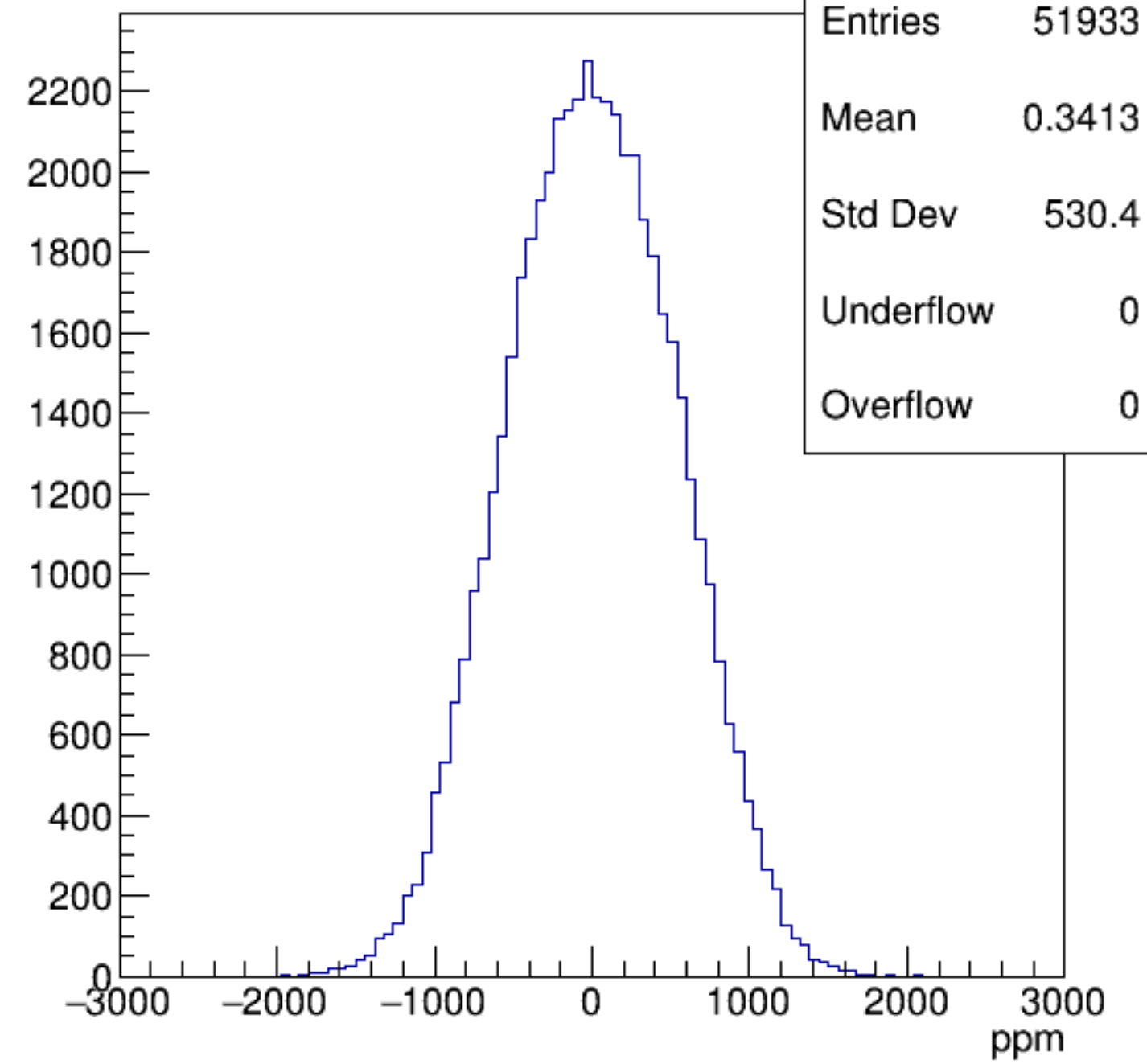
dsl*CurMon {ErrorFlag==0}



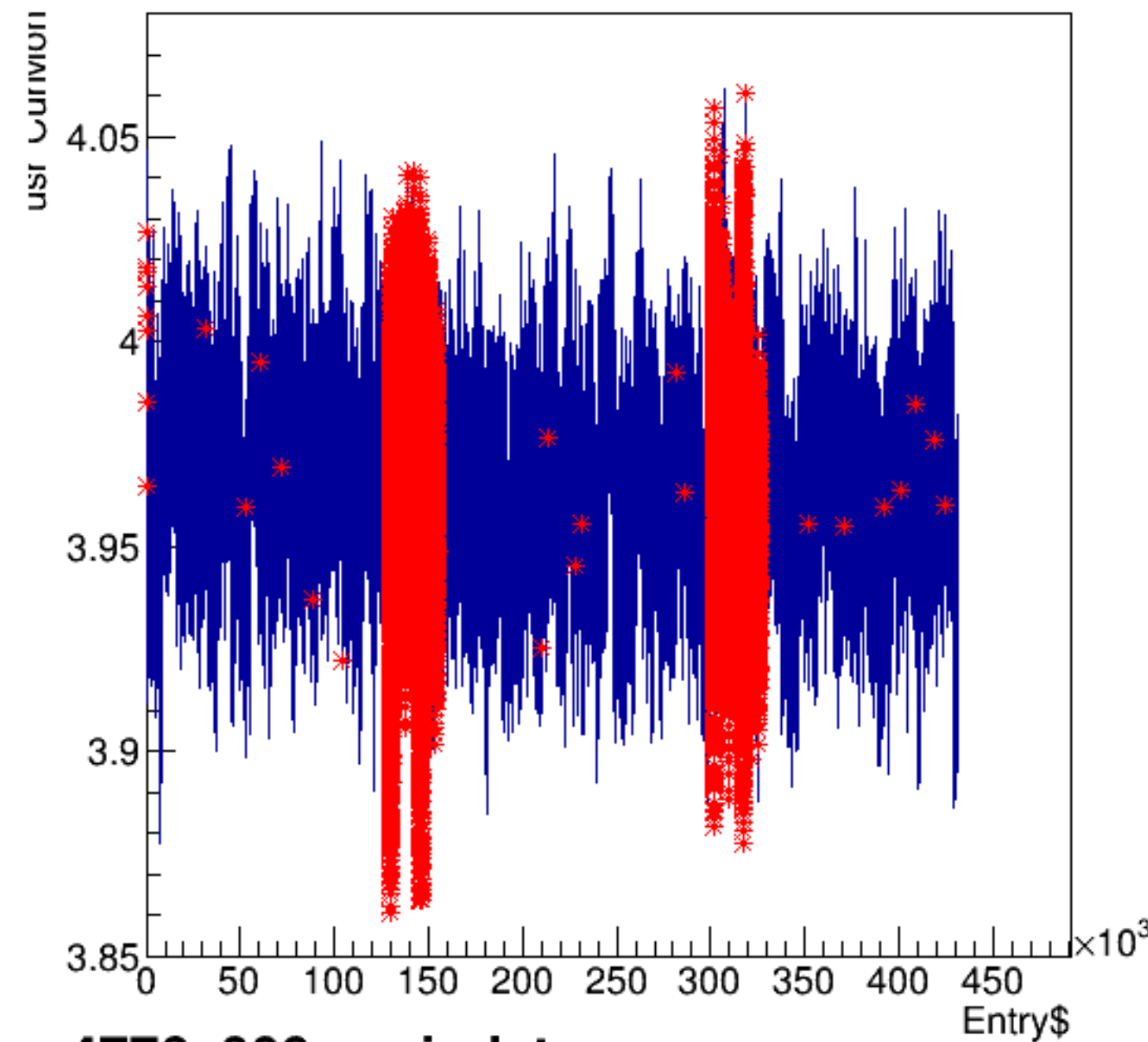
asym_dsl/ppm:pattern_number {ErrorFlag==0}



asym_dsl

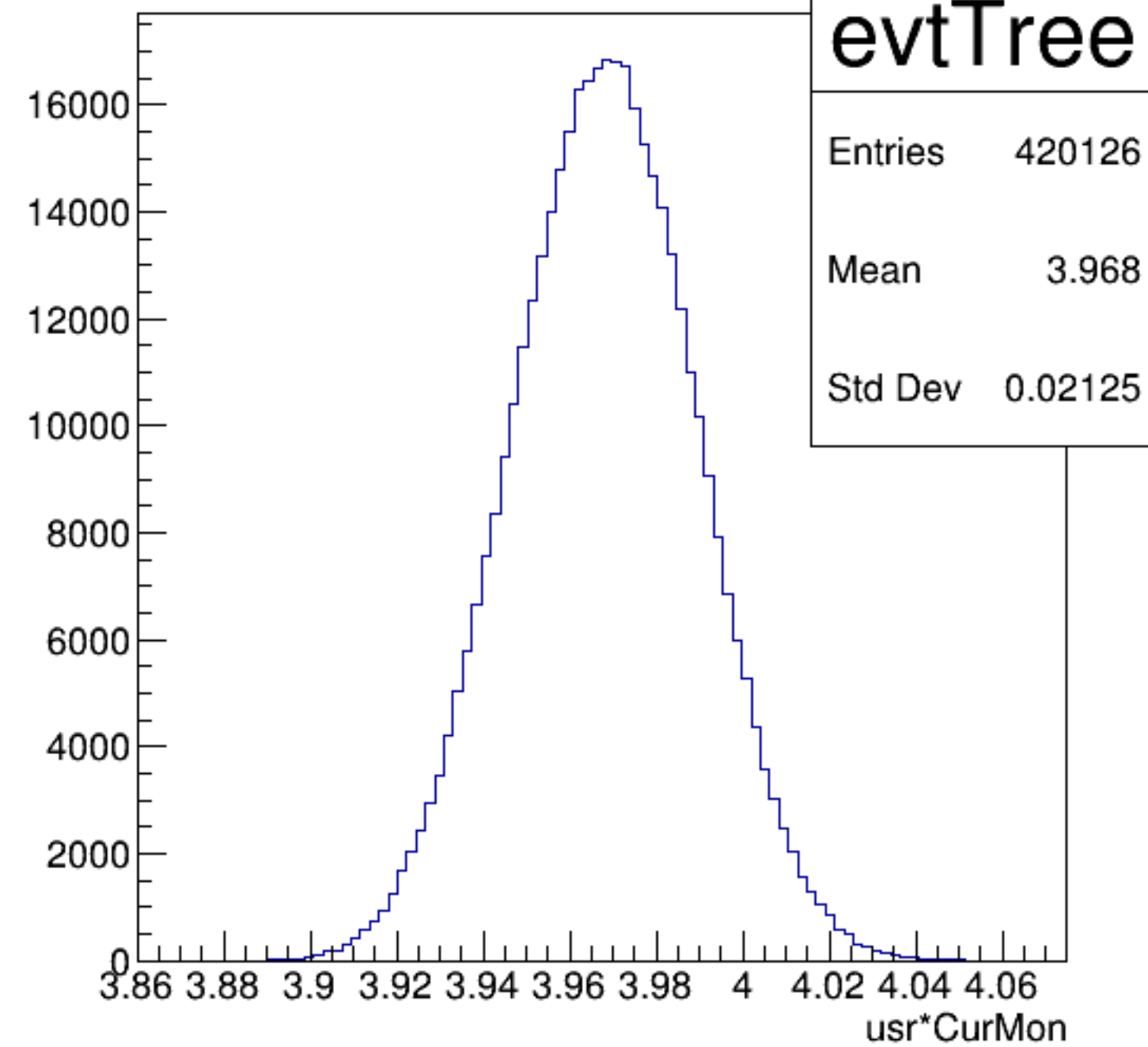


usr*CurMon:Entry\$

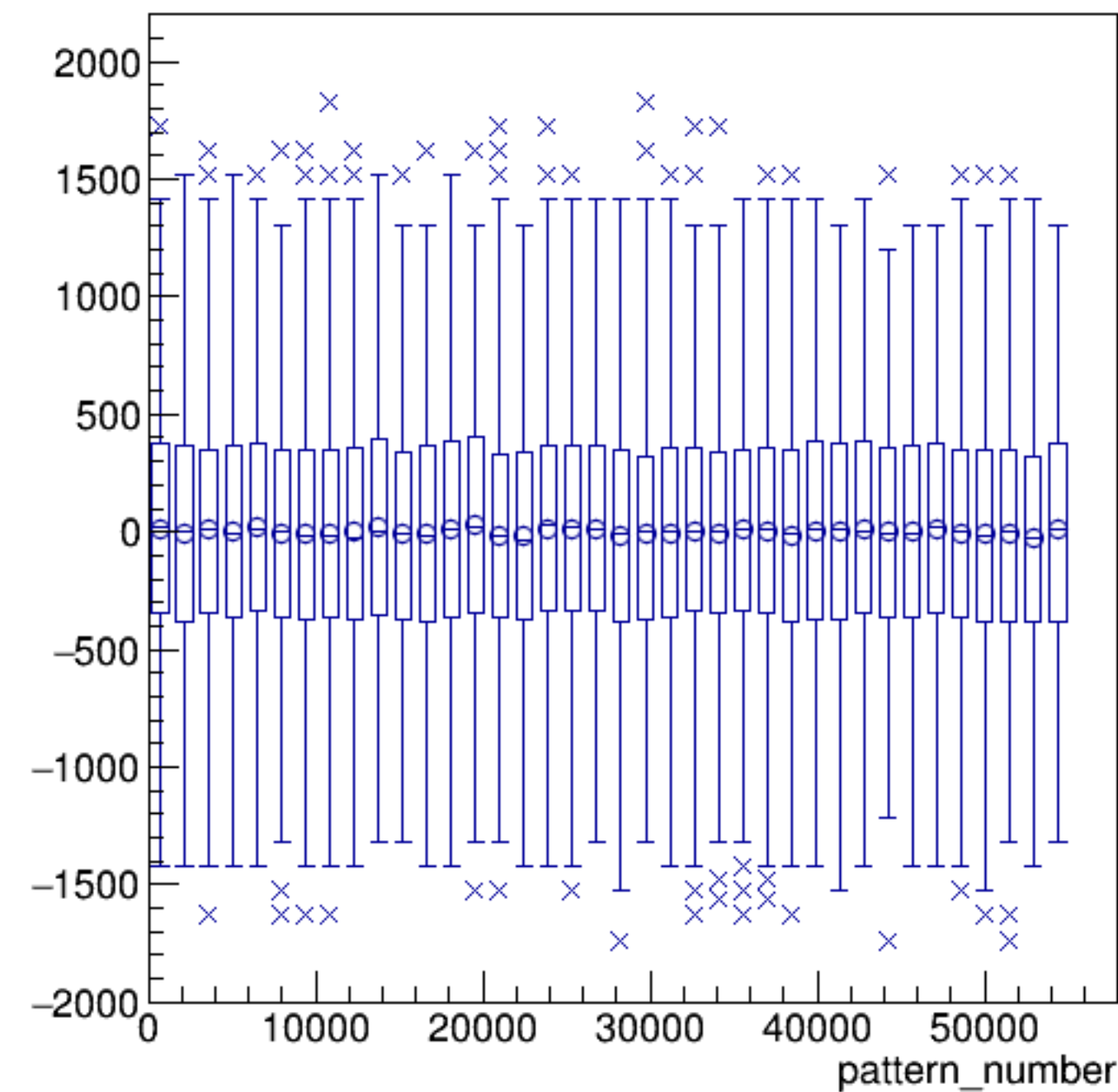


run4776_000_maintet_usr.png

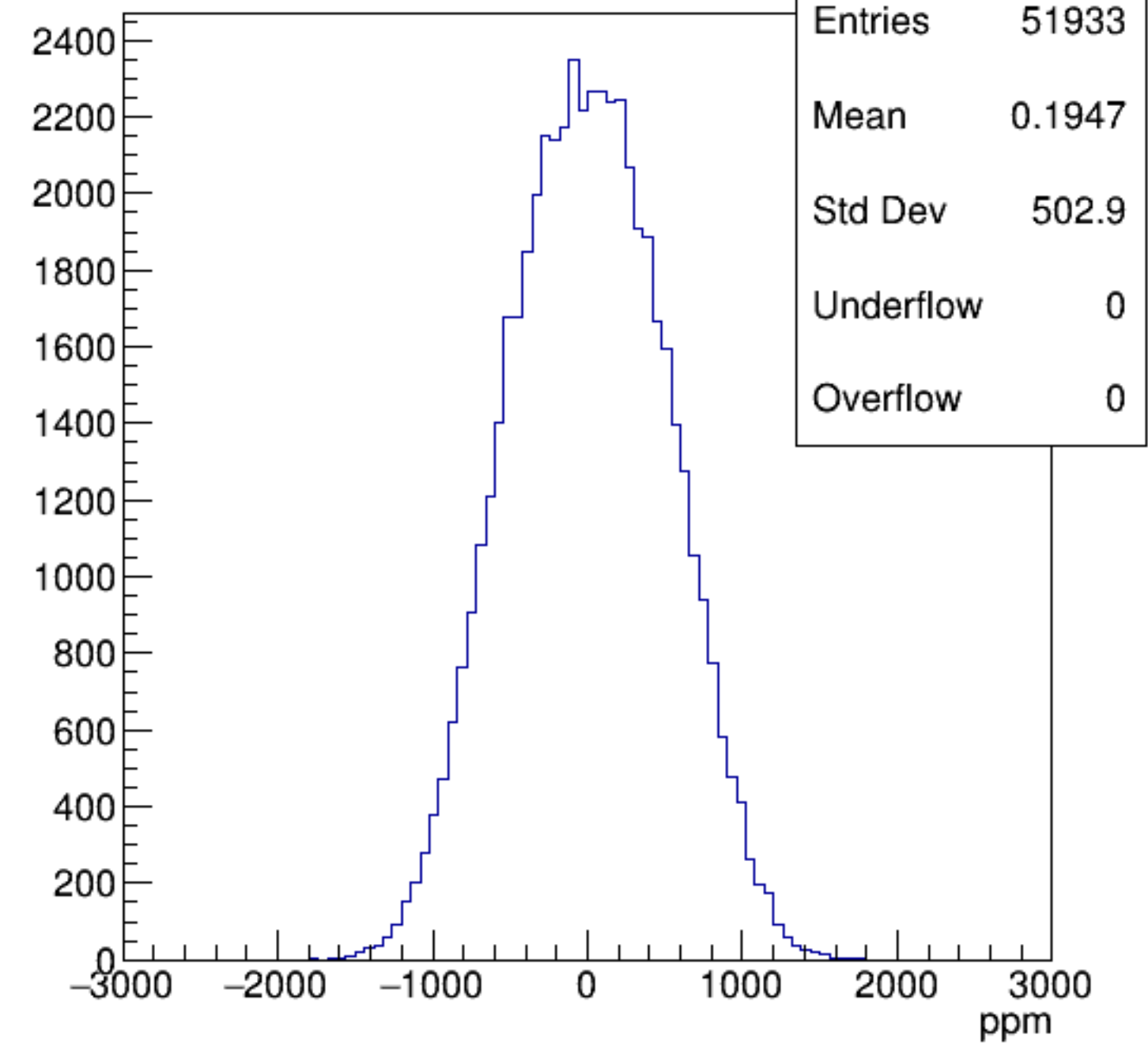
usr*CurMon {ErrorFlag==0}



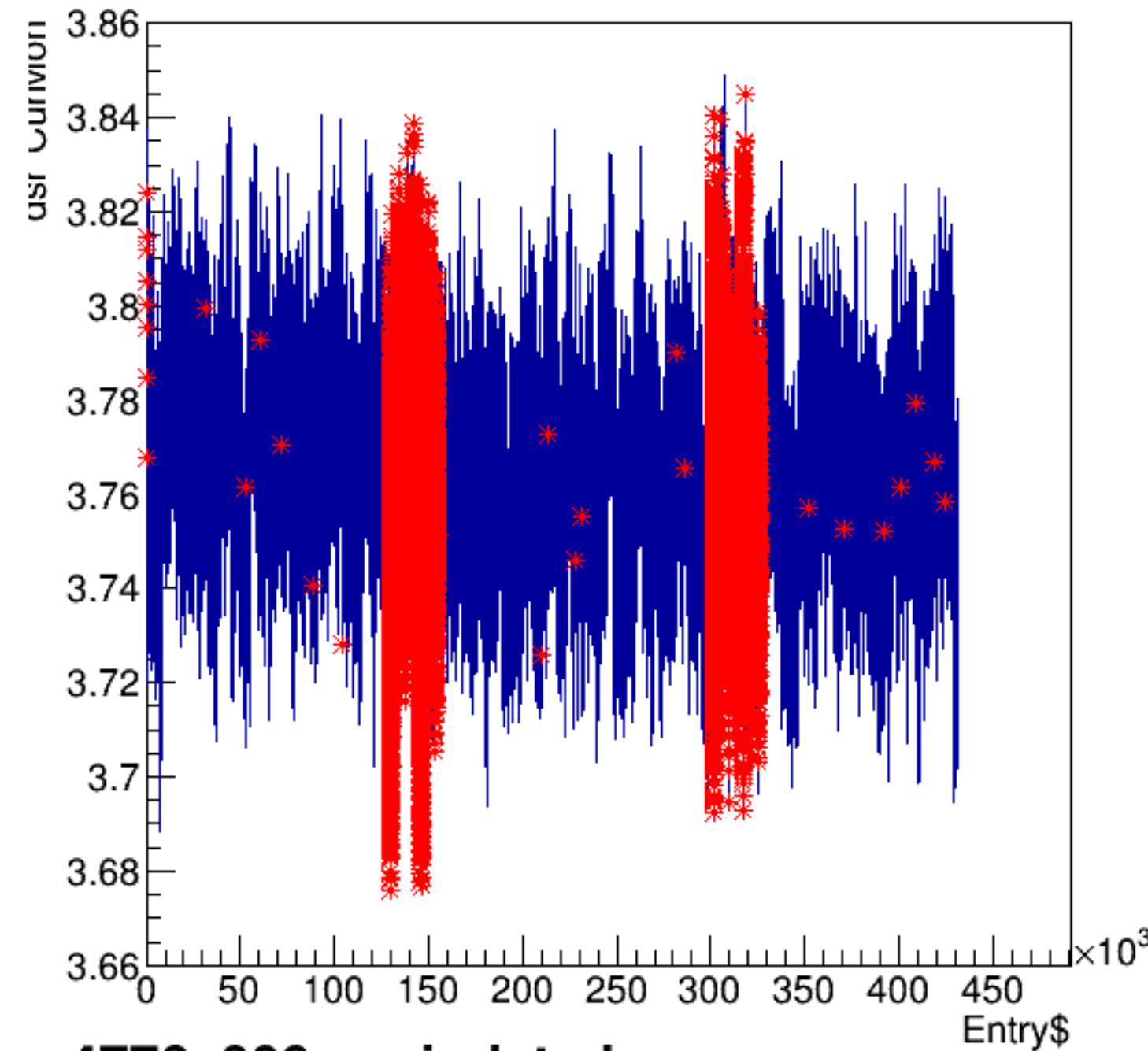
asym_usr/ppm:pattern_number {ErrorFlag==0}



asym_usr

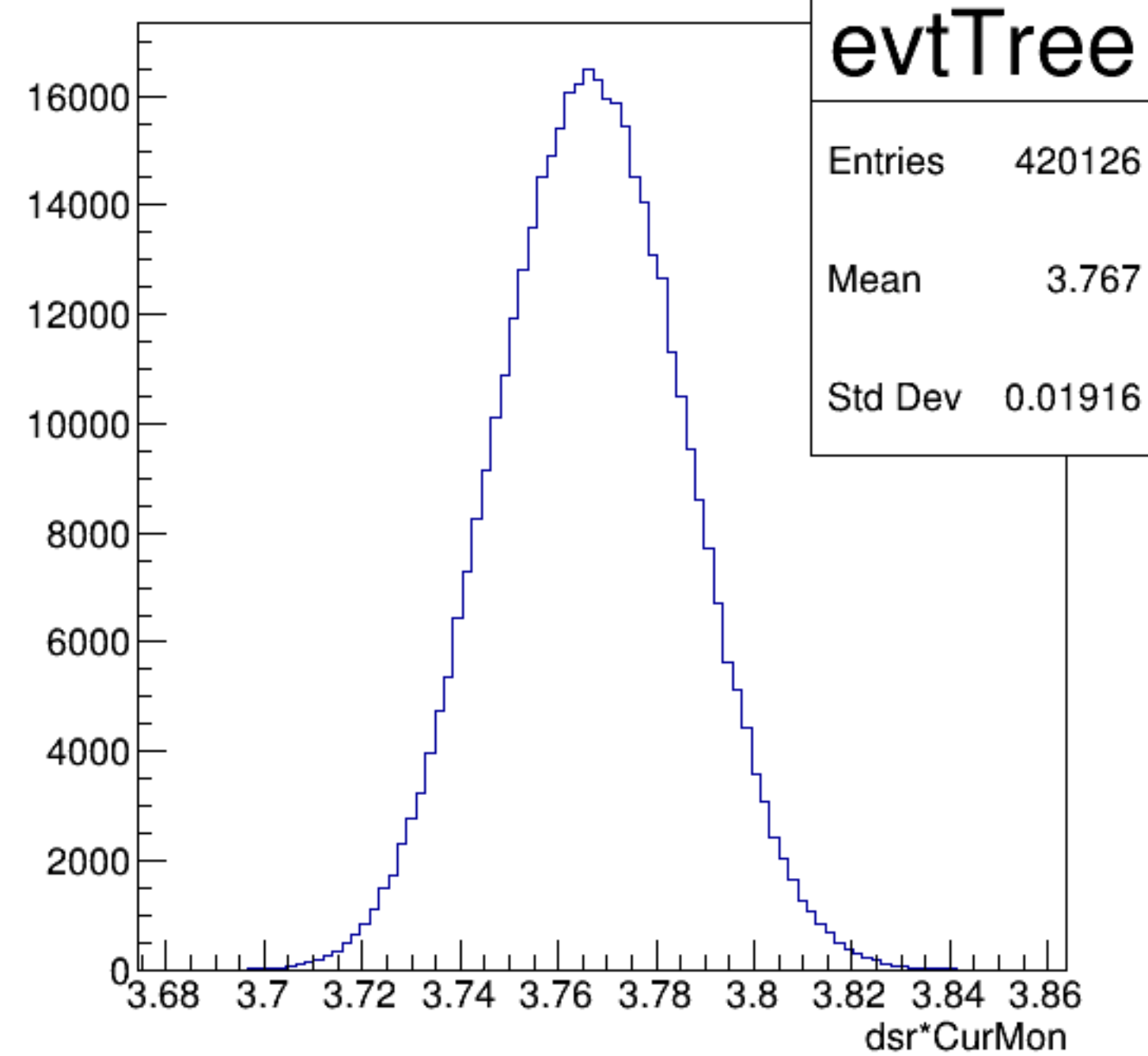


dsr*CurMon:Entry\$

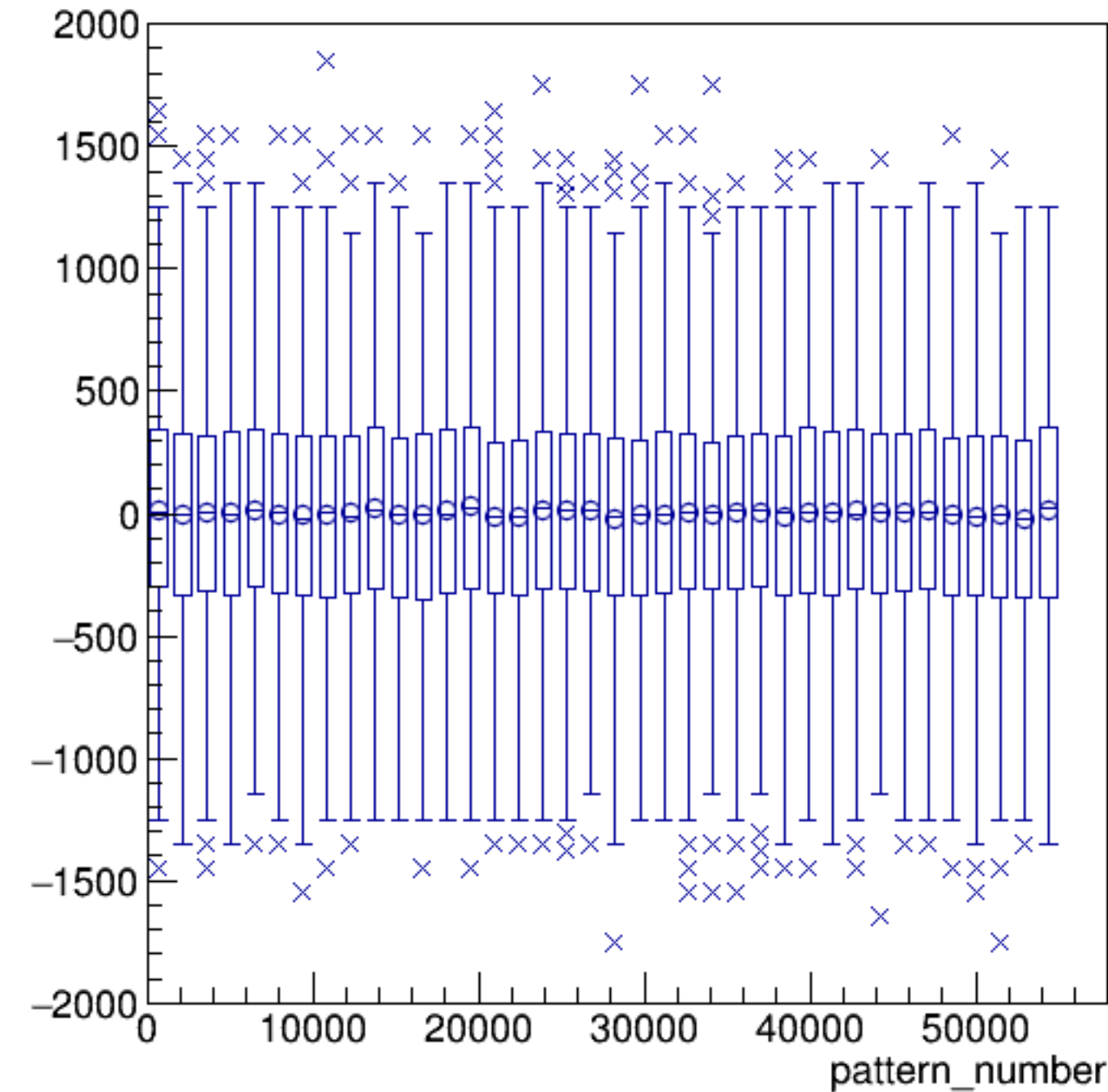


run4776_000_maindet_dsr.png

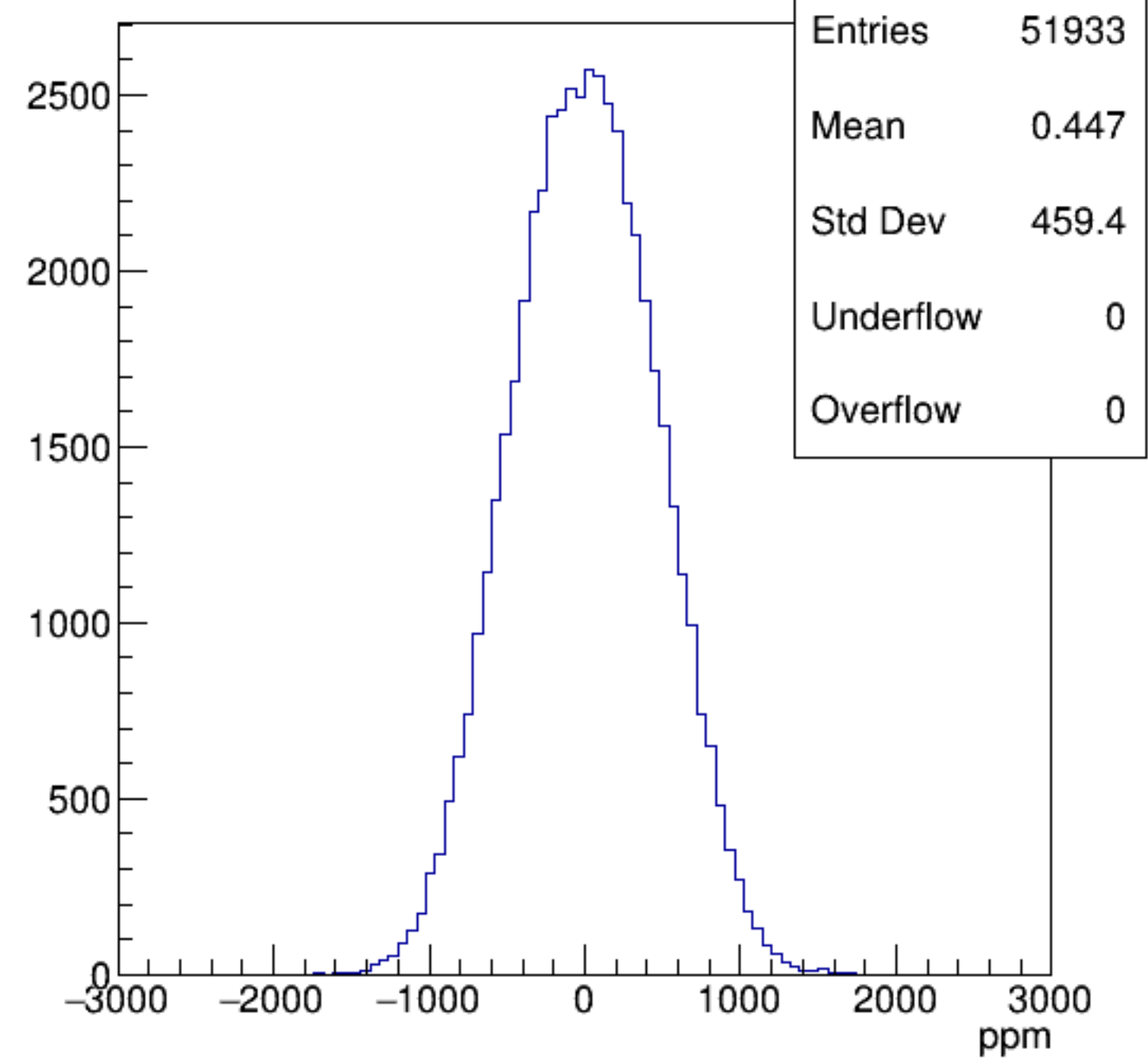
dsr*CurMon {ErrorFlag==0}



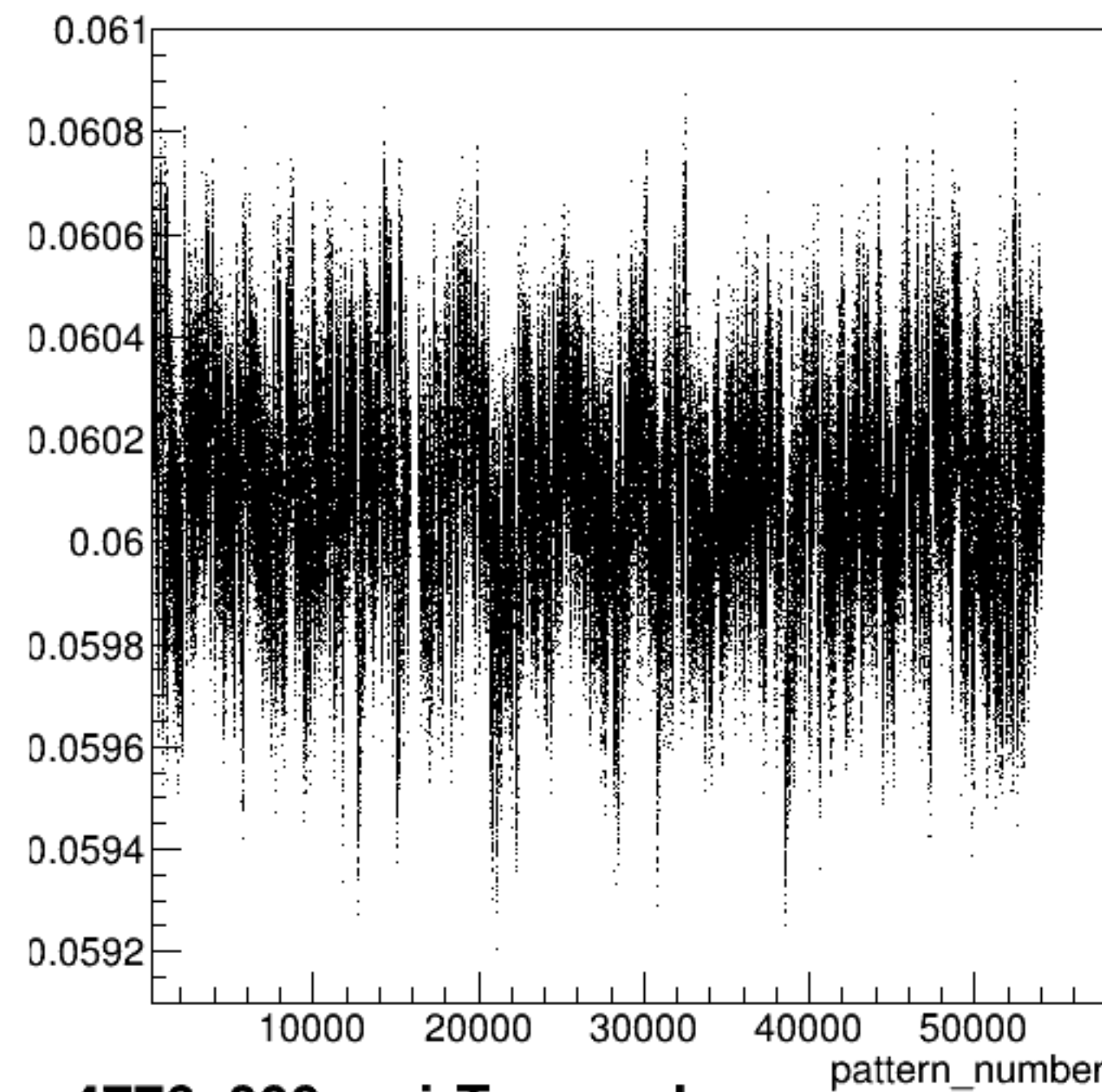
asym_dsr/ppm:pattern_number {ErrorFlag==0}



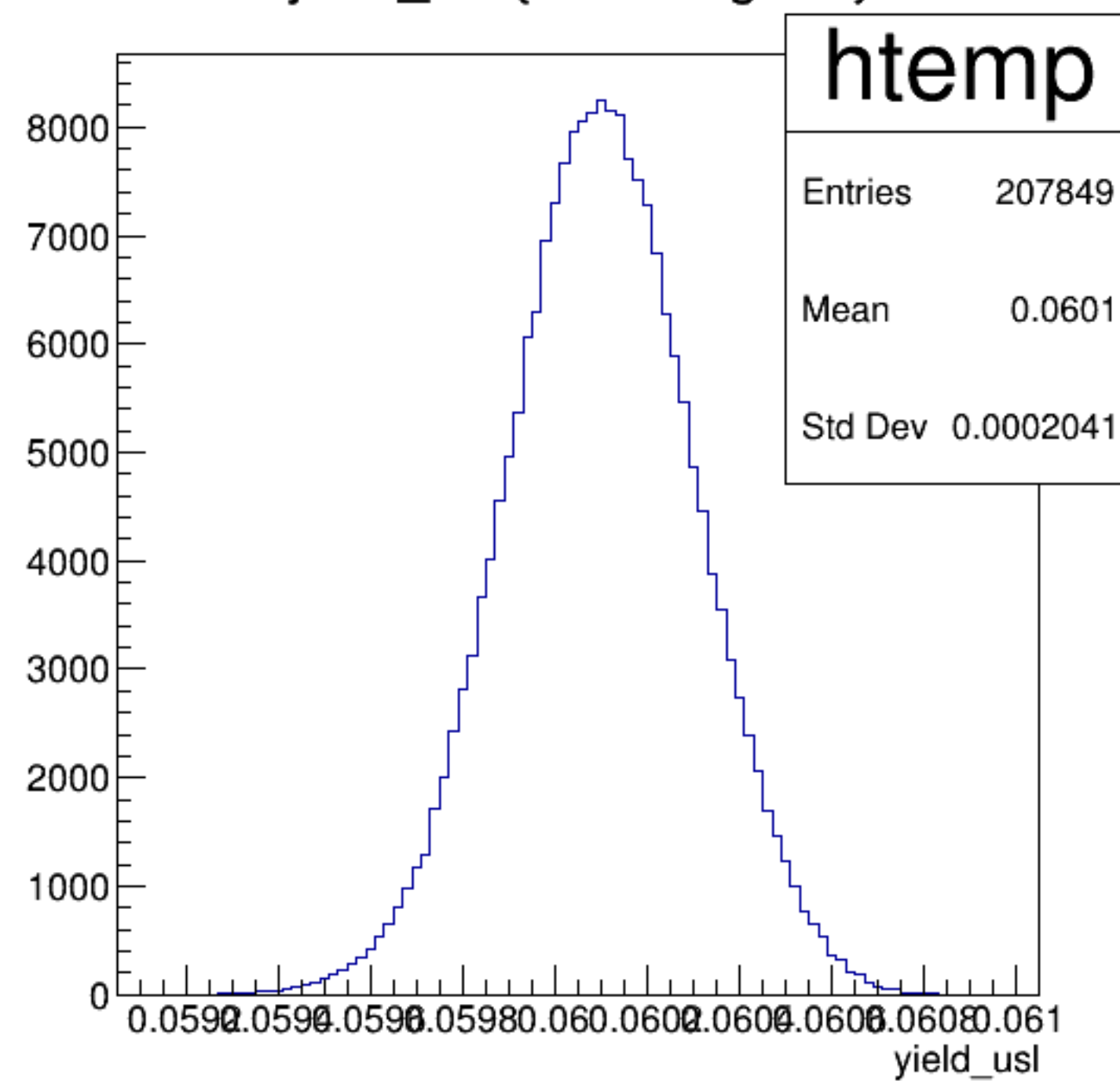
asym_dsr



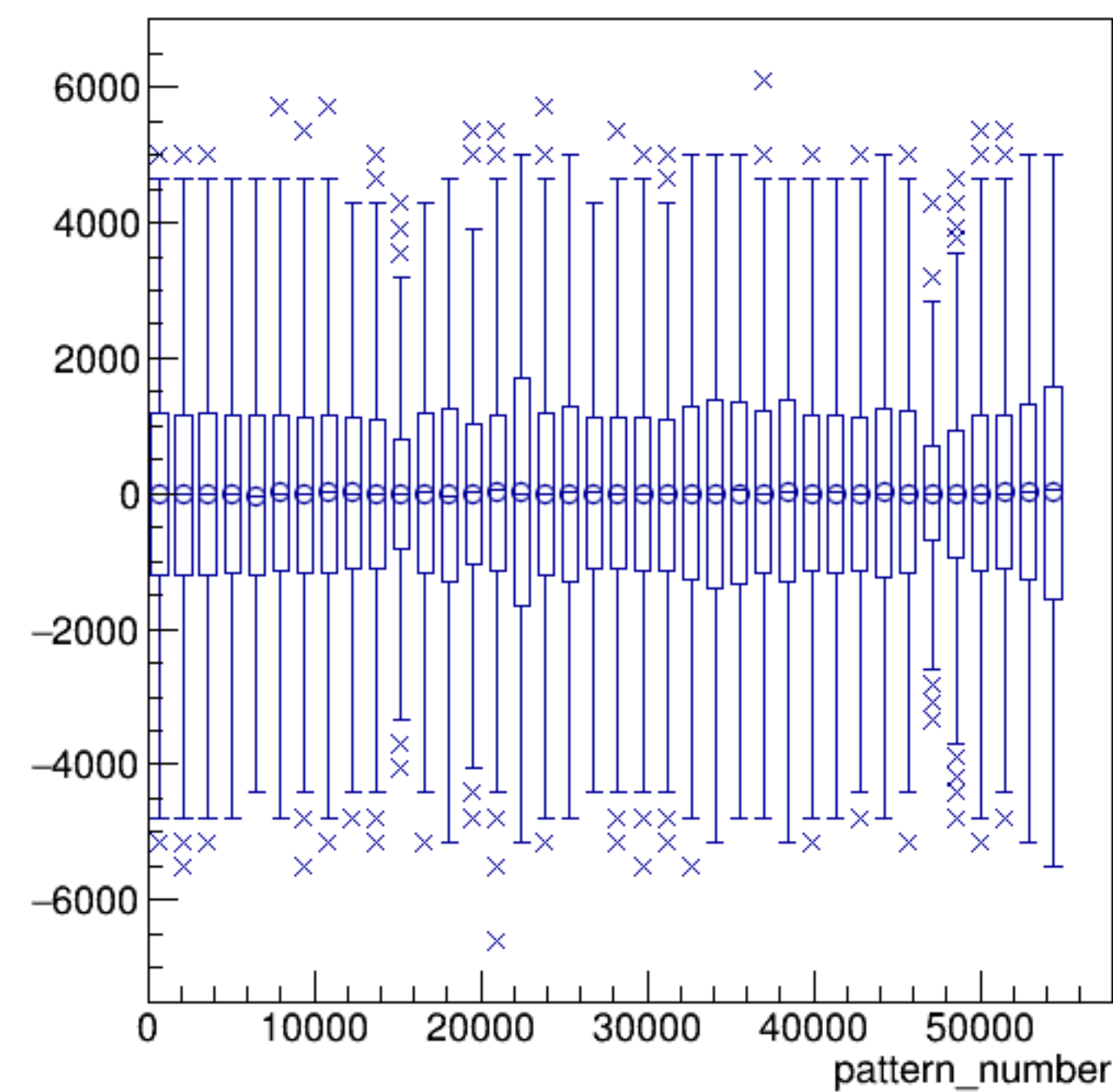
yield_usl:pattern_number {ErrorFlag==0}



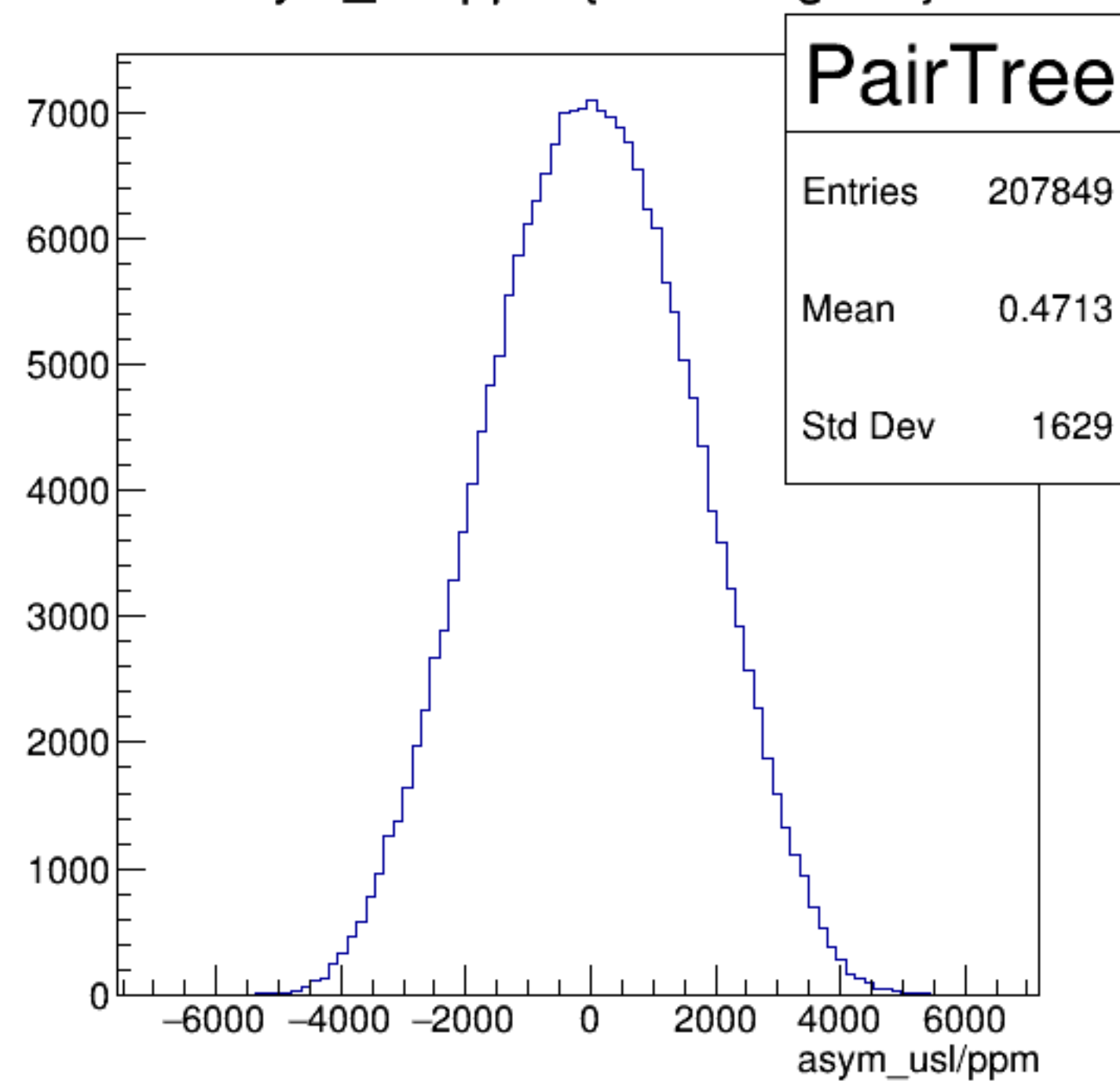
yield_usl {ErrorFlag==0}



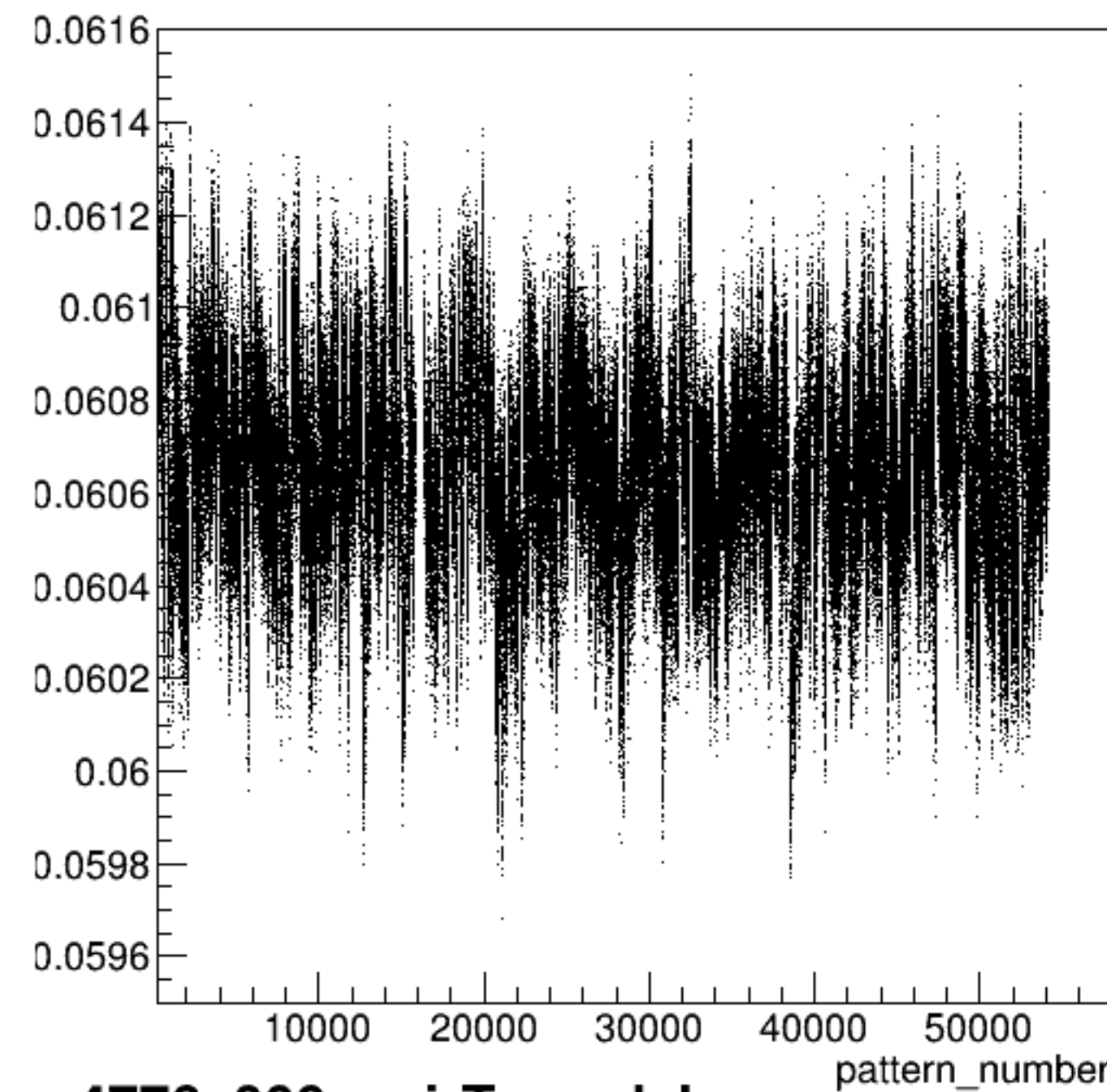
asym_usl/ppm:pattern_number {ErrorFlag==0}



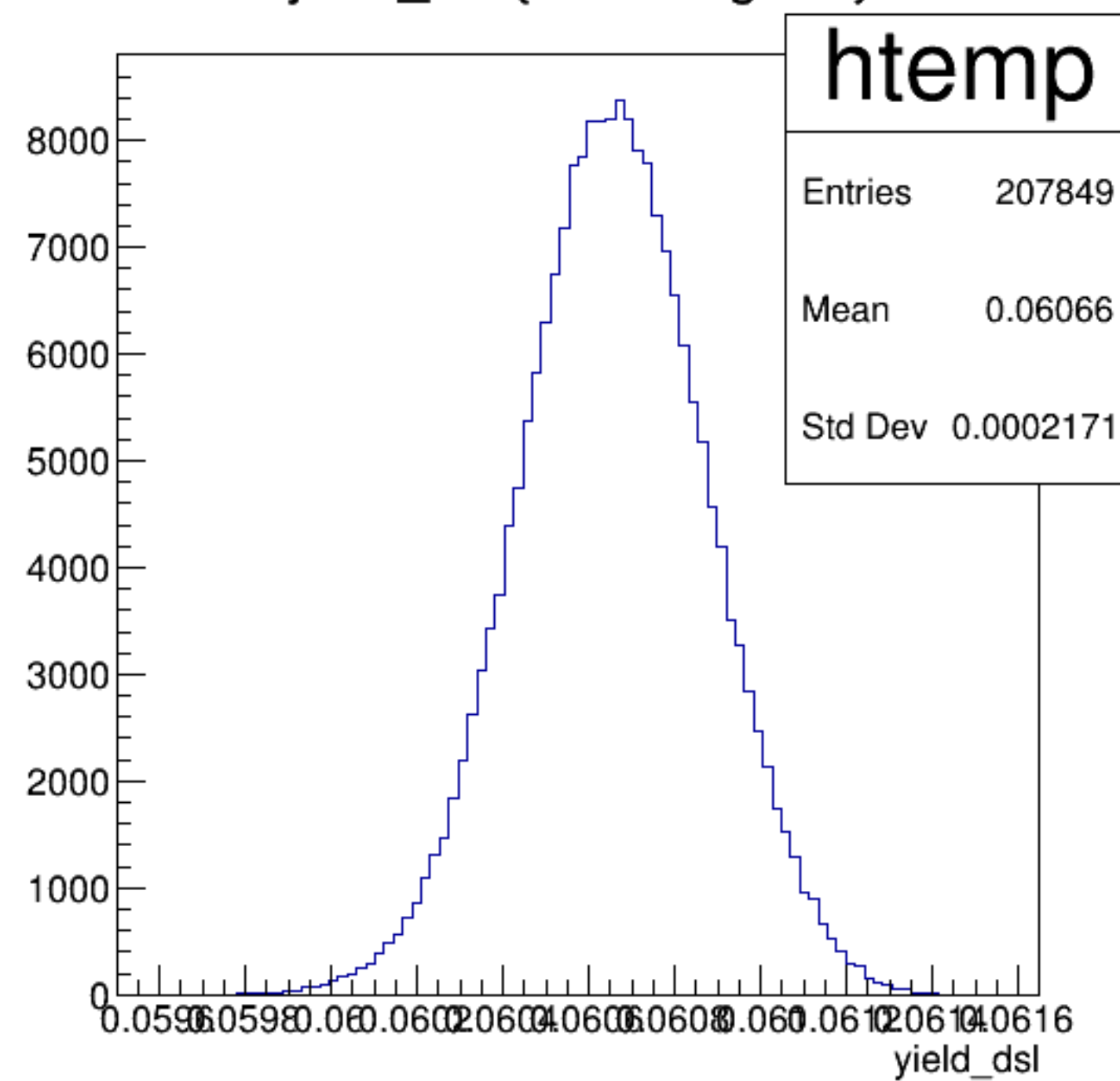
asym_usl/ppm {ErrorFlag==0}



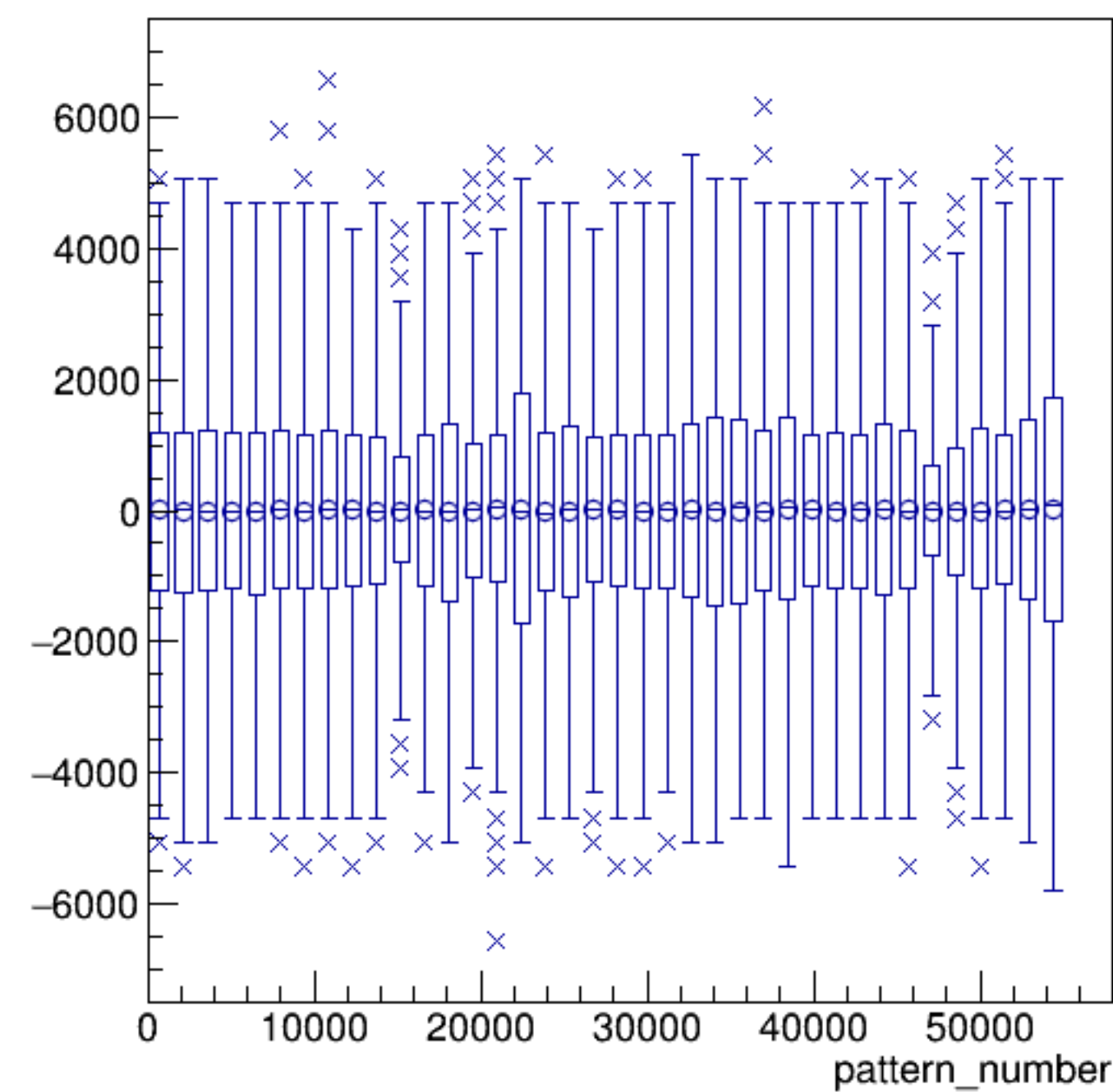
yield_dsl:pattern_number {ErrorFlag==0}



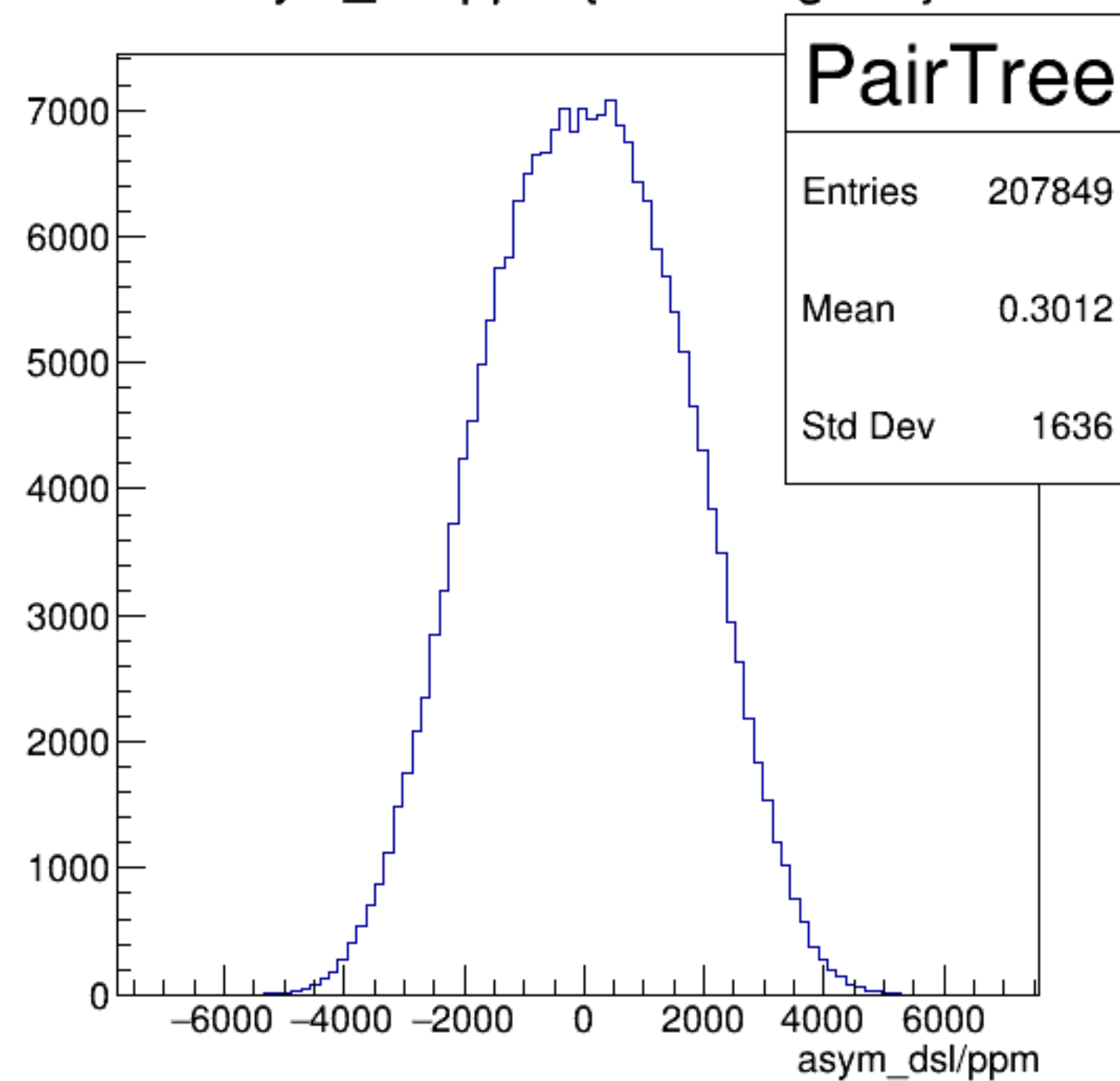
yield_dsl {ErrorFlag==0}



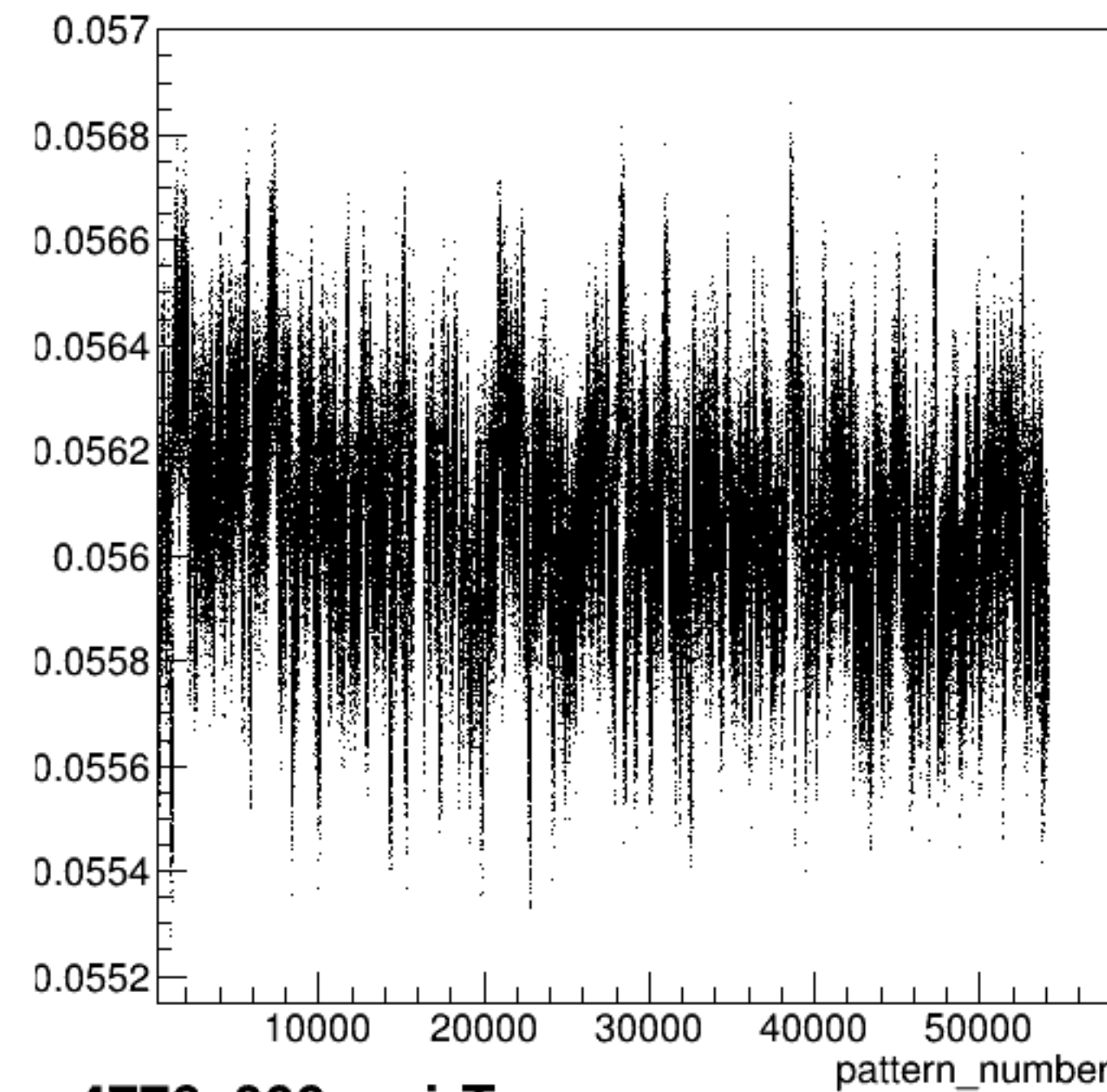
asym_dsl/ppm:pattern_number {ErrorFlag==0}



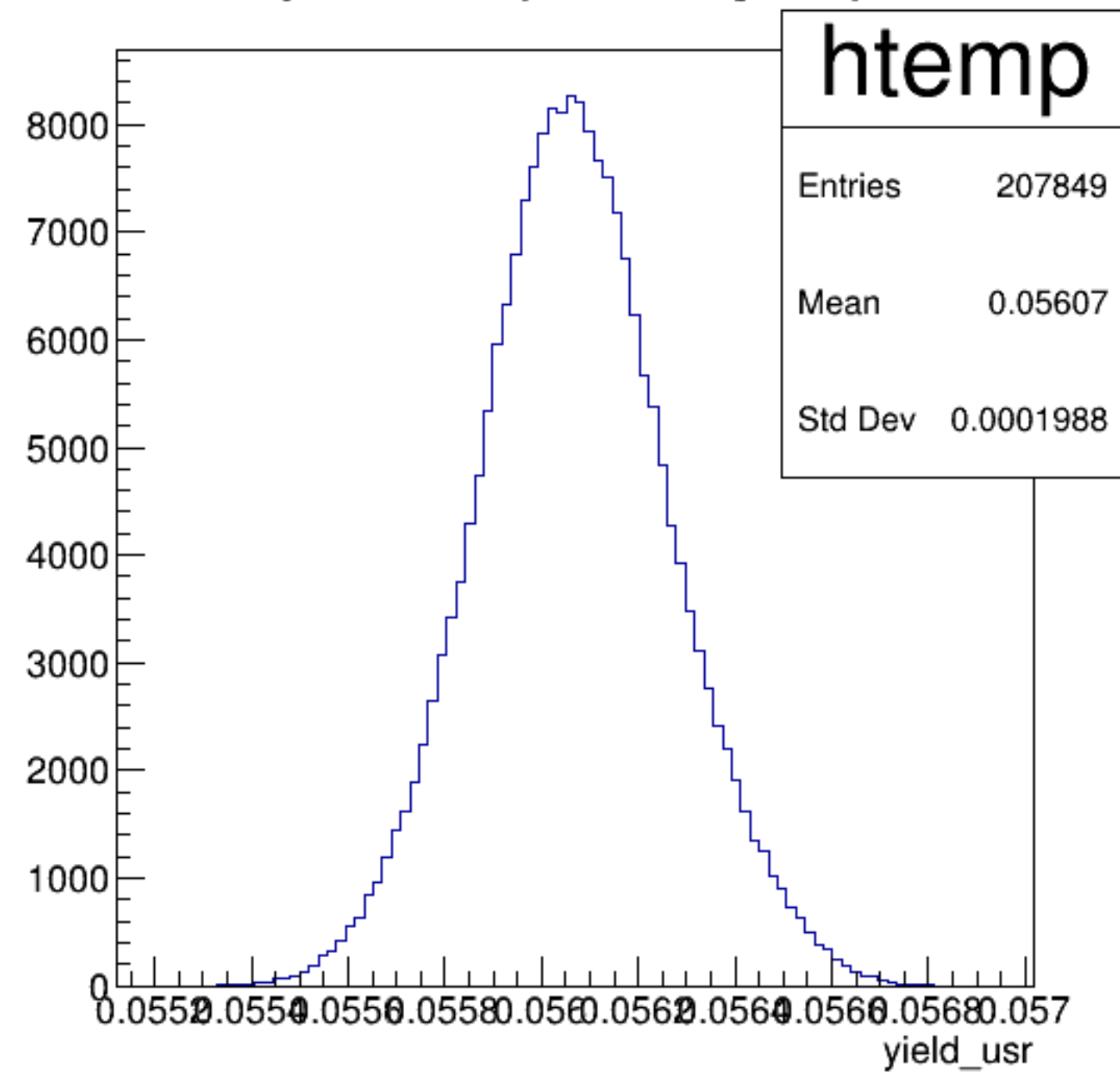
asym_dsl/ppm {ErrorFlag==0}



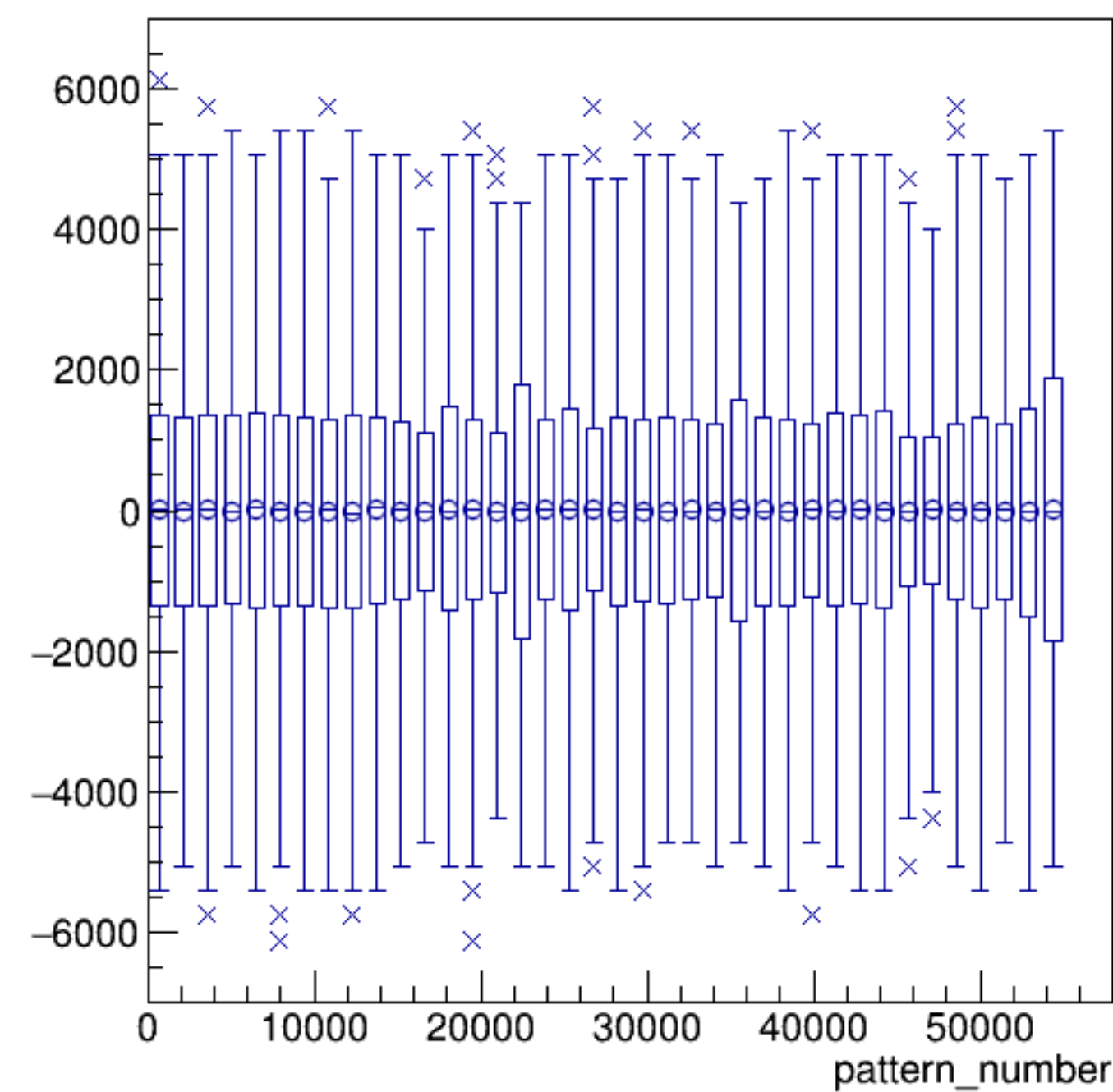
yield_usr:pattern_number {ErrorFlag==0}



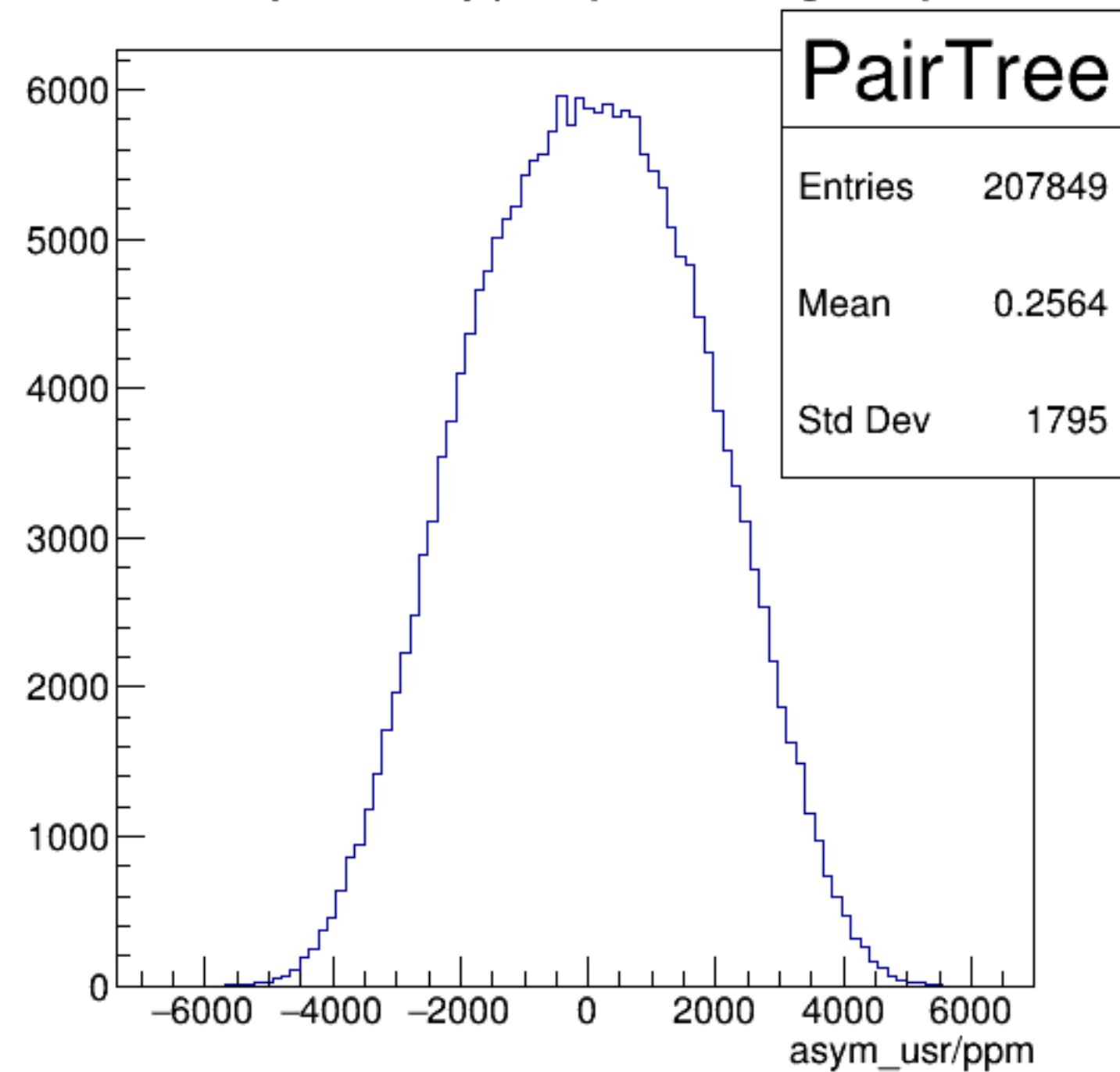
yield_usr {ErrorFlag==0}



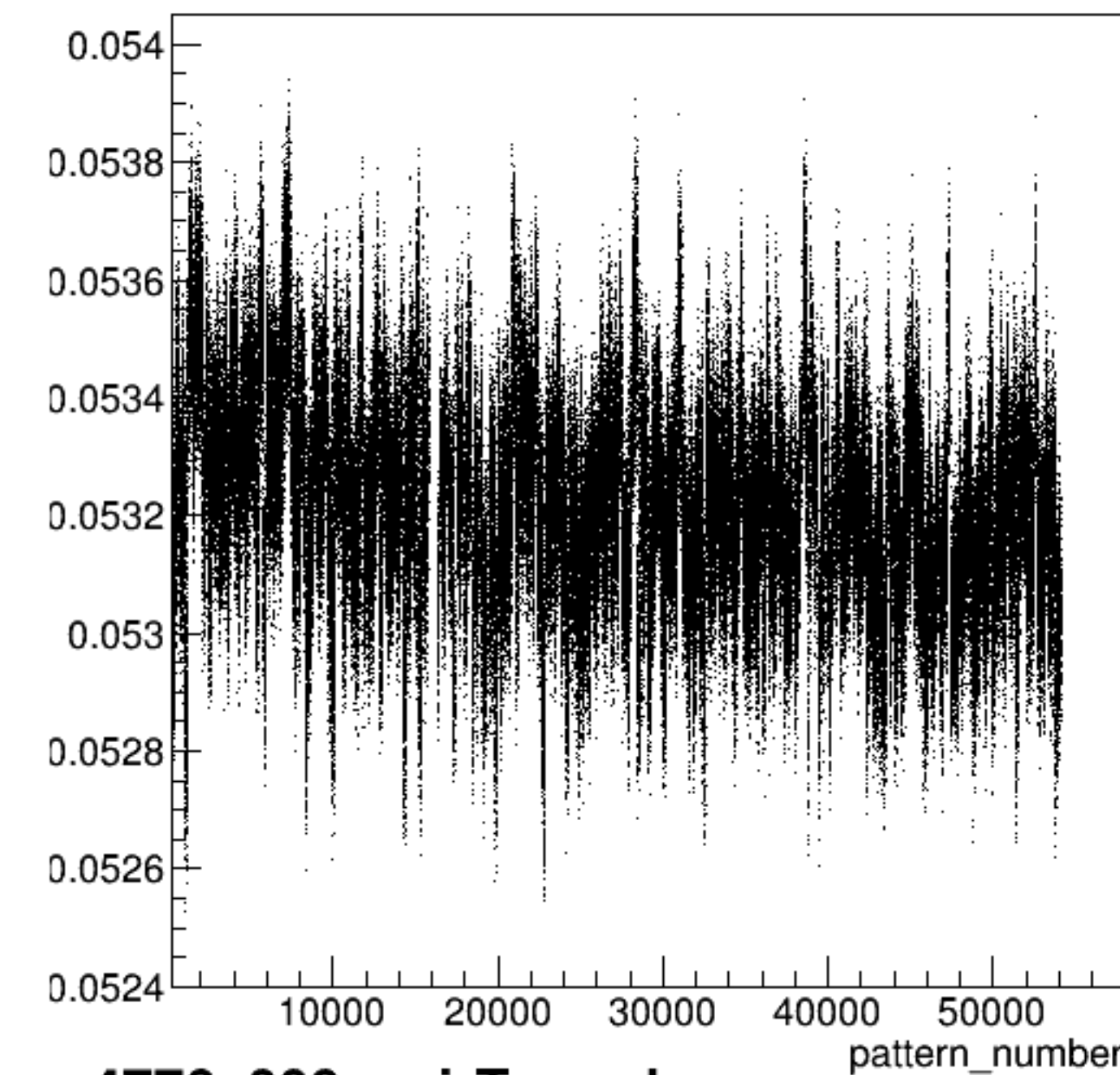
asym_usr/ppm:pattern_number {ErrorFlag==0}



asym_usr/ppm {ErrorFlag==0}

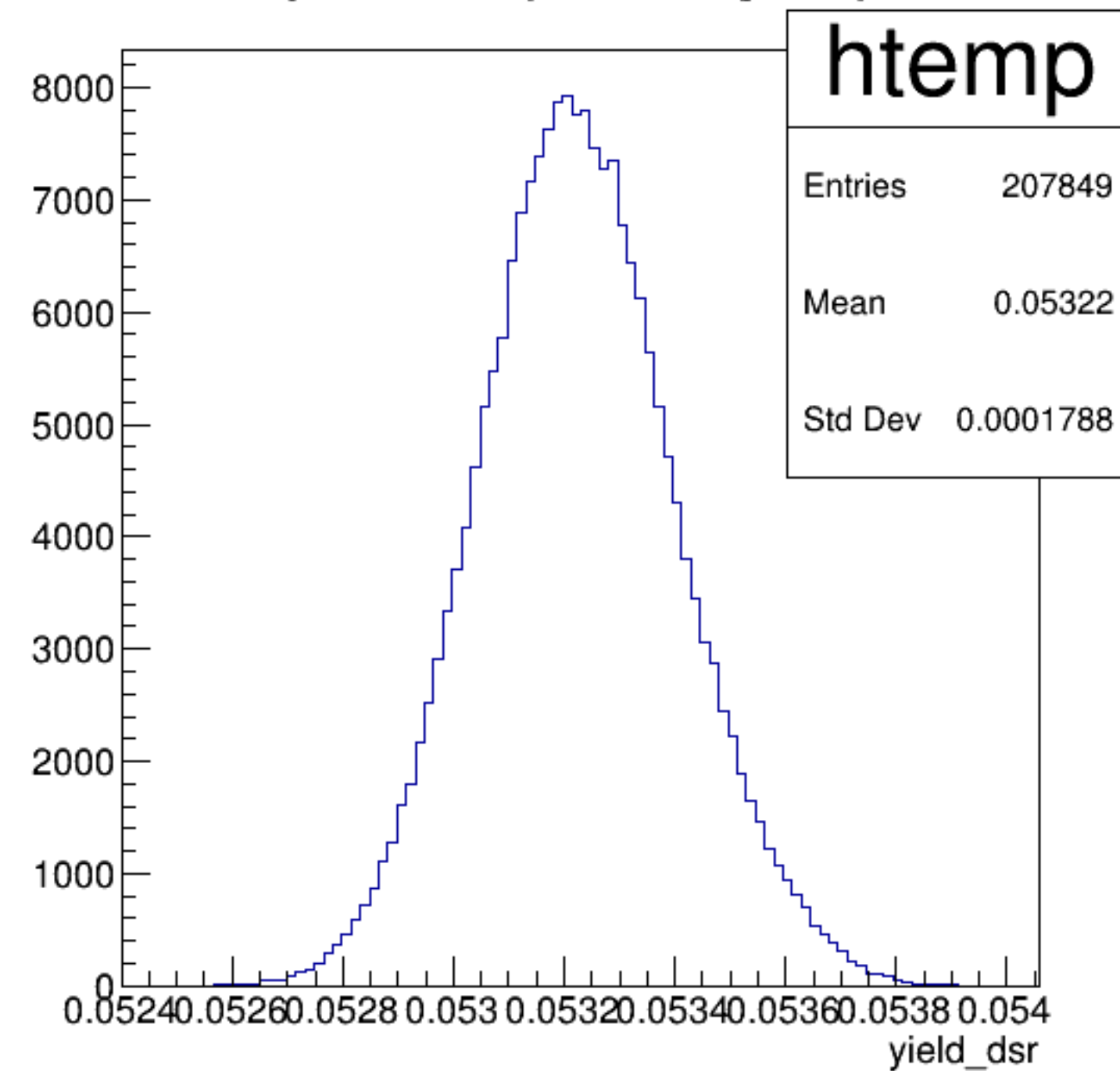


yield_dsr:pattern_number {ErrorFlag==0}

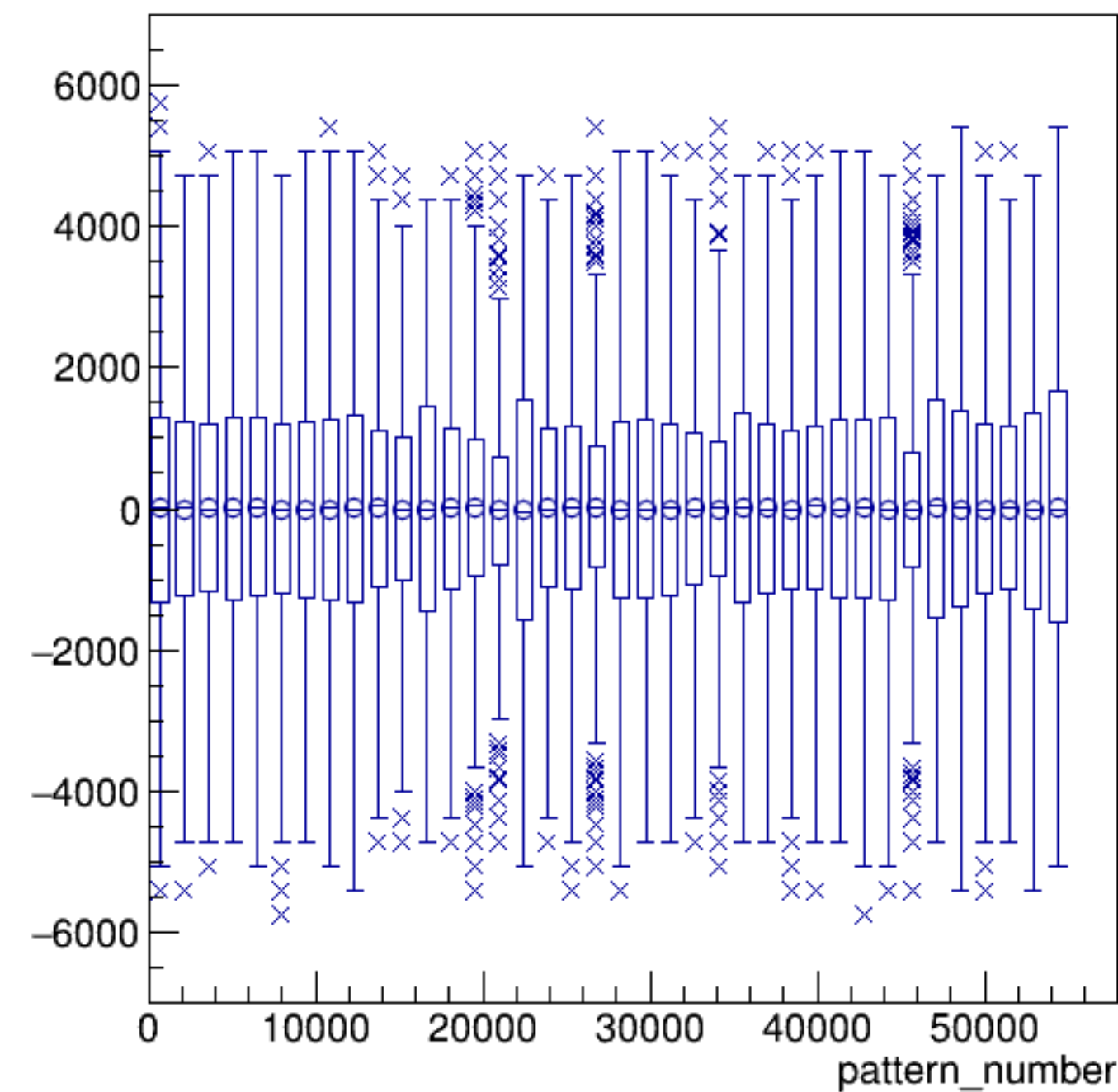


run4776_000_pairTree_dsr.png

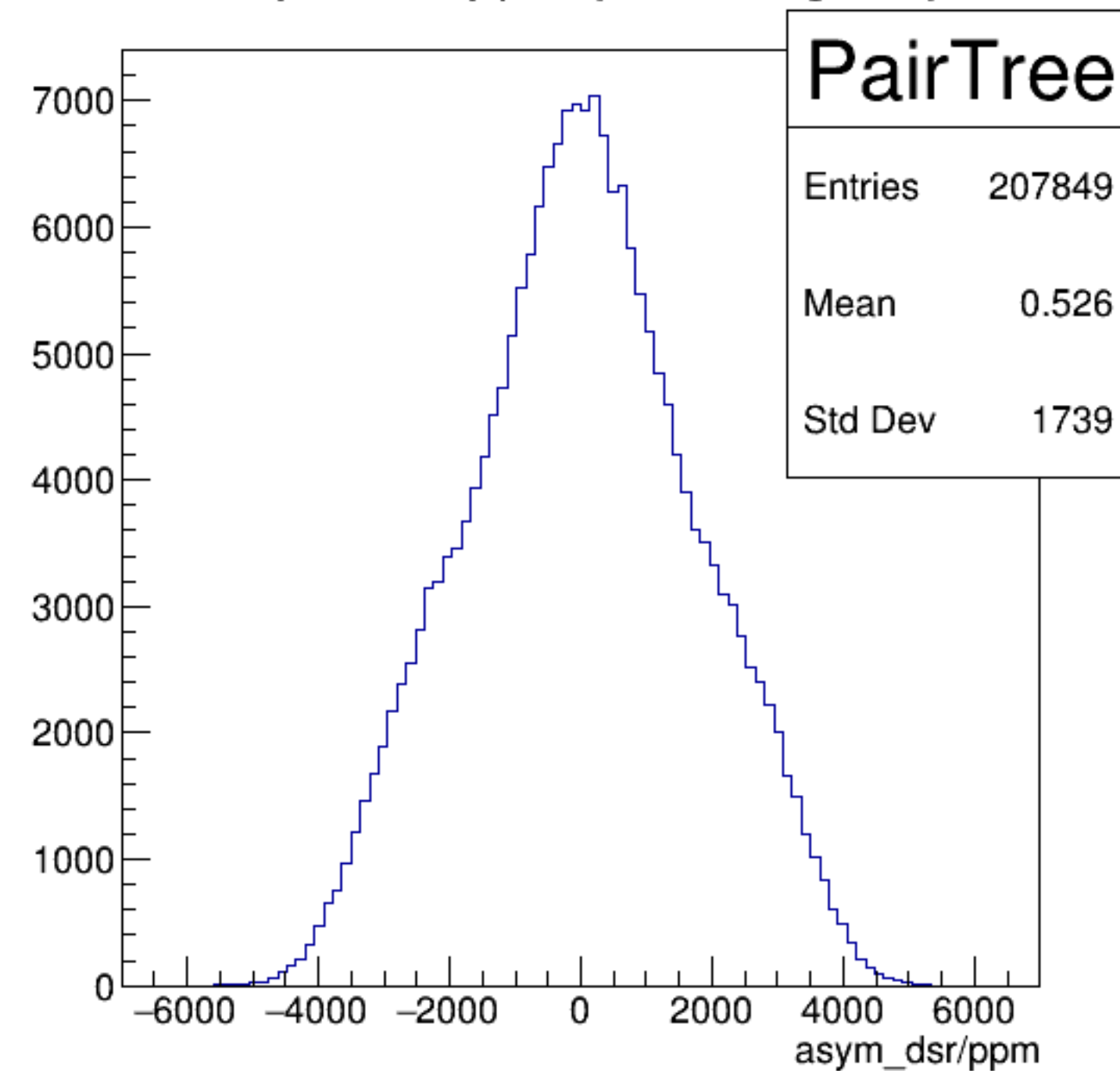
yield_dsr {ErrorFlag==0}



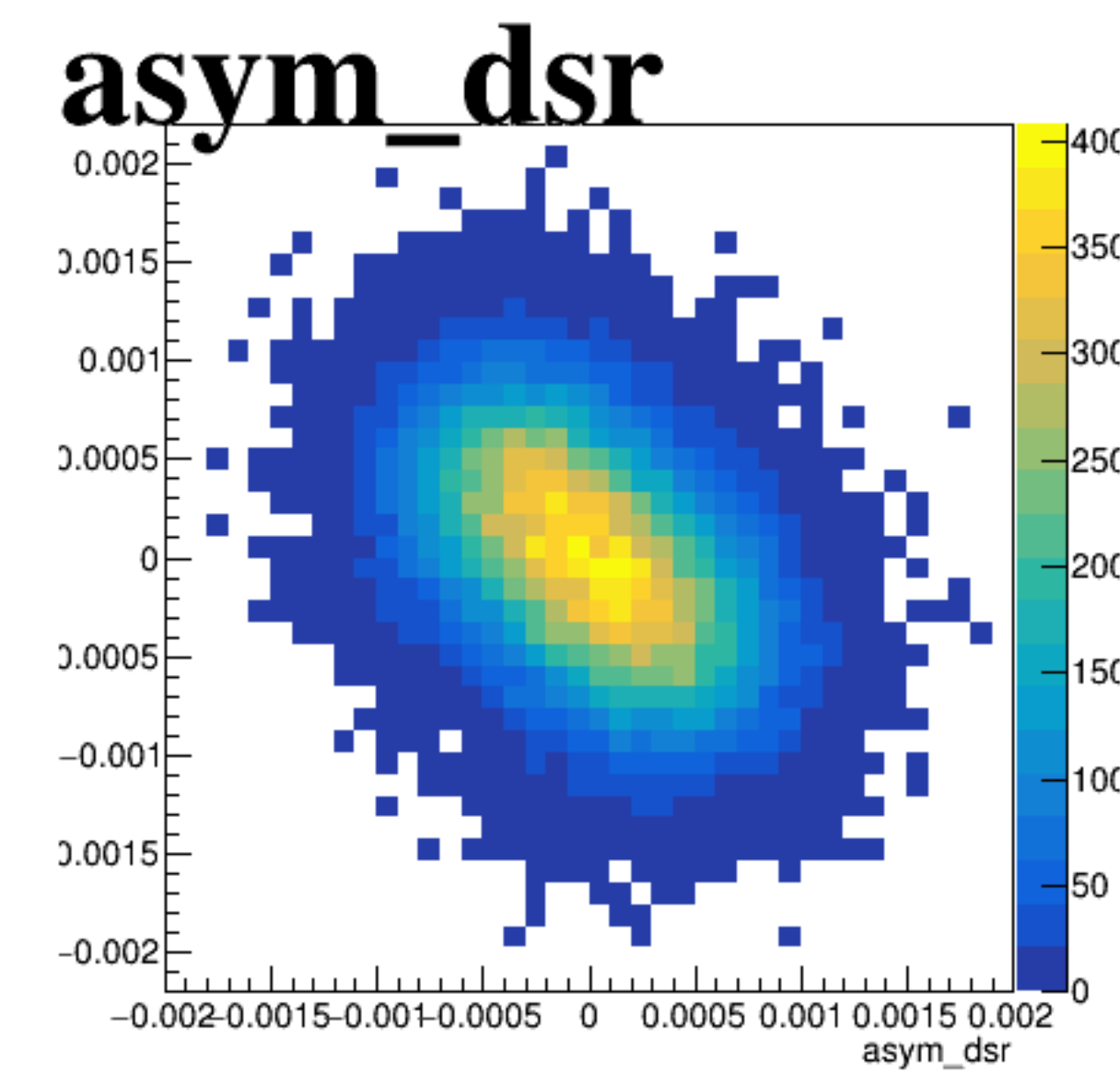
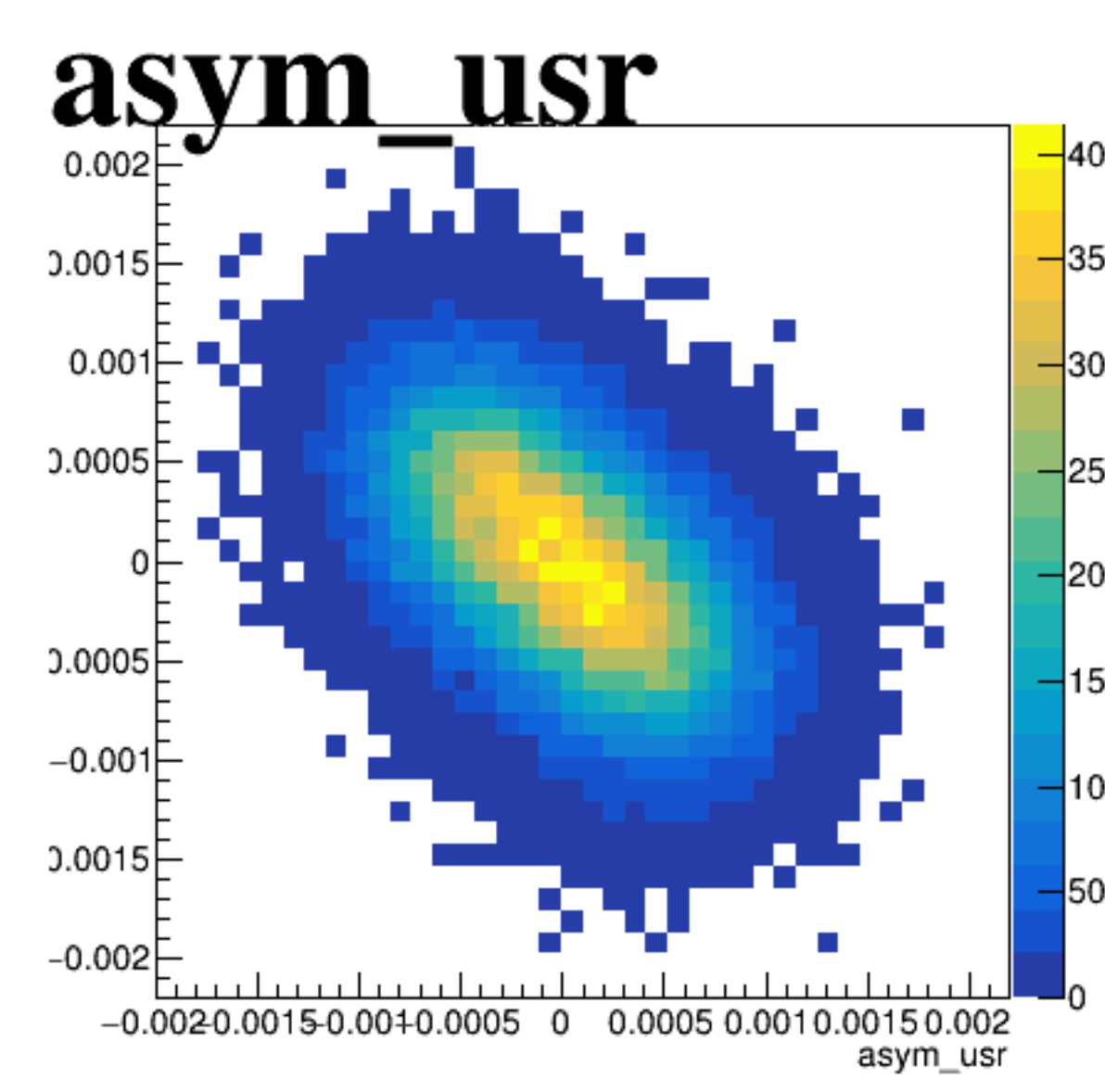
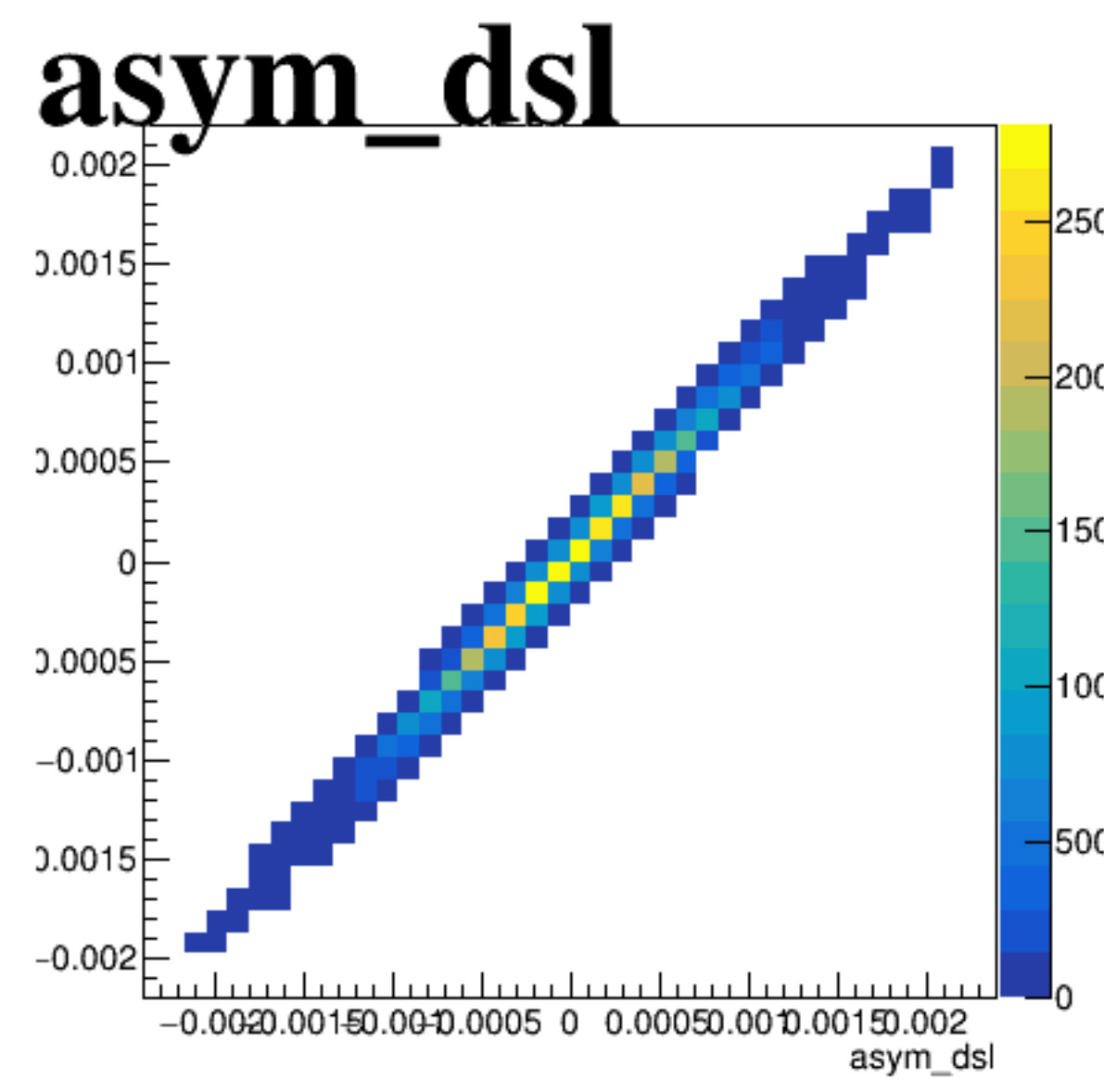
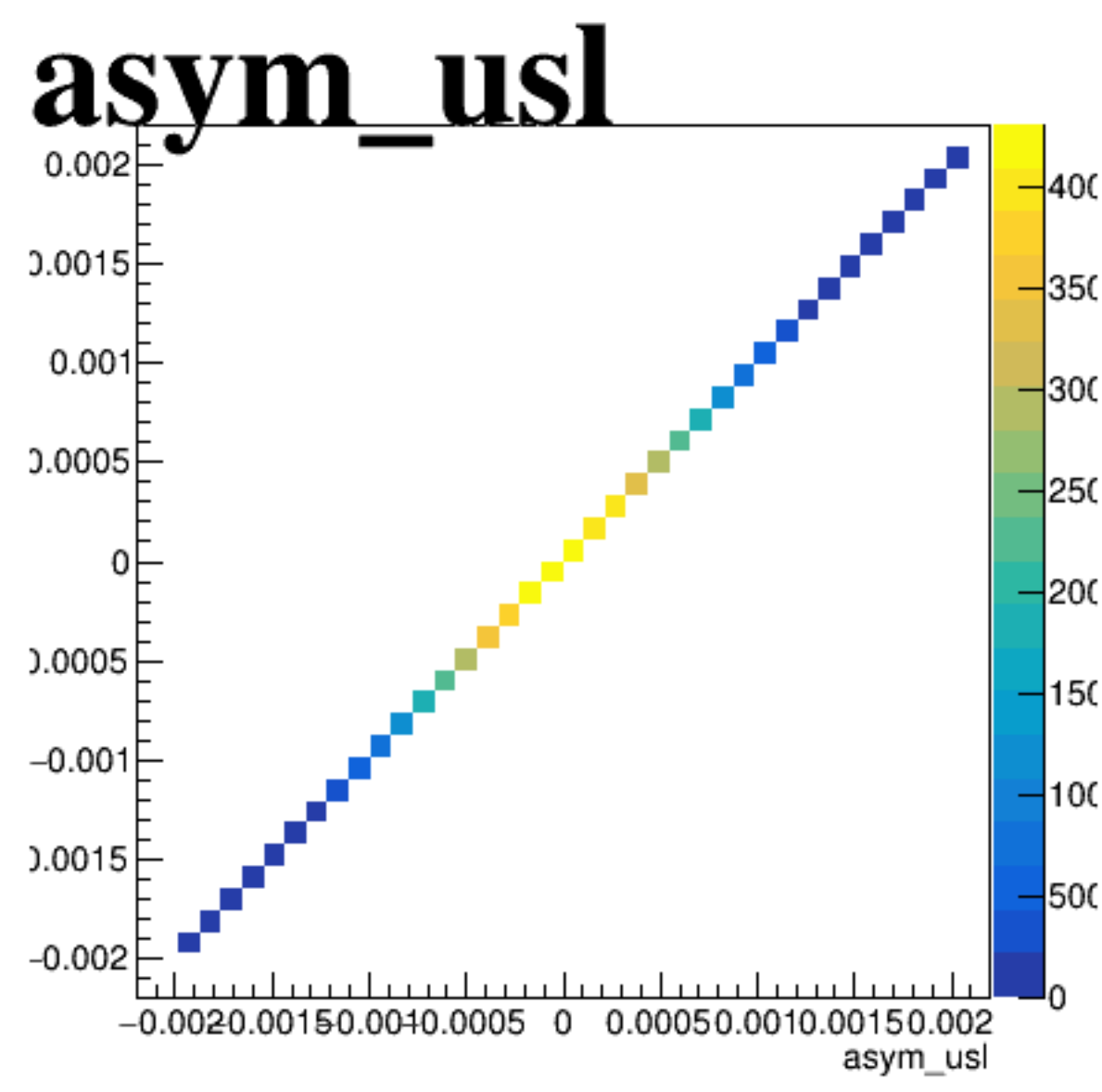
asym_dsr/ppm:pattern_number {ErrorFlag==0}



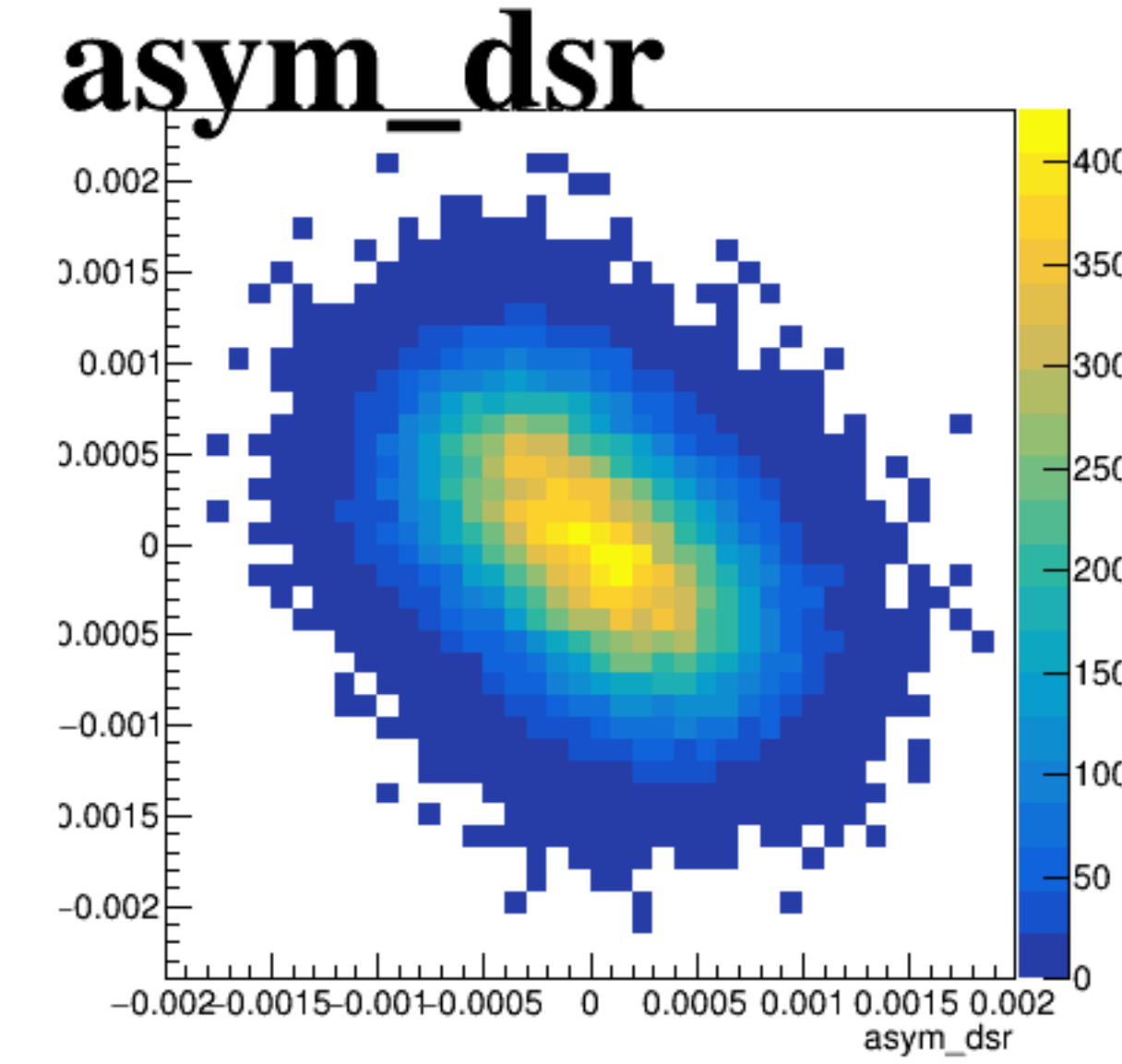
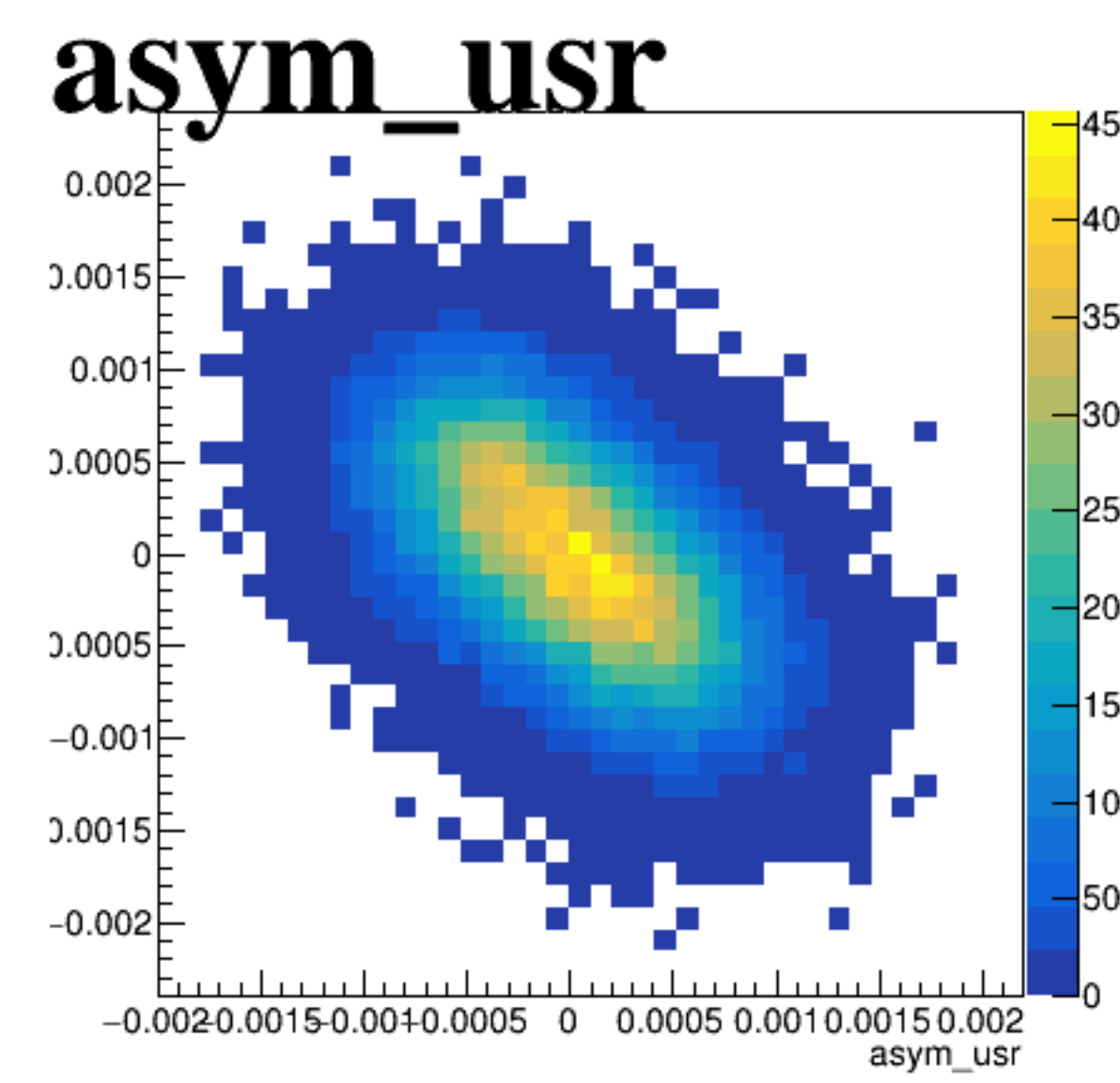
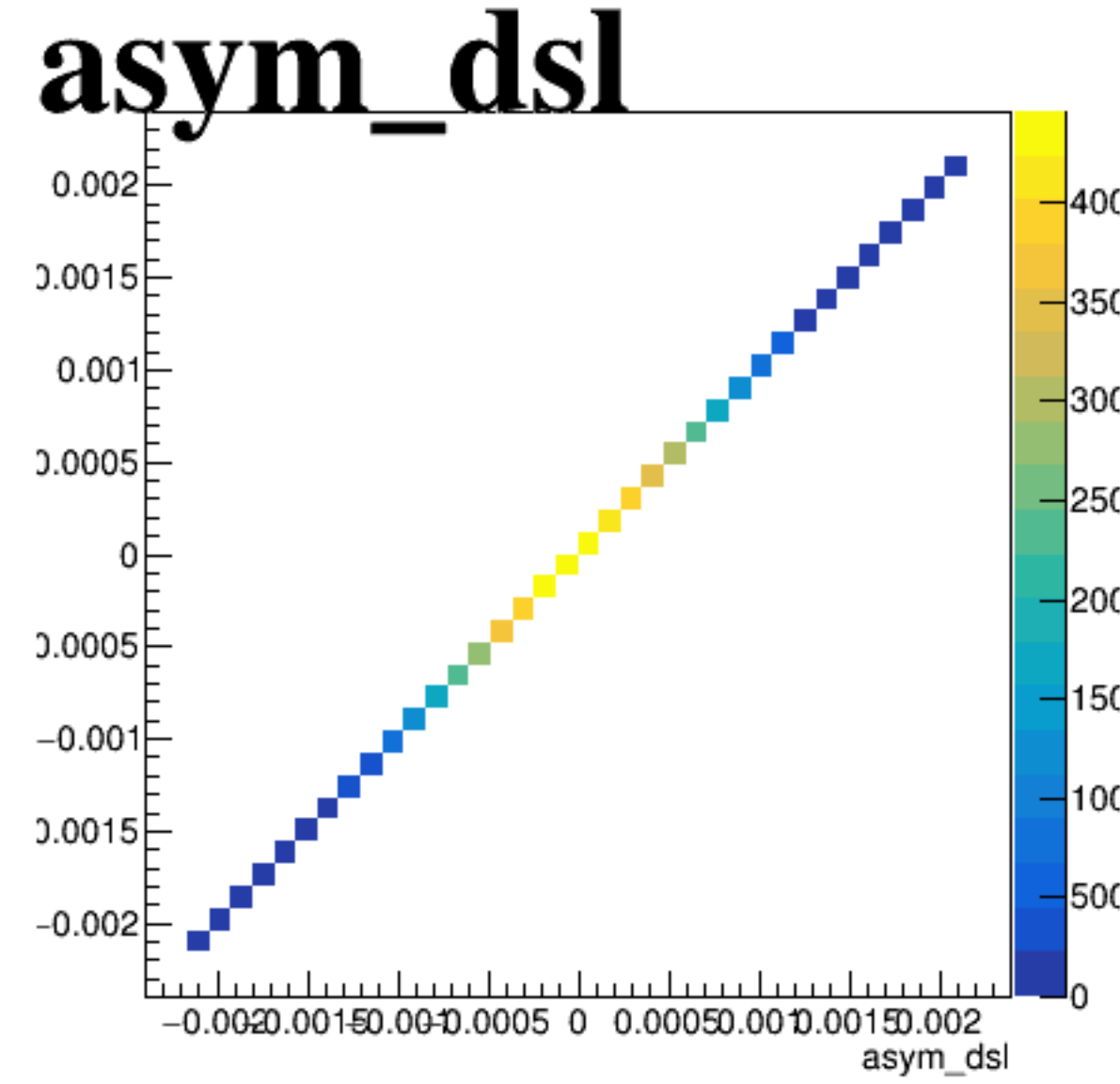
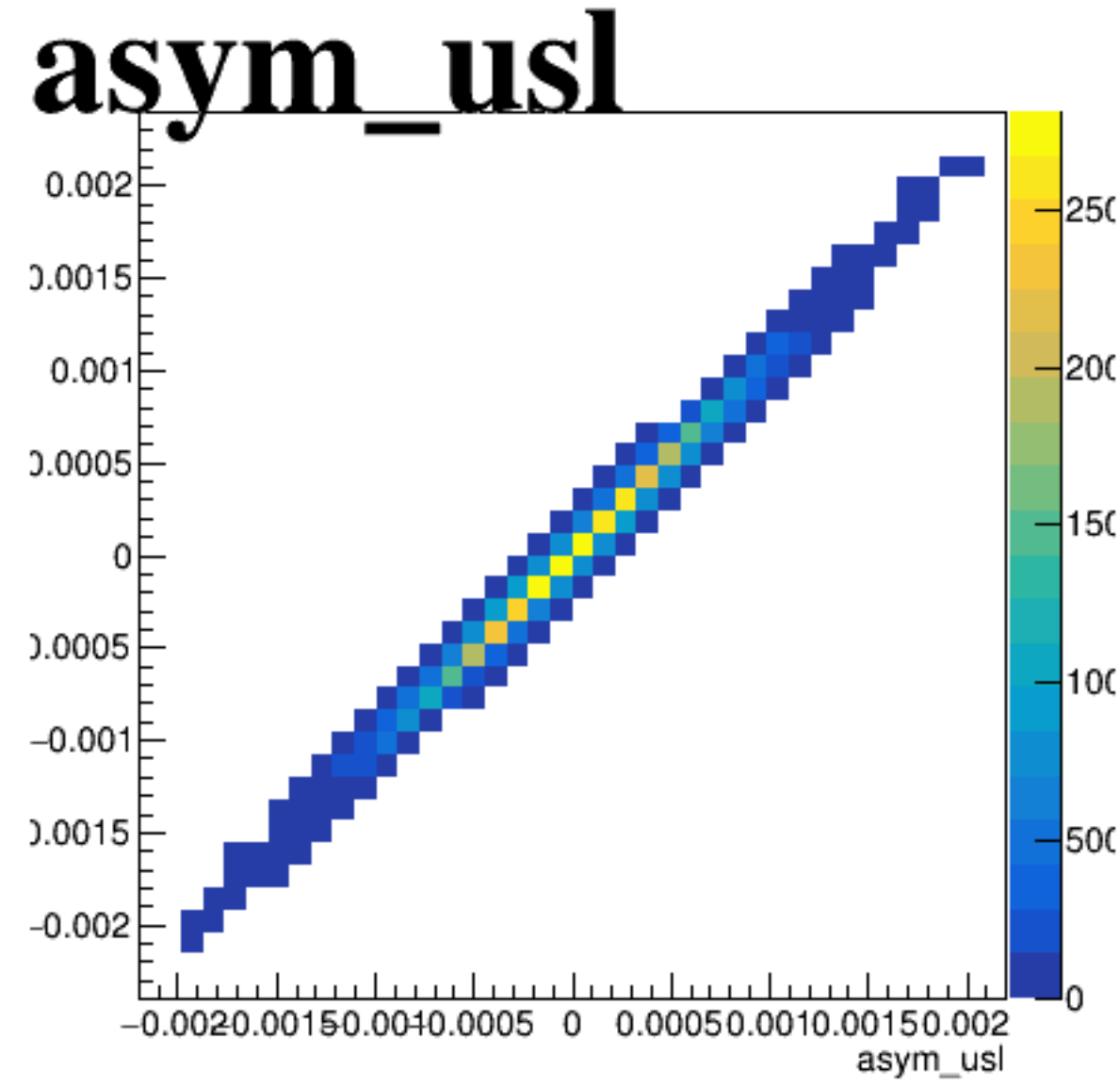
asym_dsr/ppm {ErrorFlag==0}



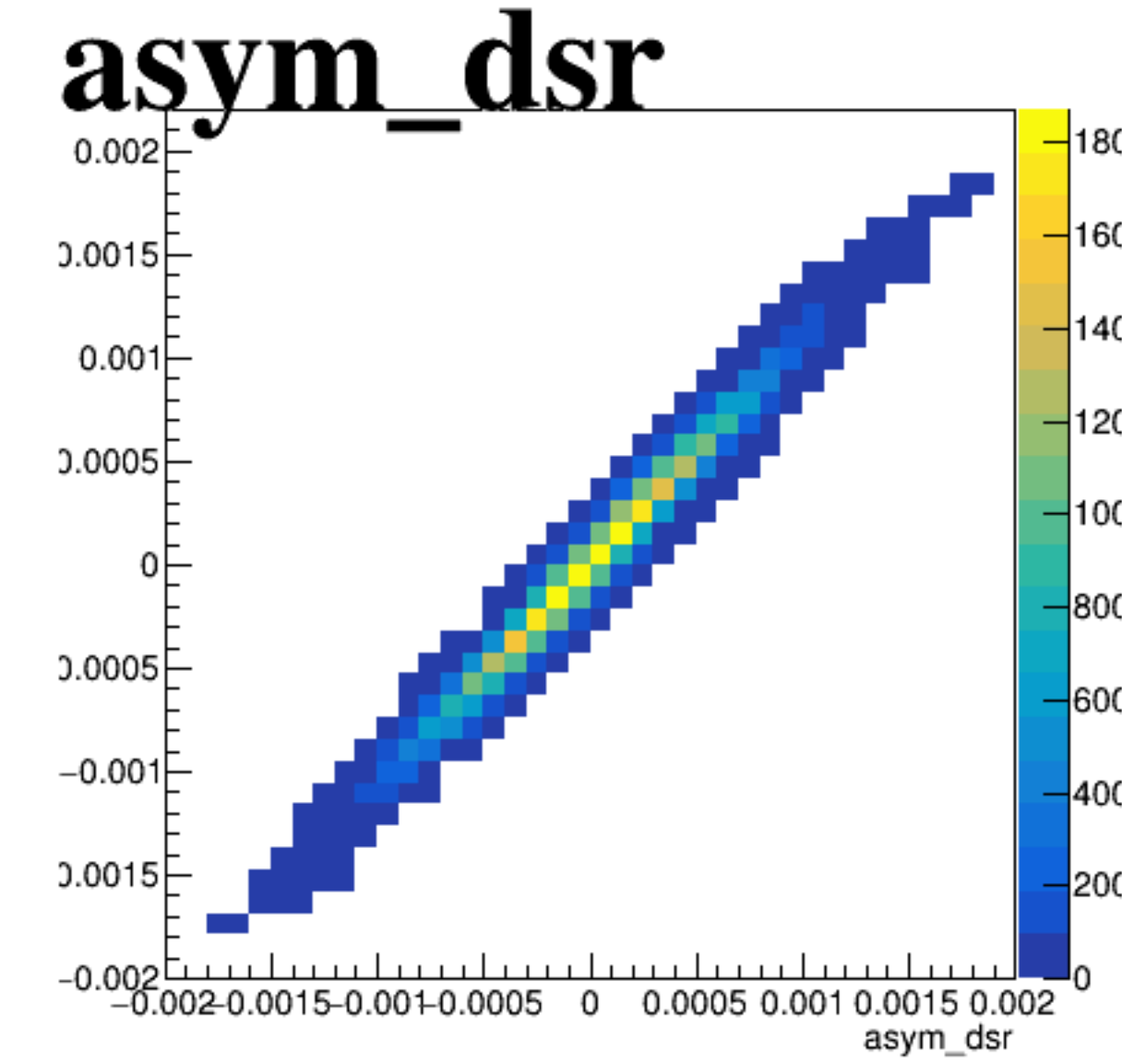
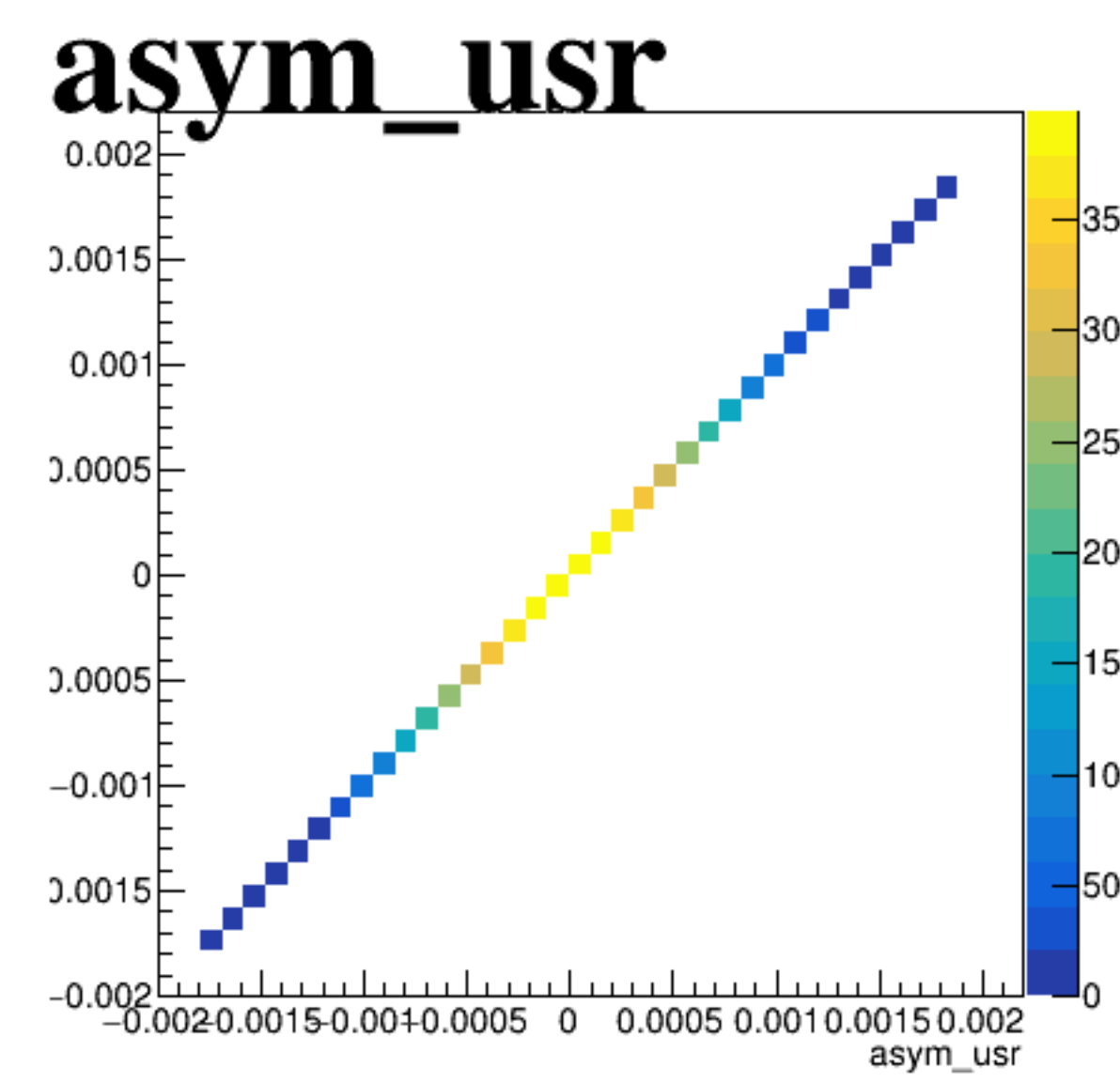
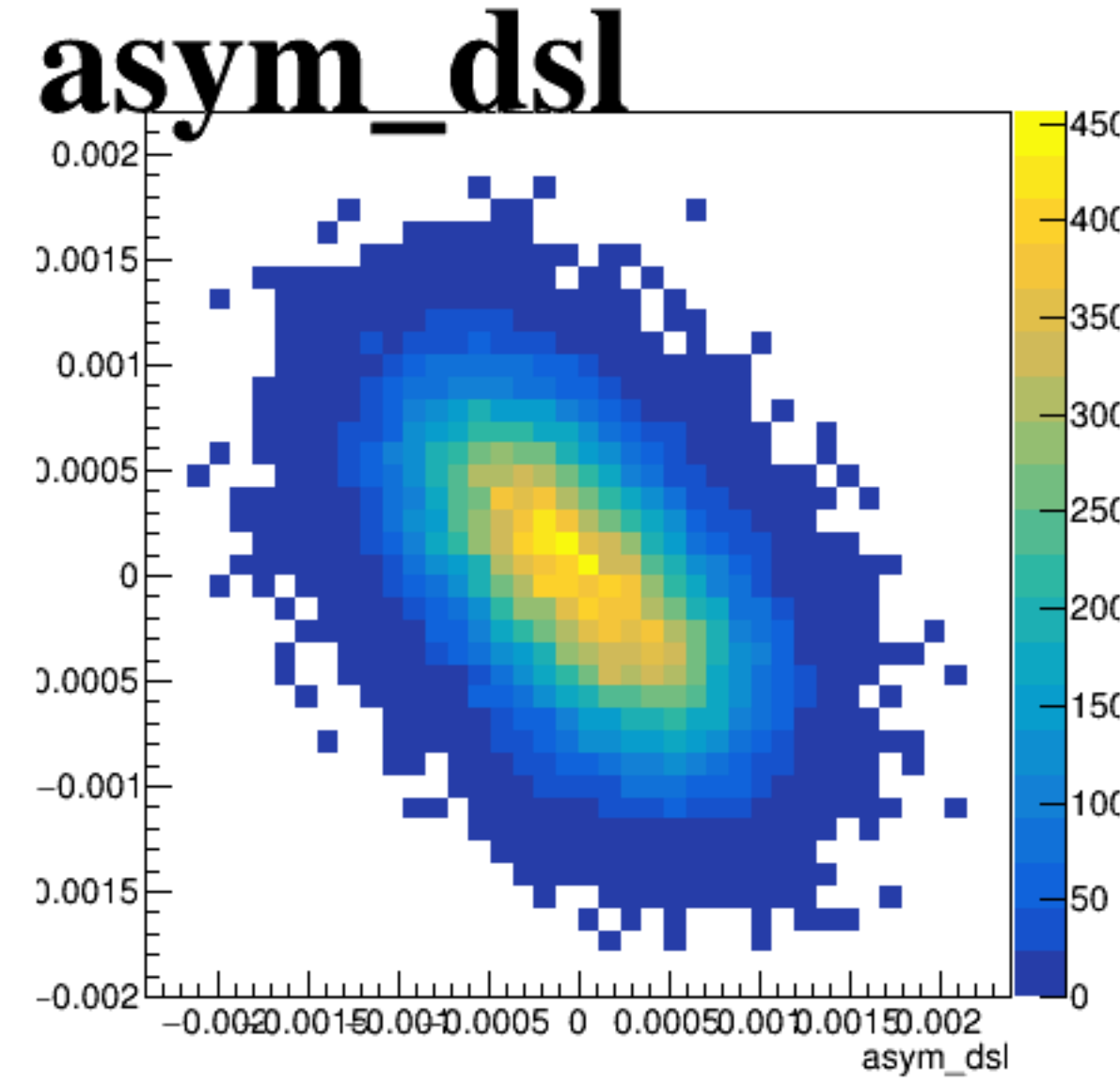
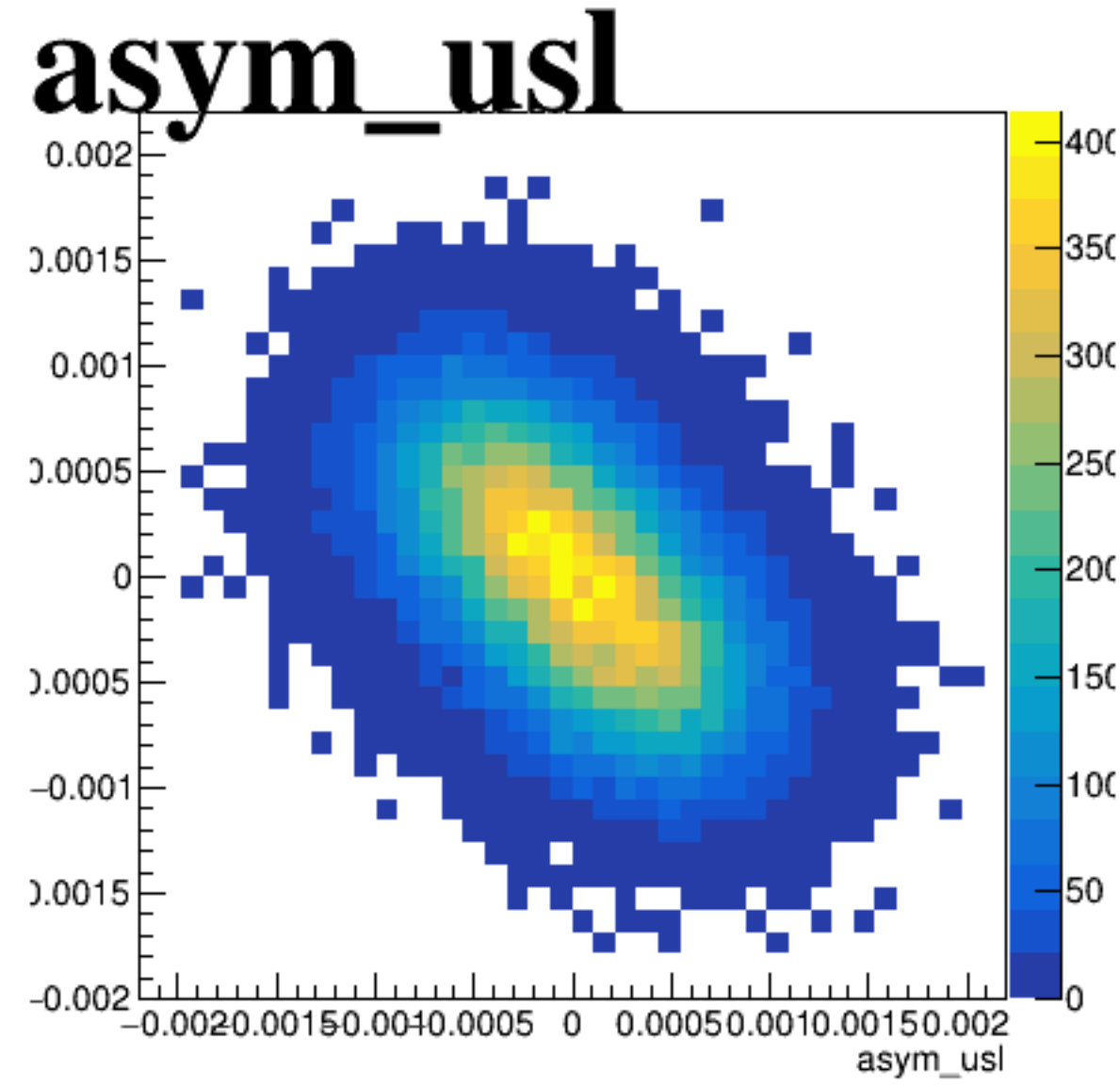
asym_usl



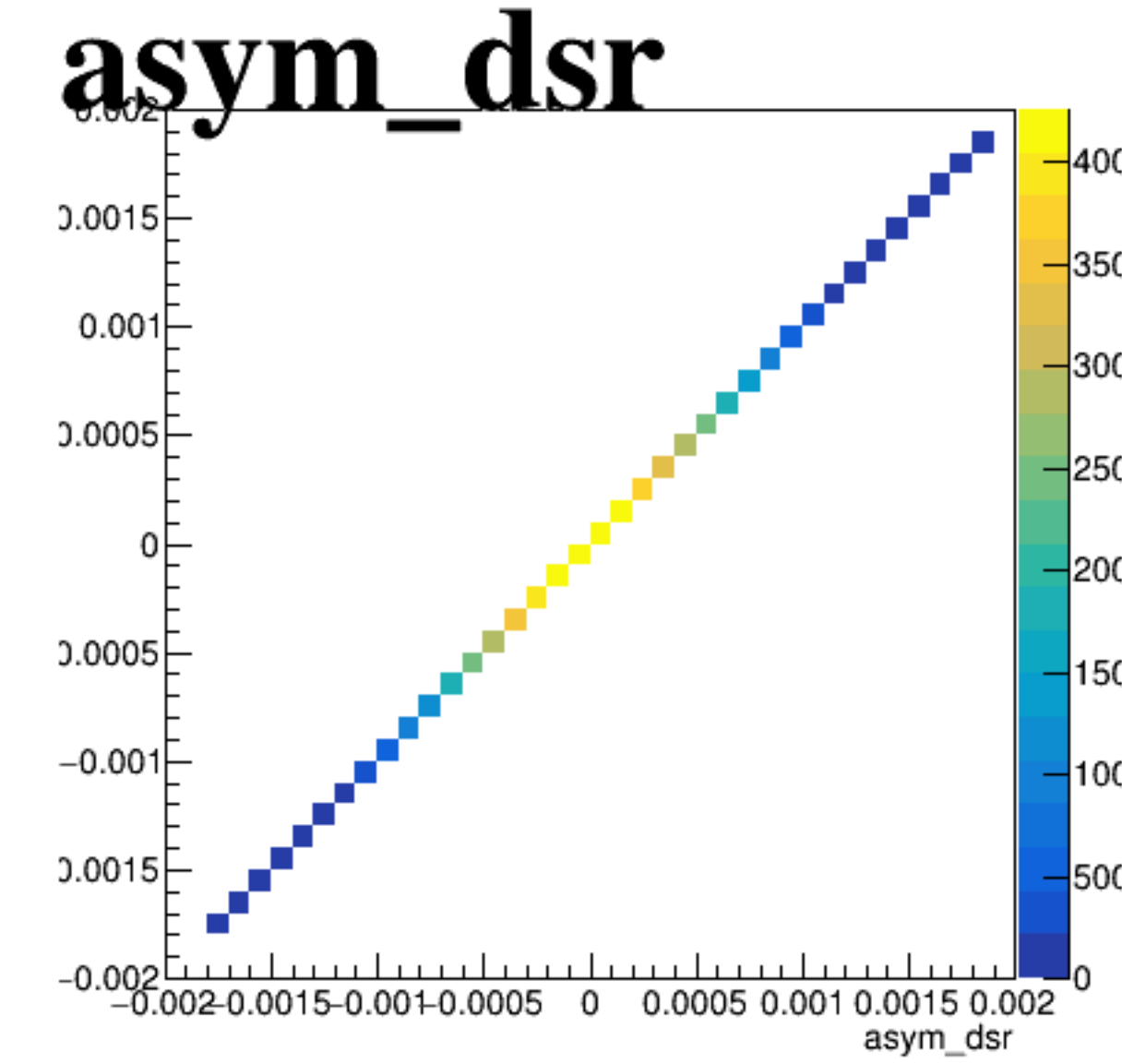
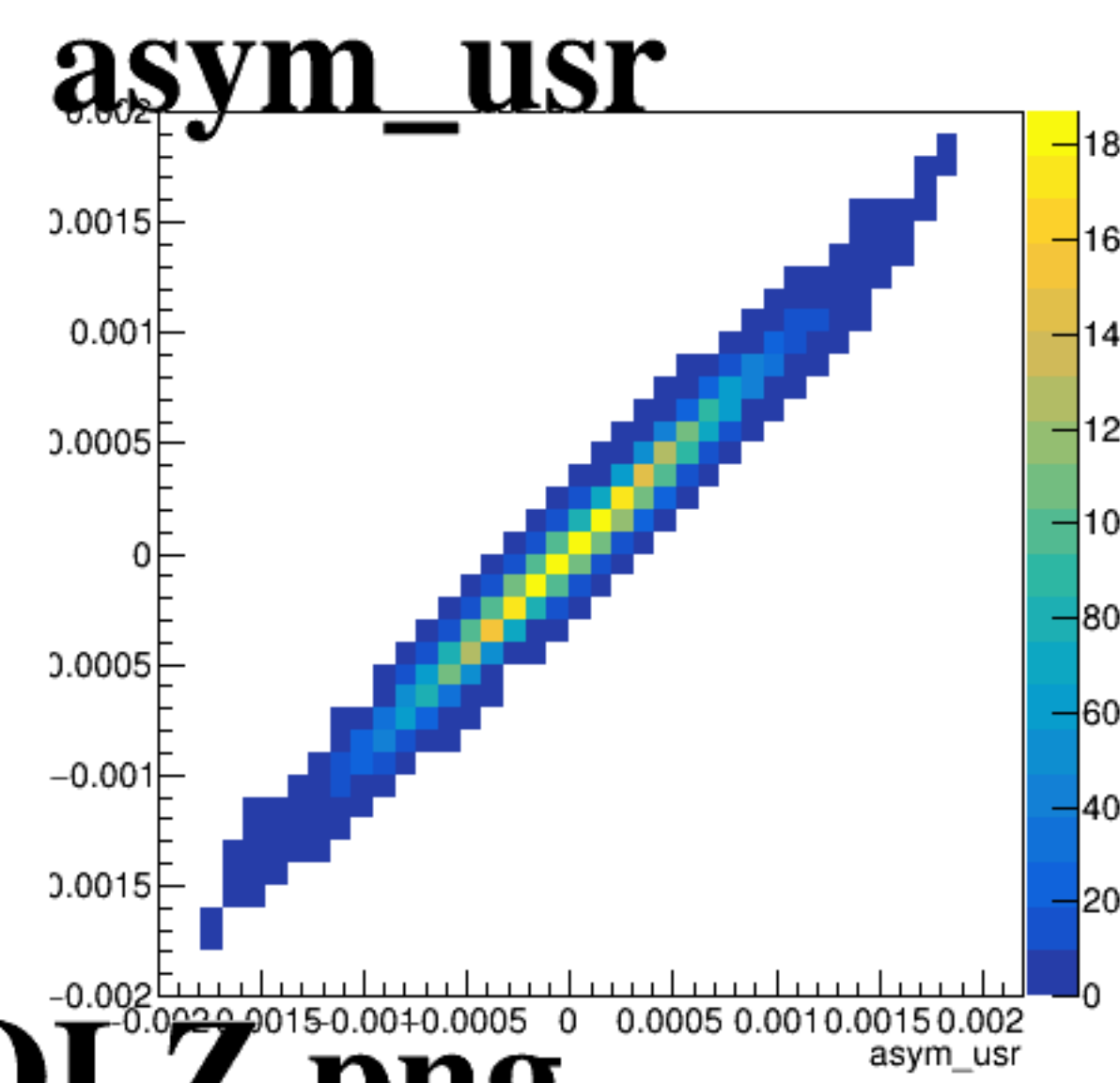
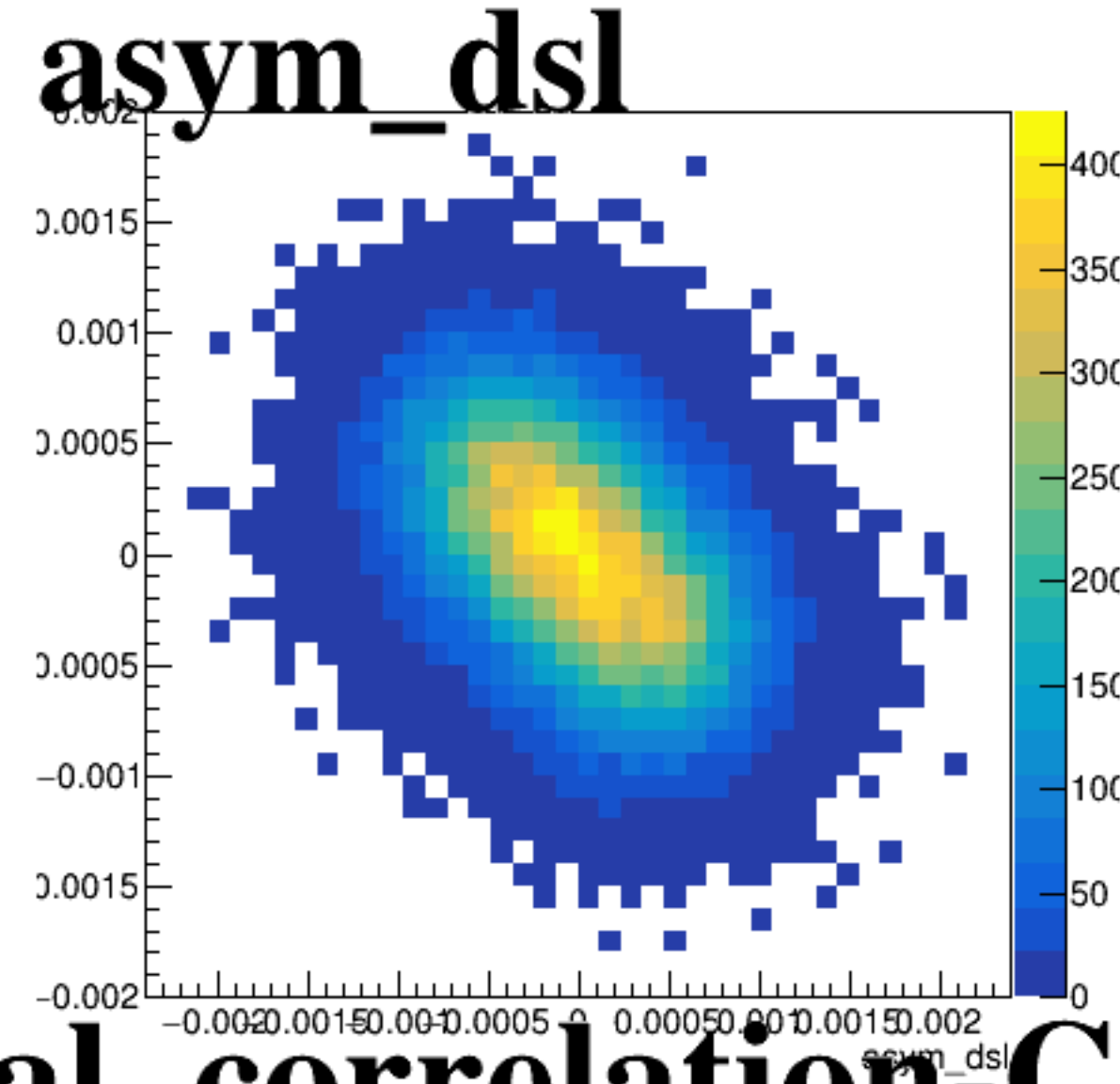
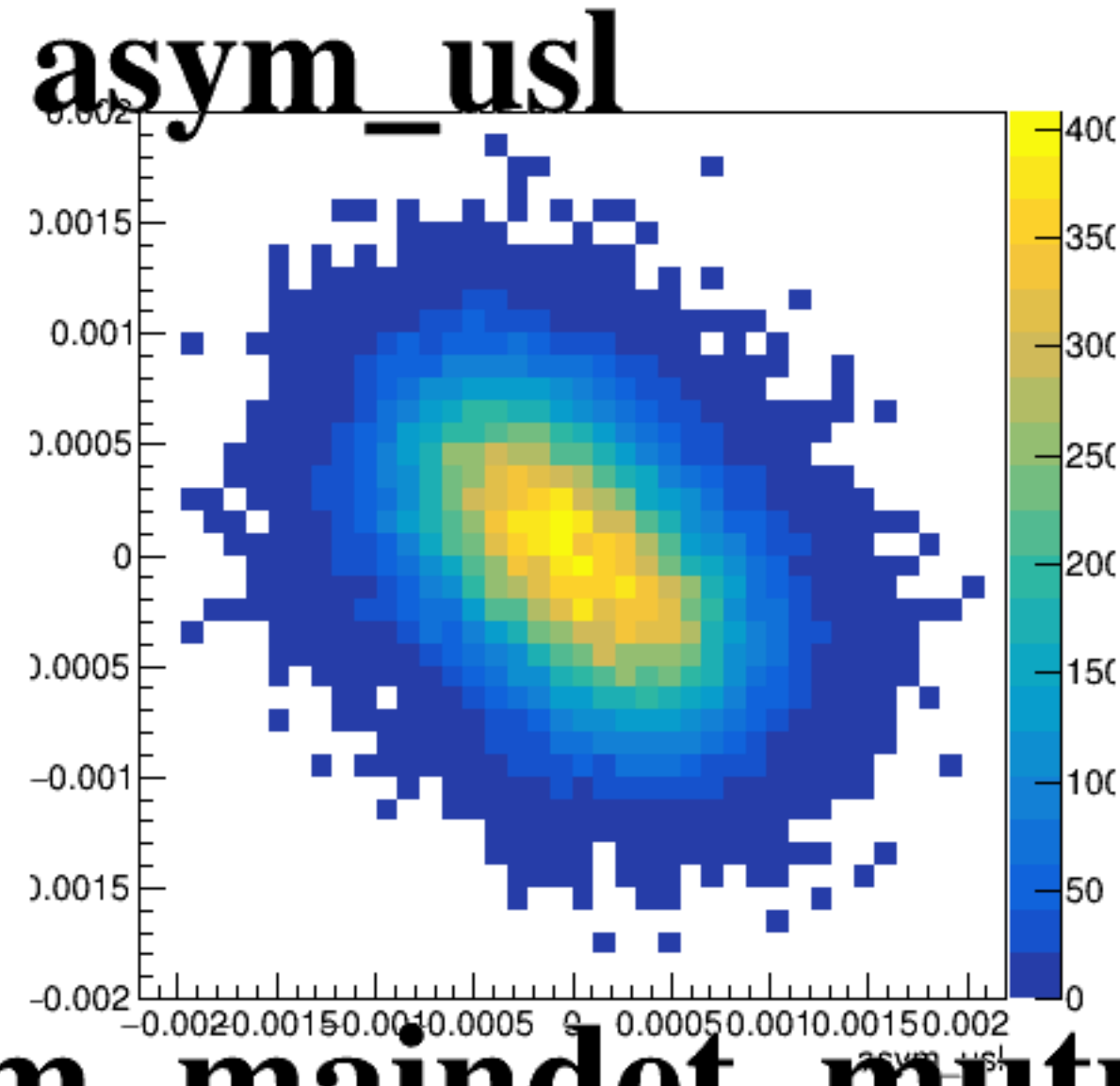
asym_dsl



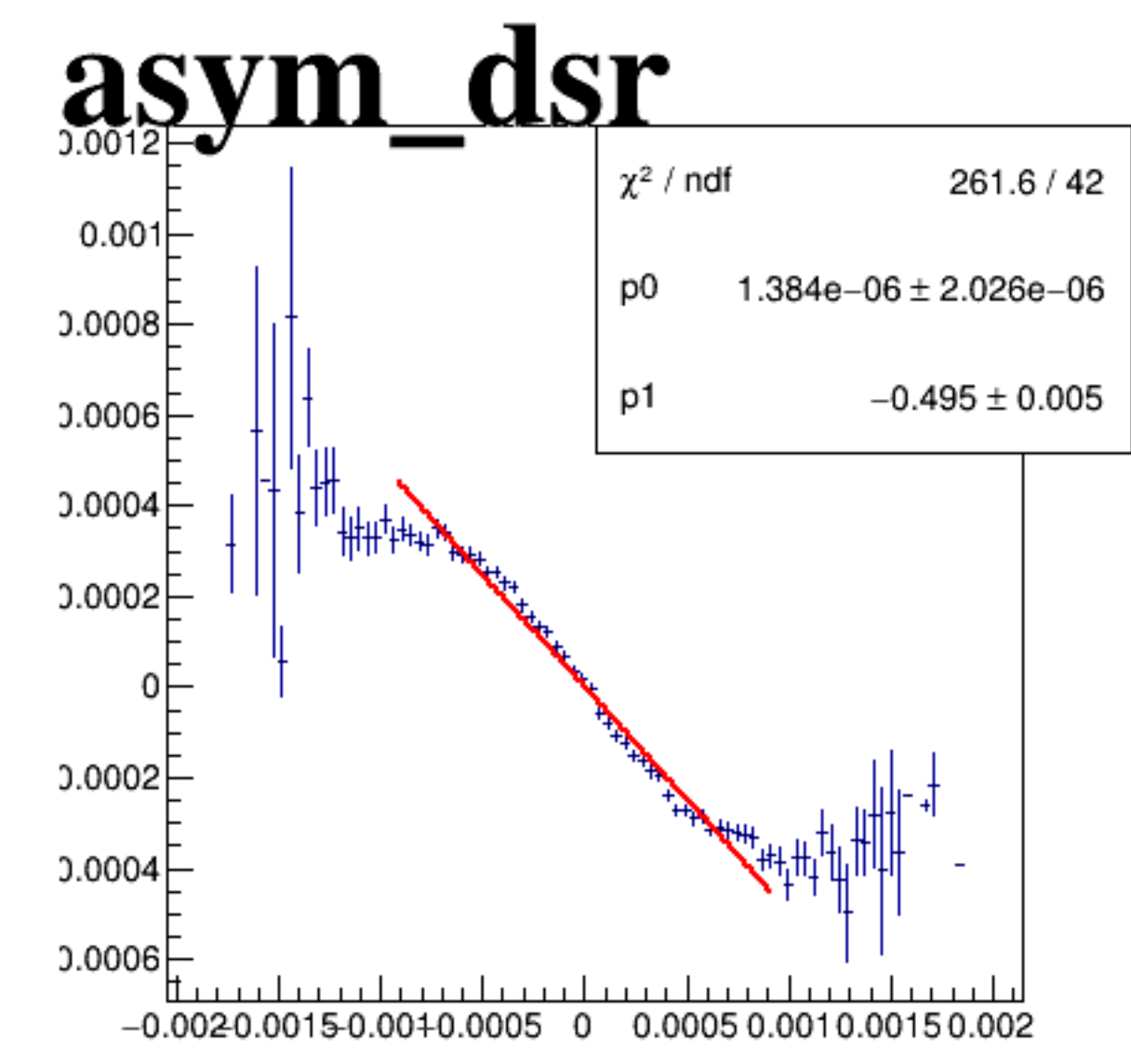
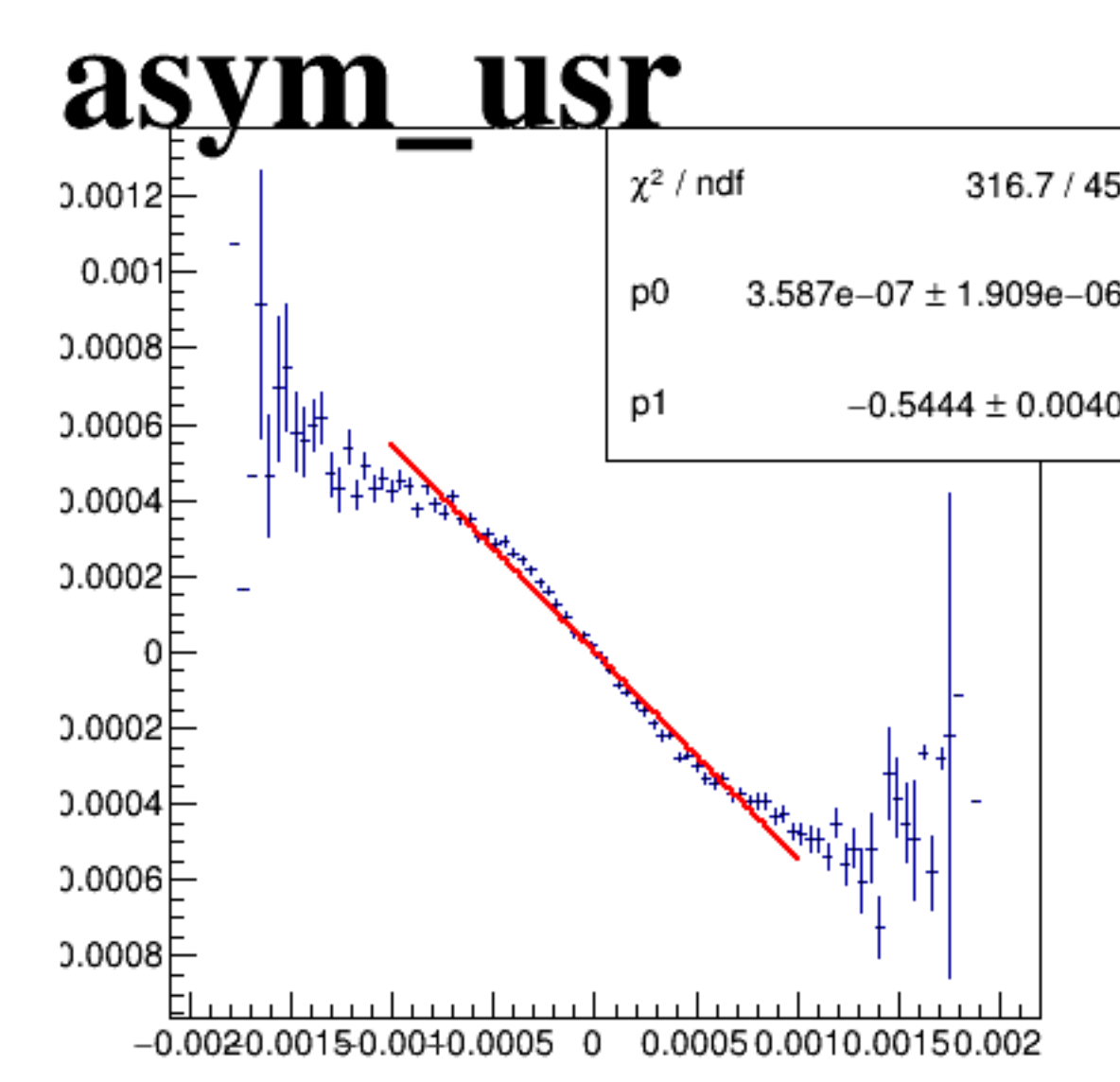
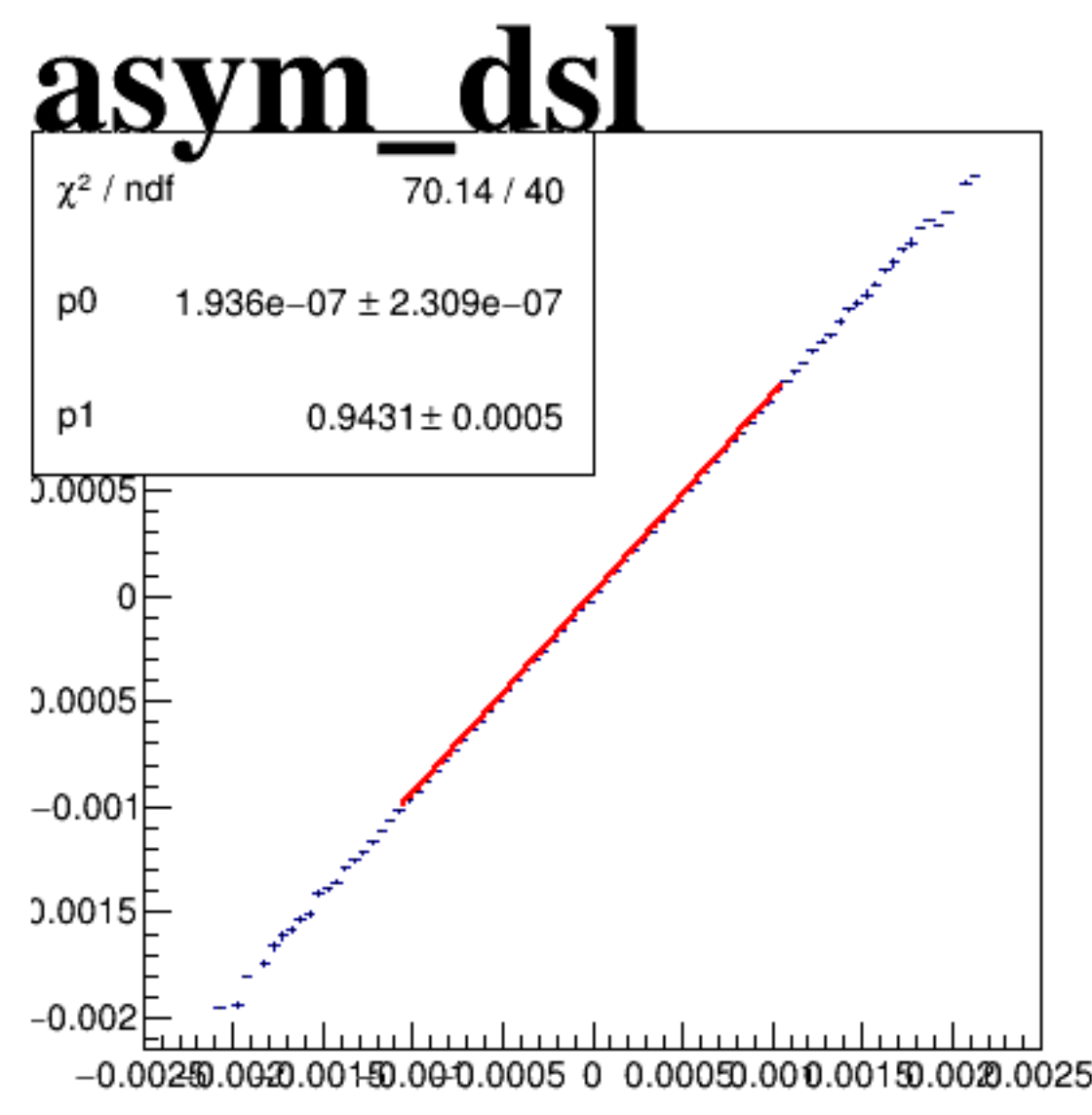
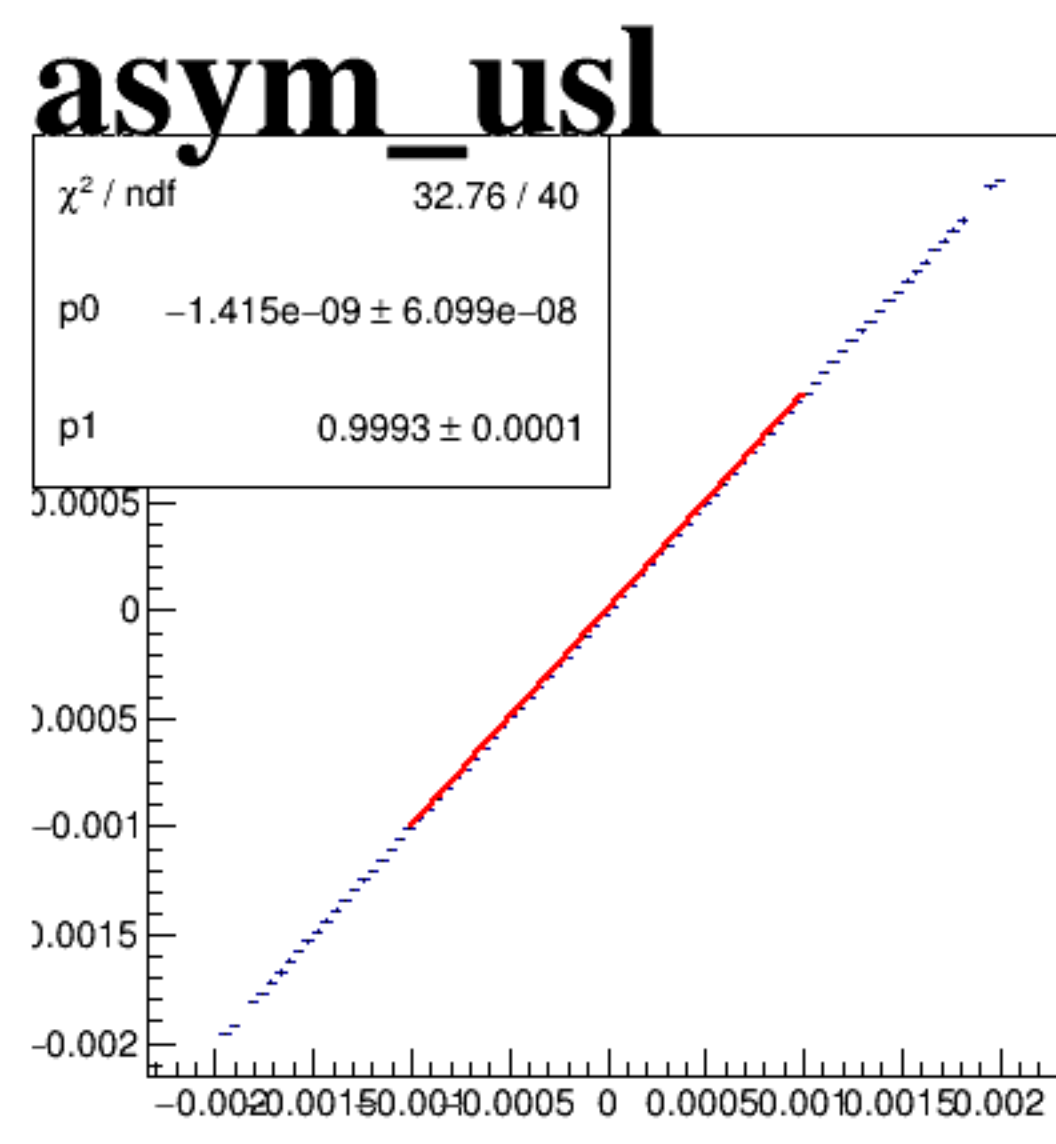
asym_usr



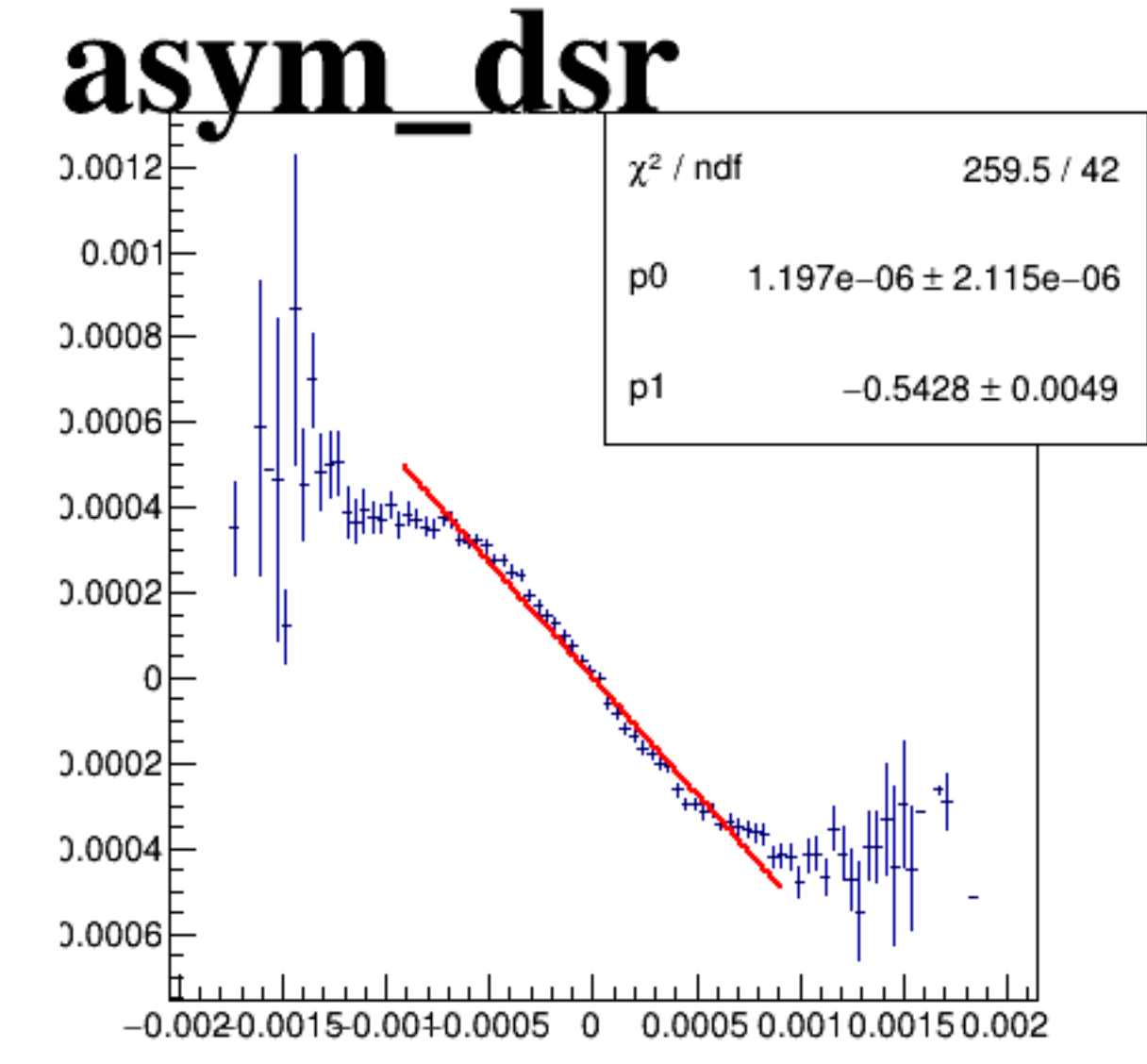
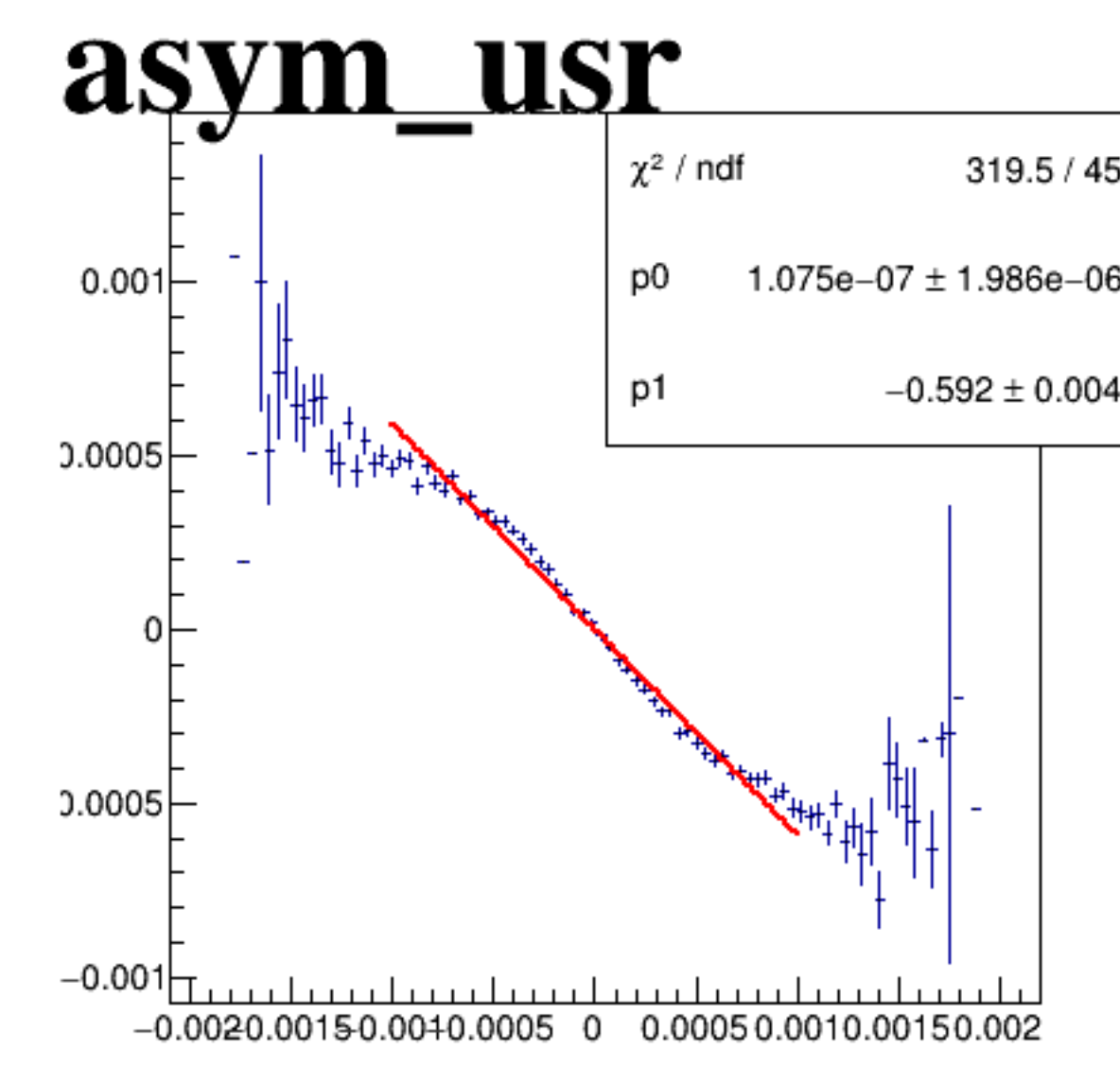
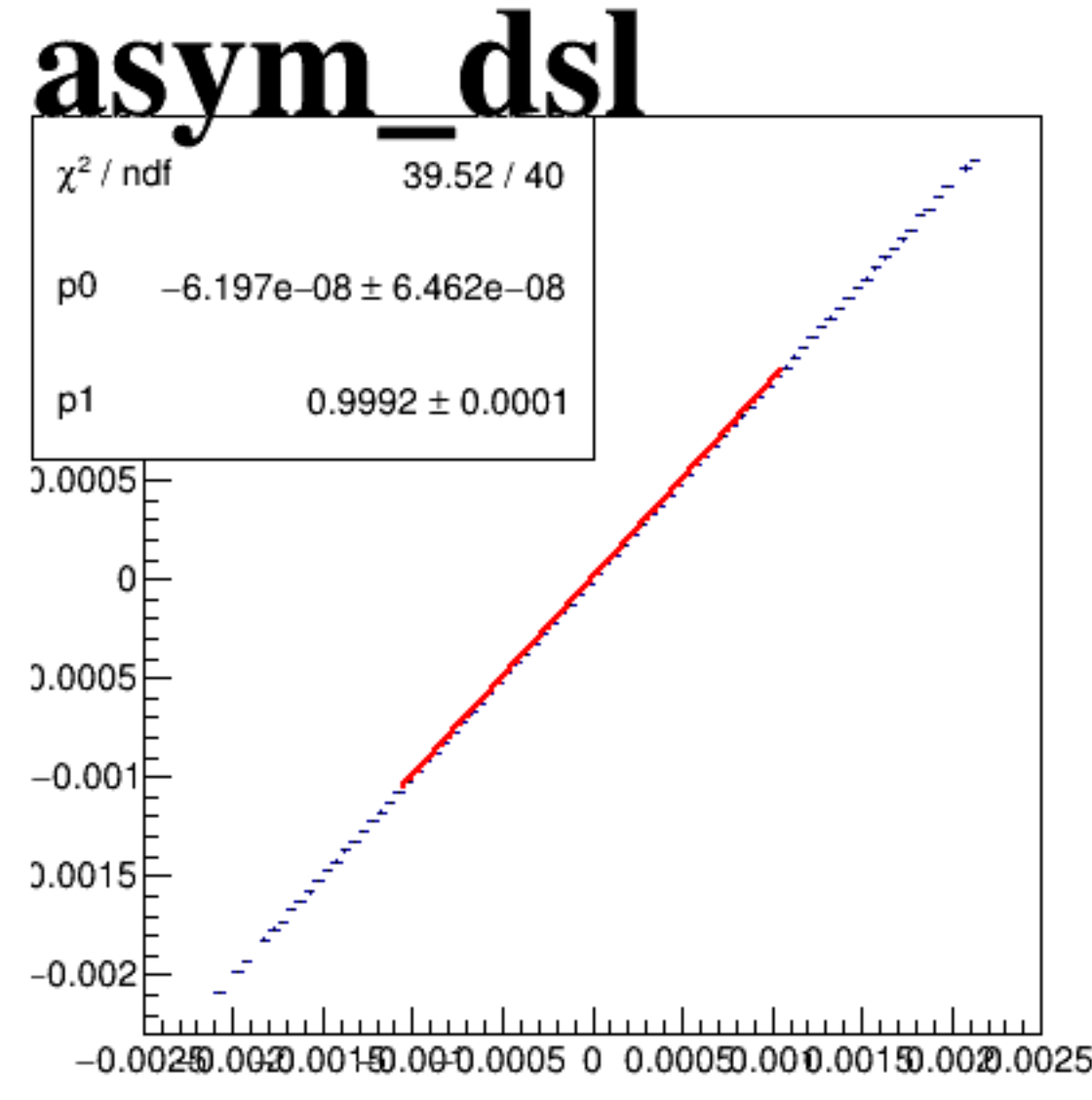
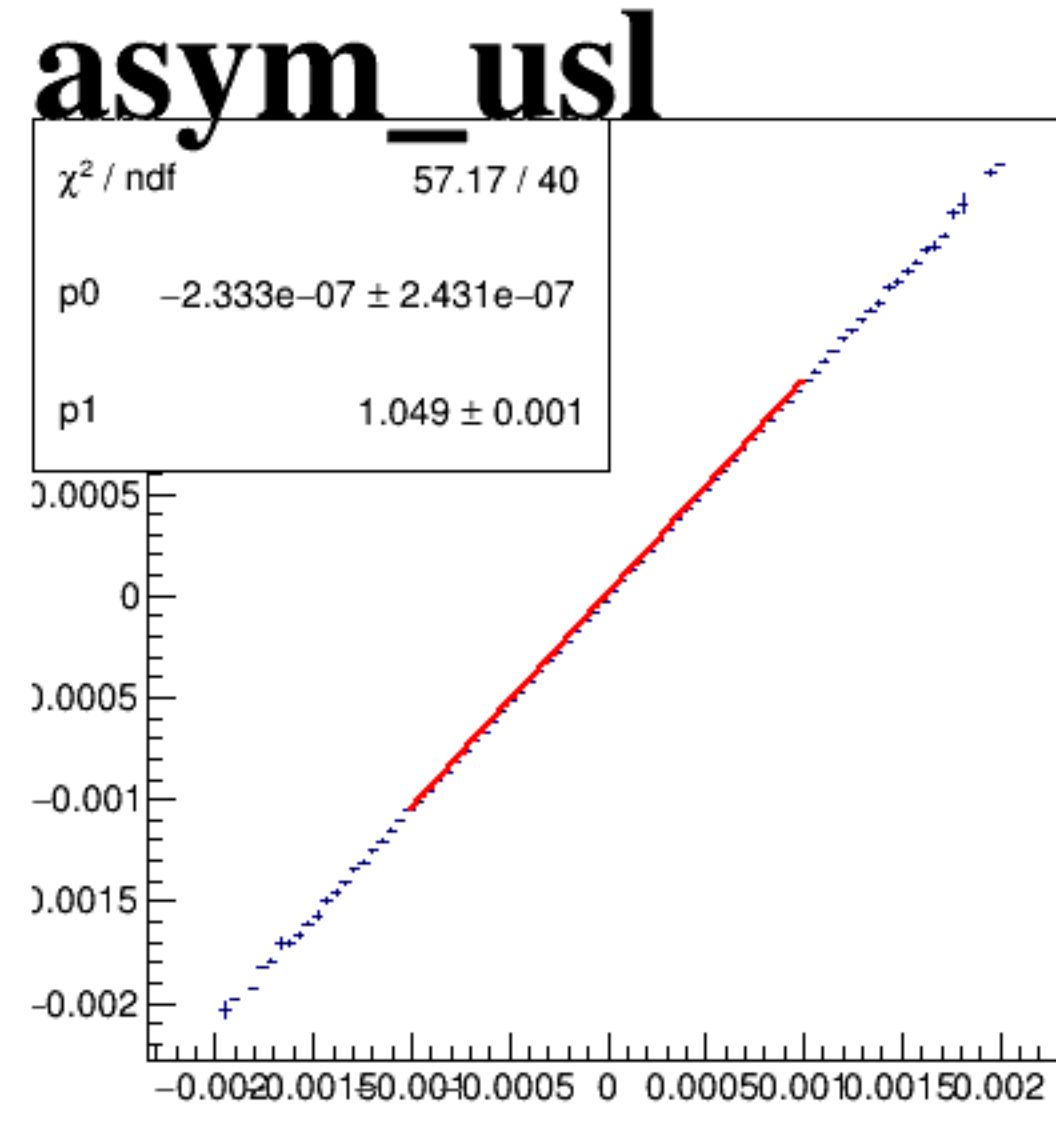
asym_dsr



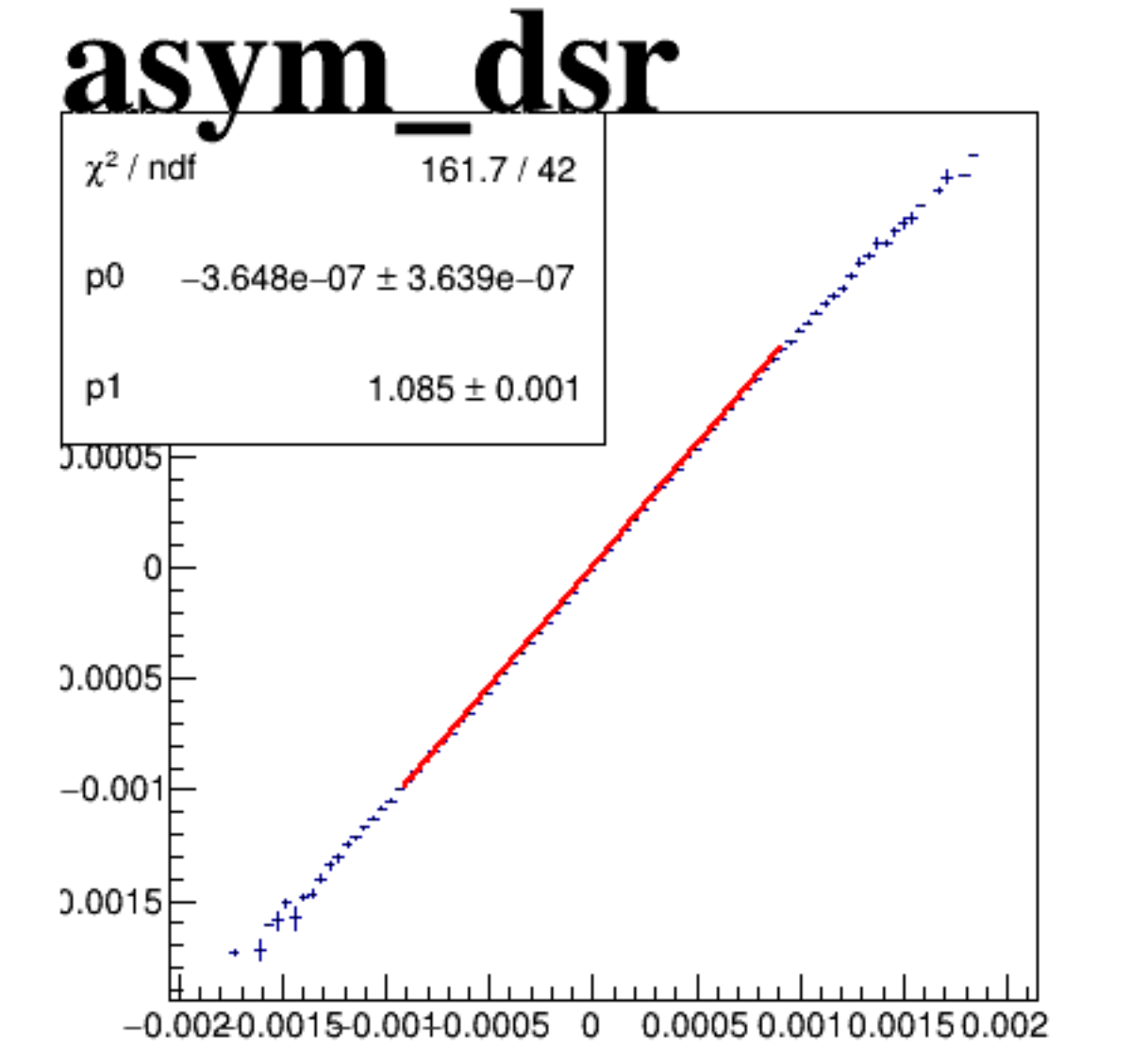
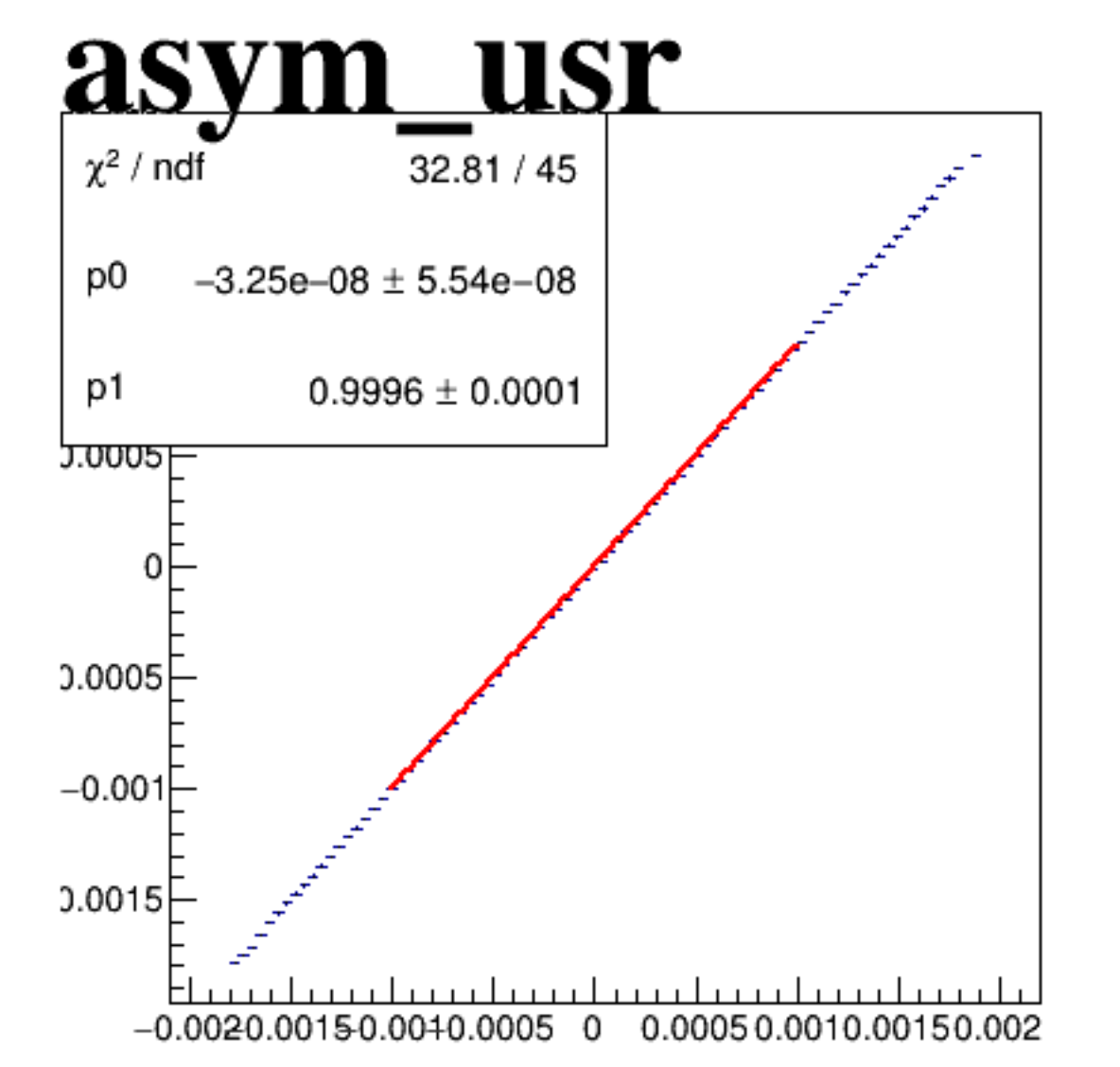
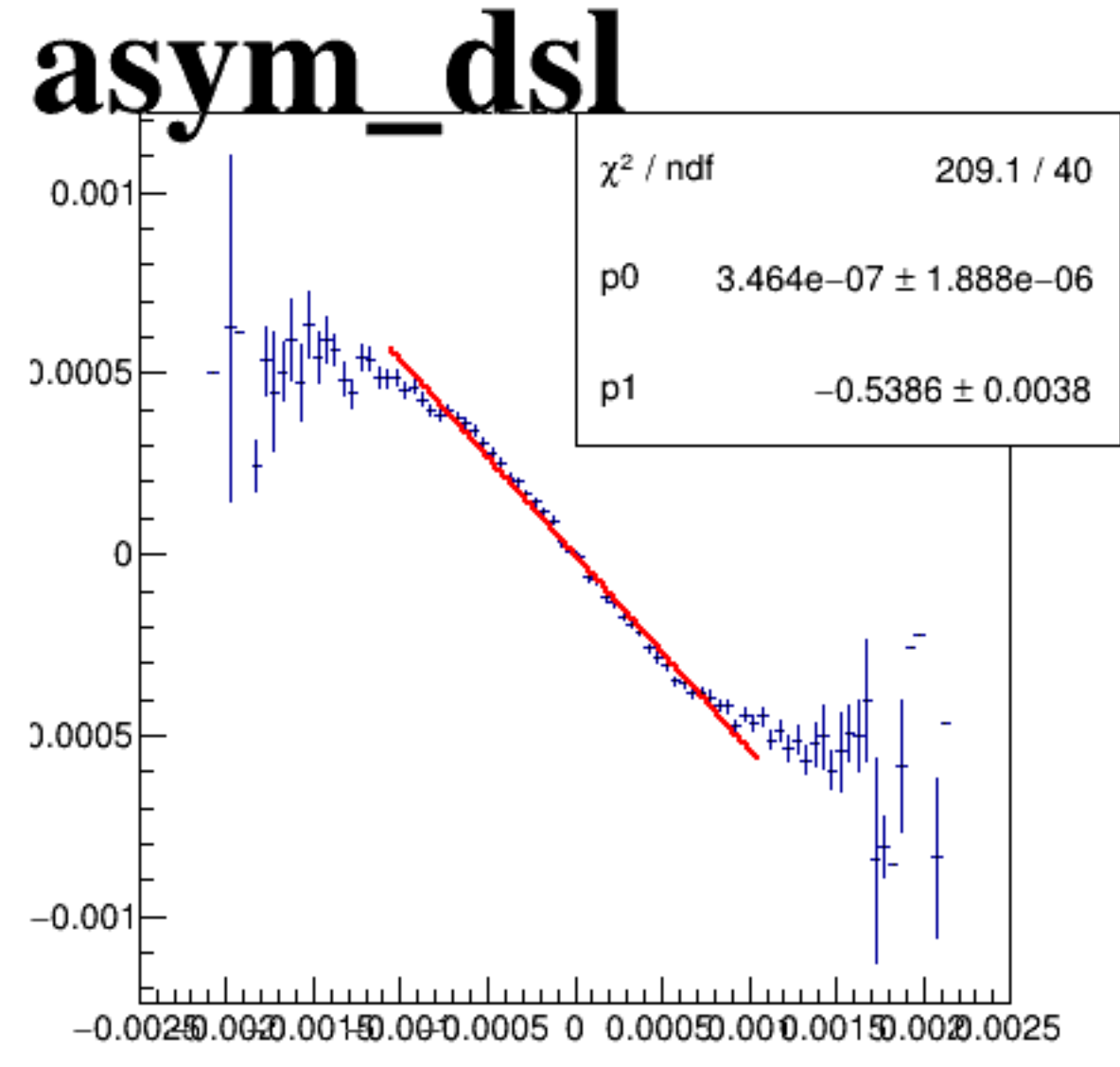
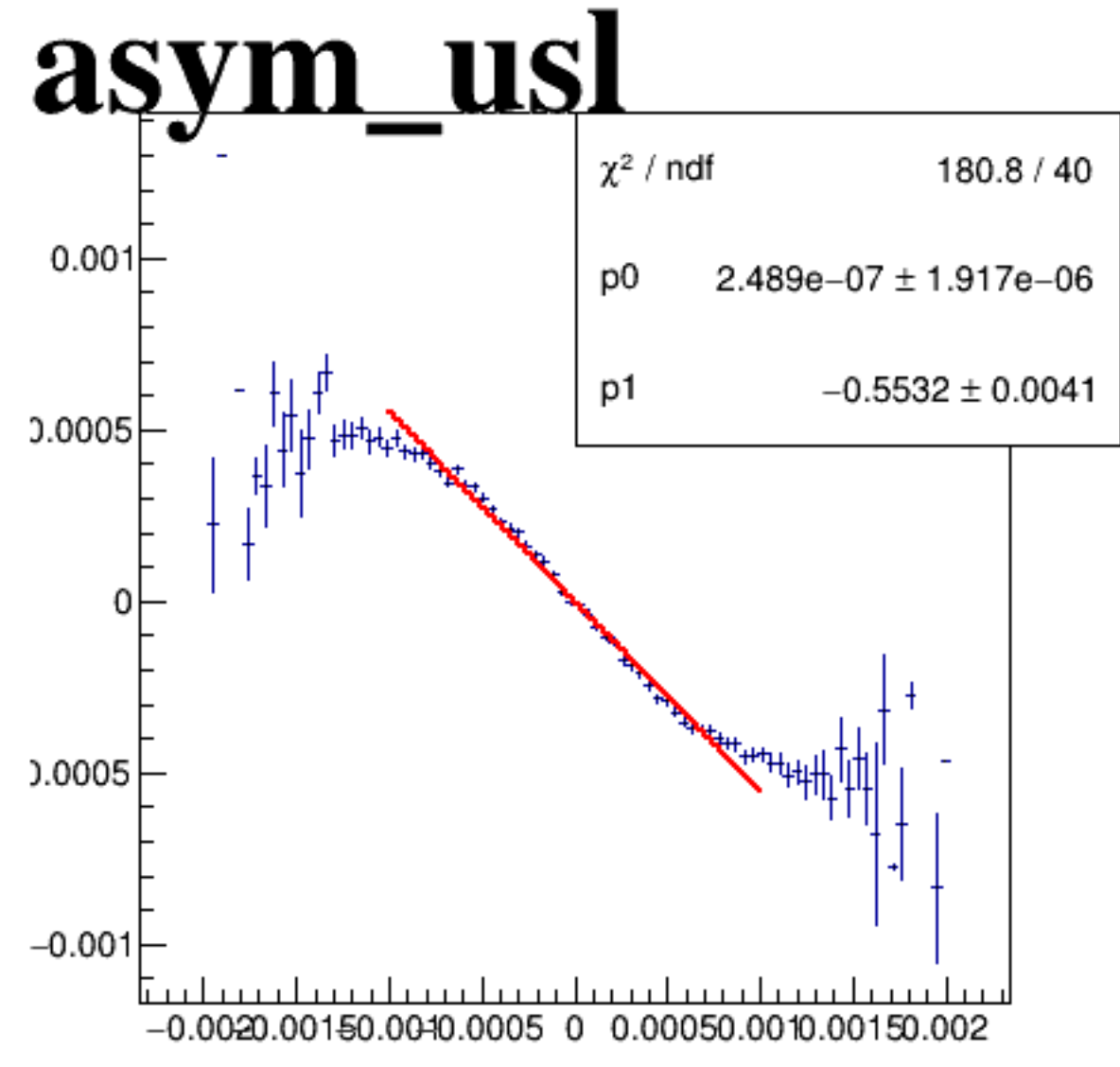
asym_usl



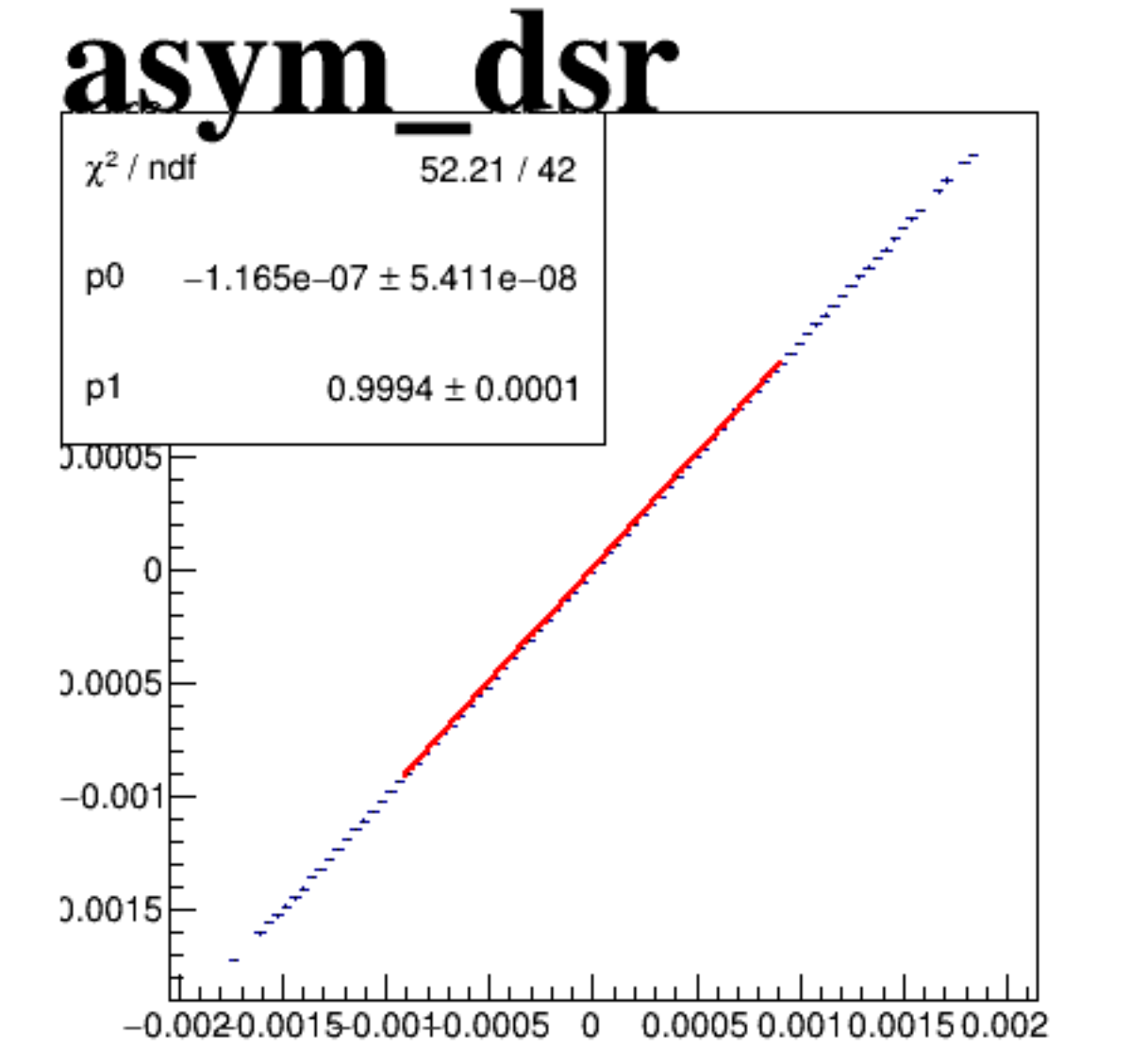
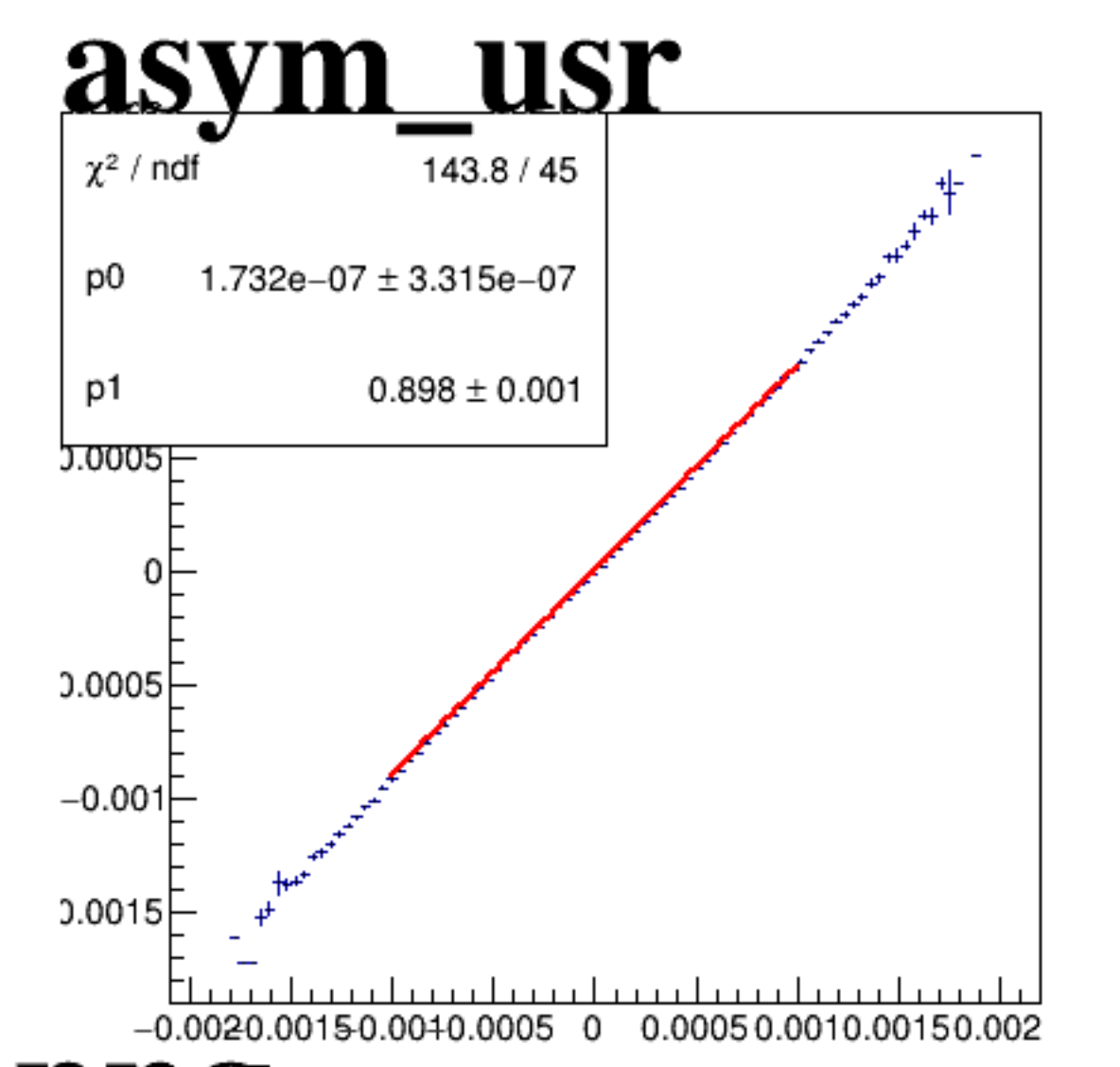
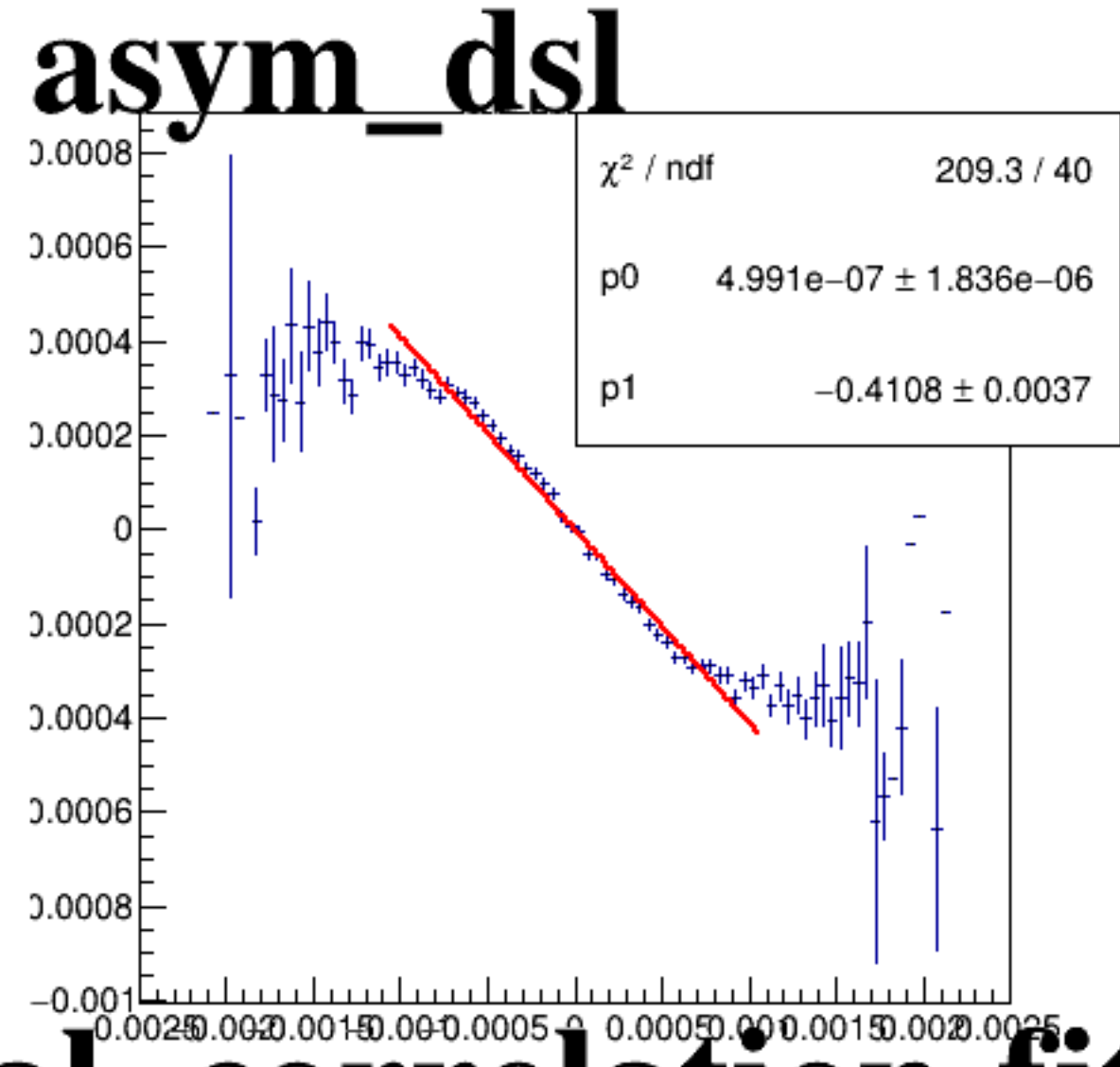
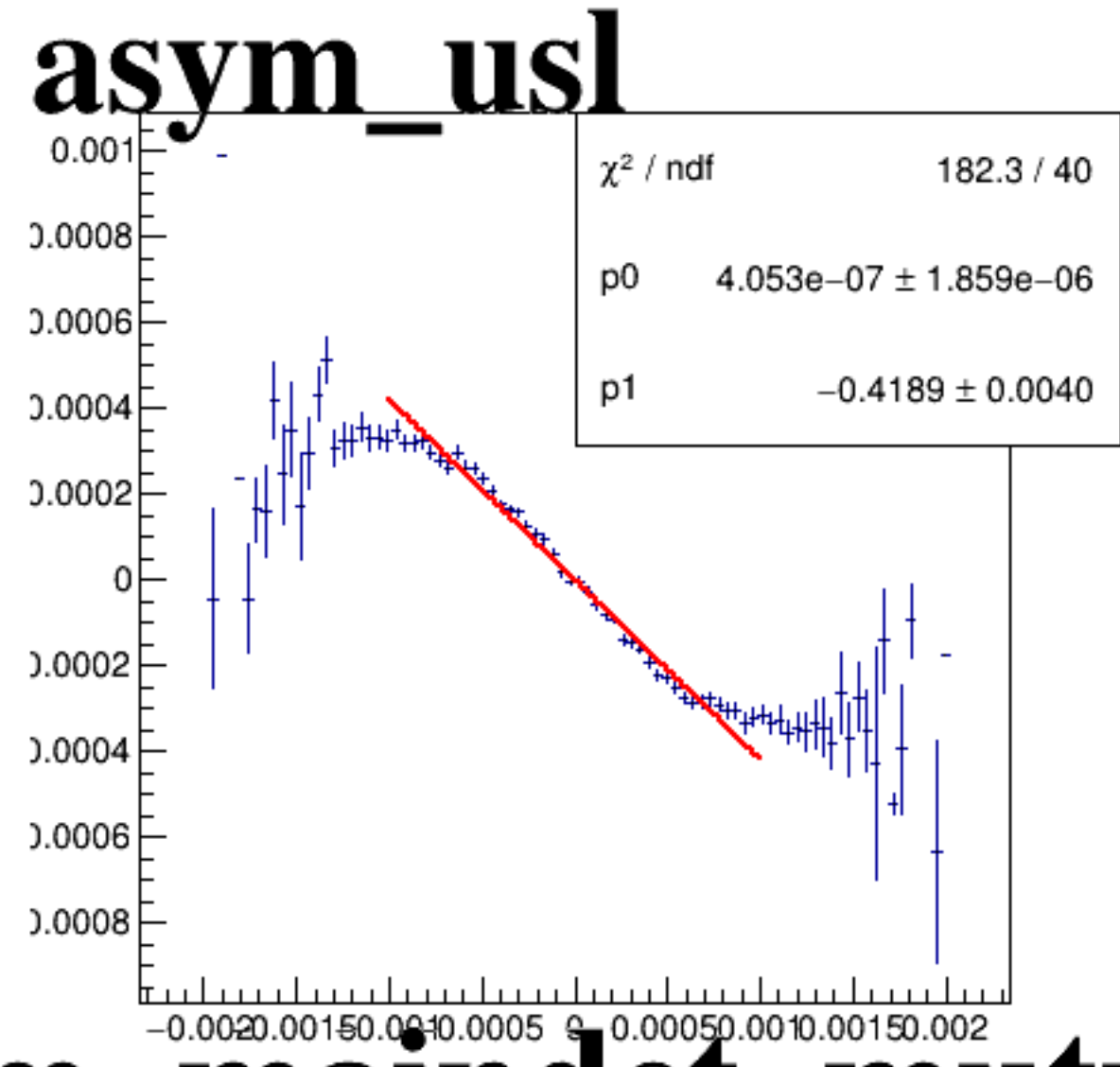
asym_dsl



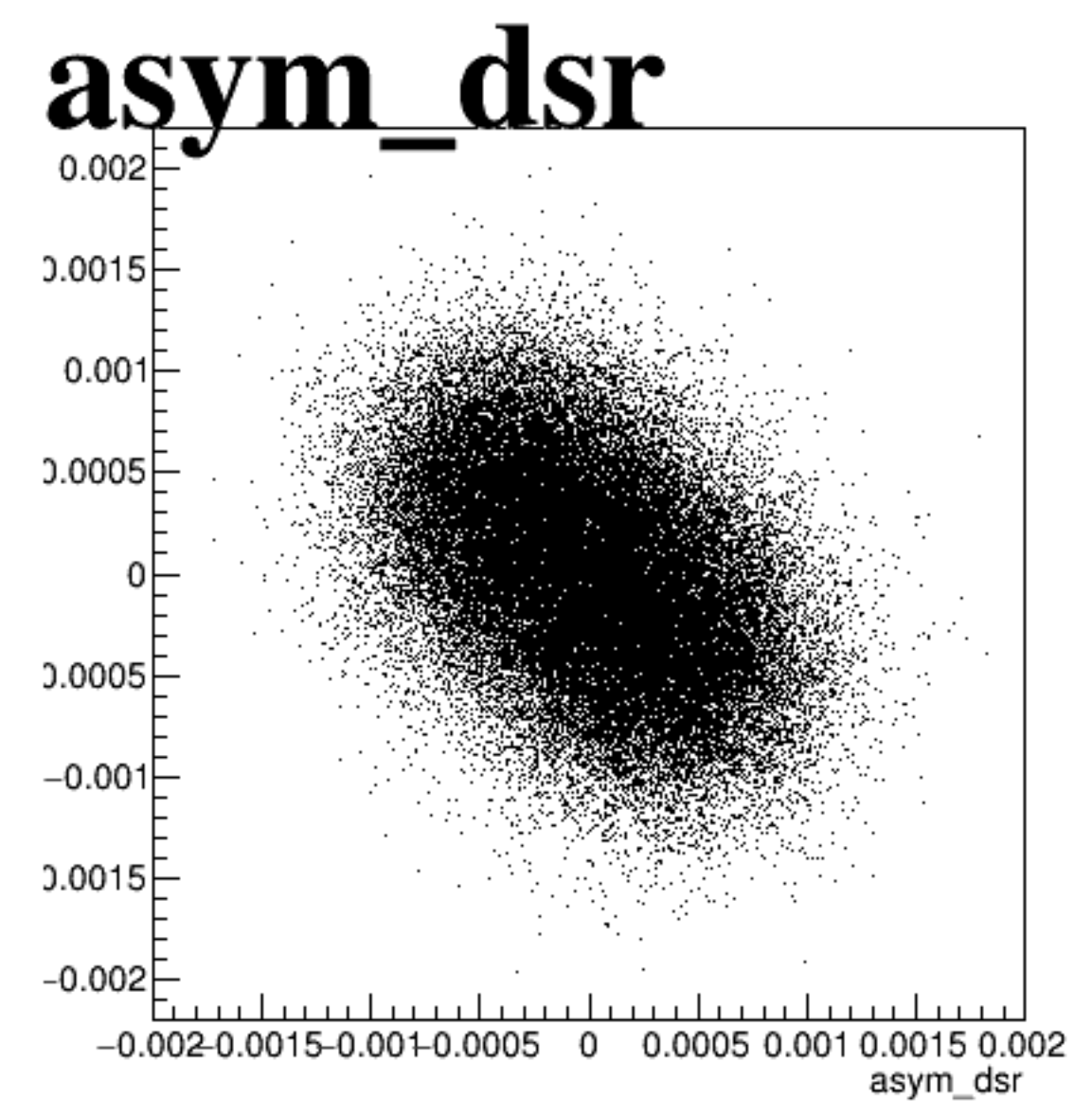
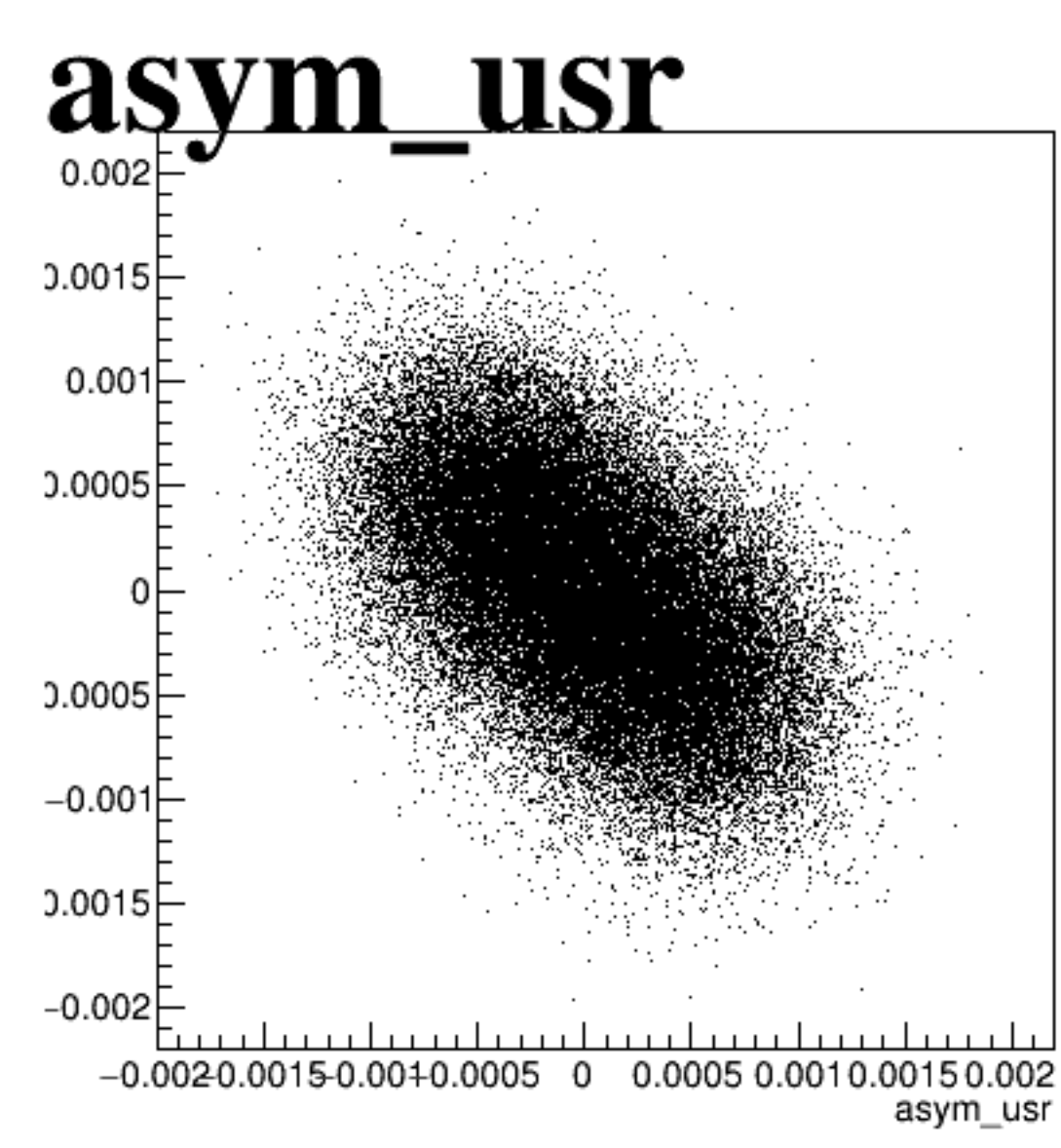
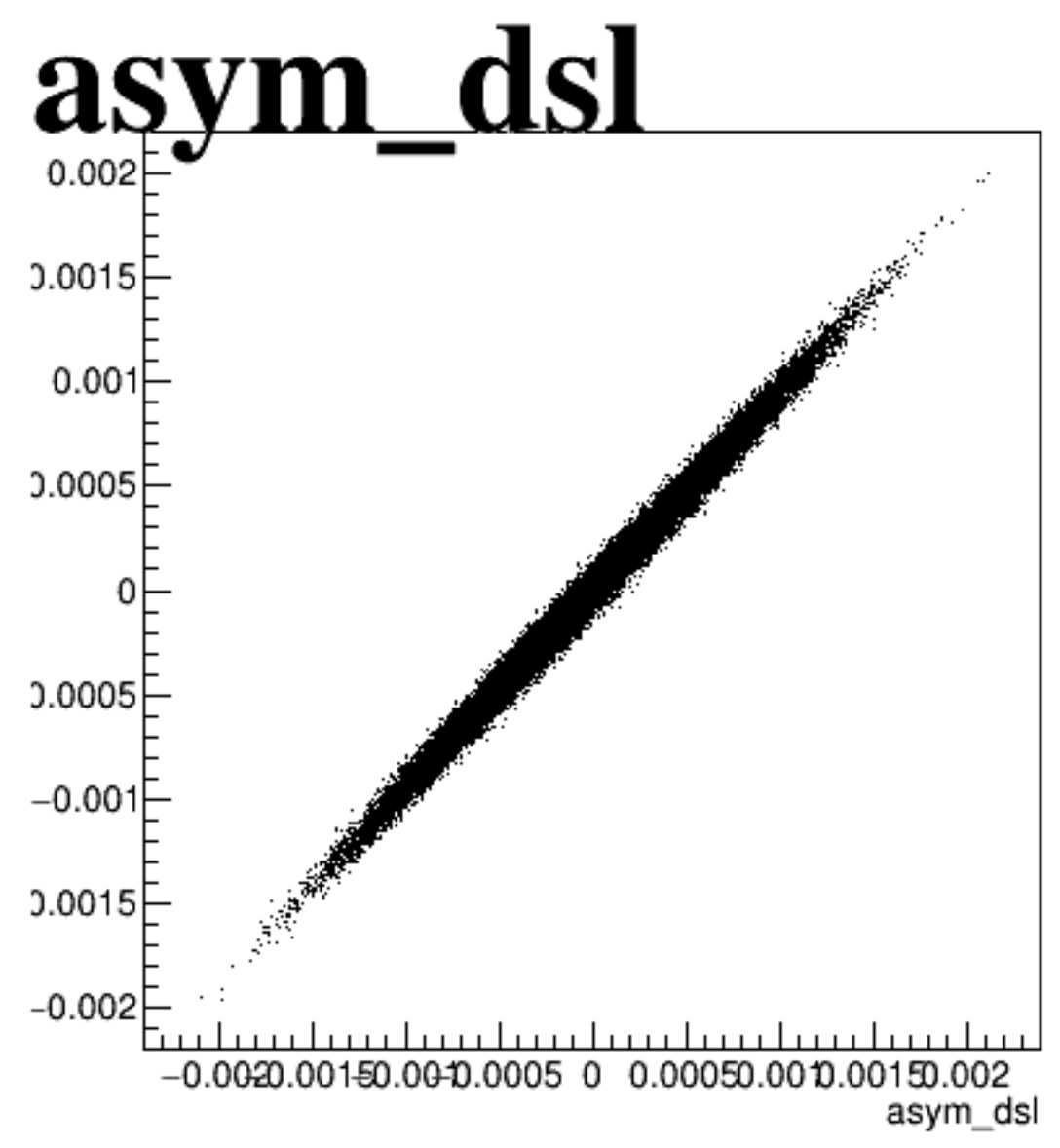
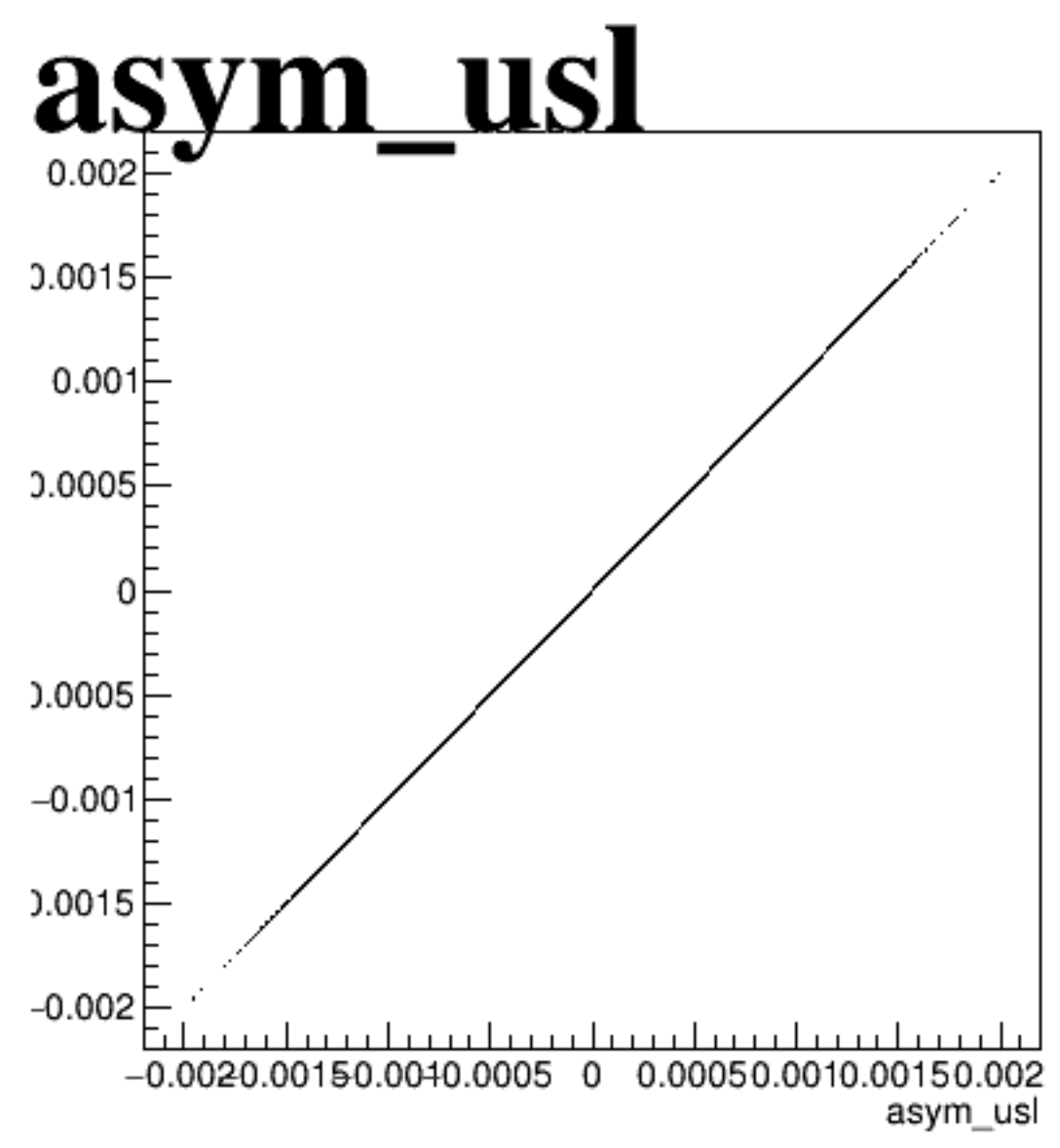
asym_usr



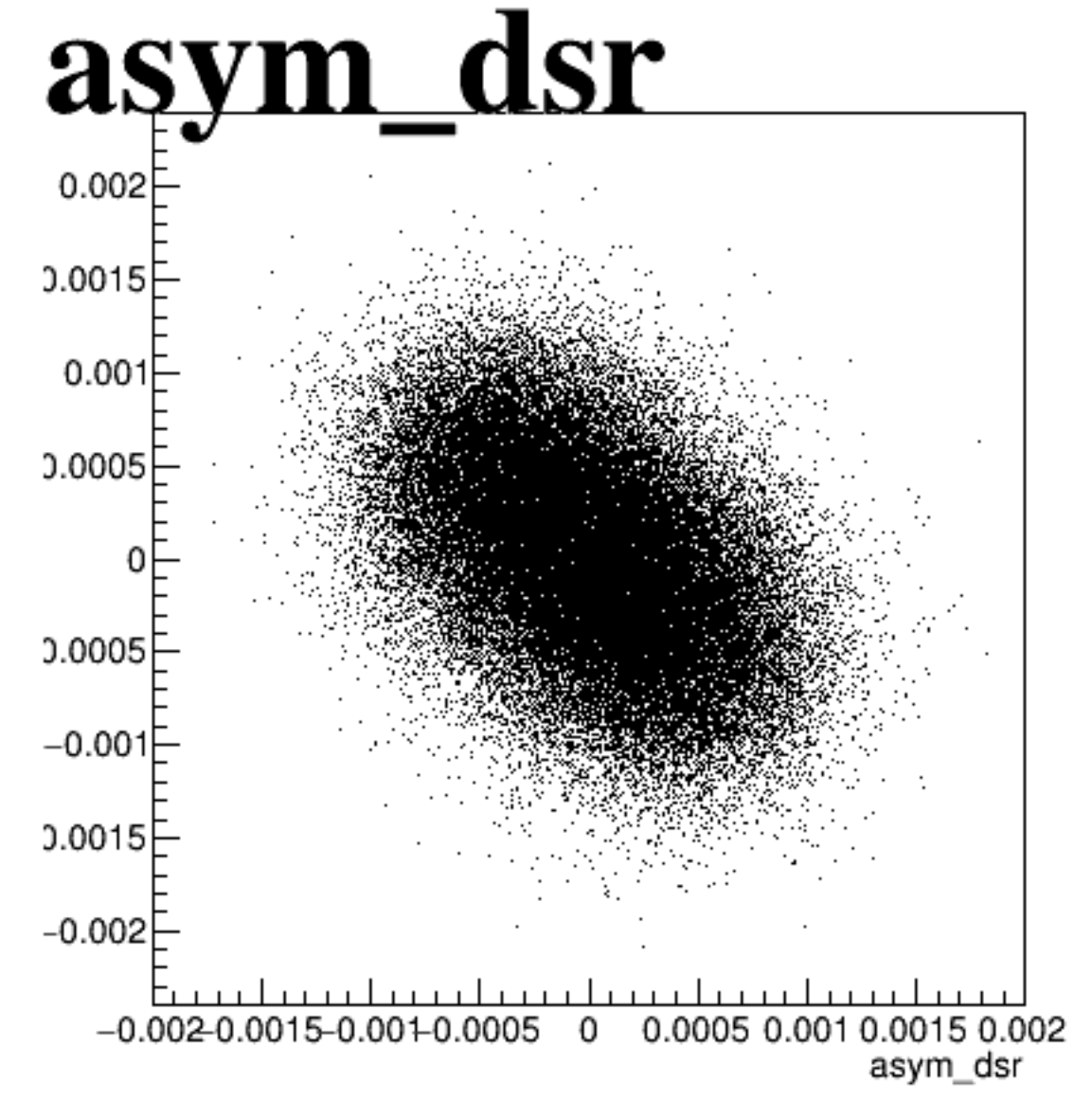
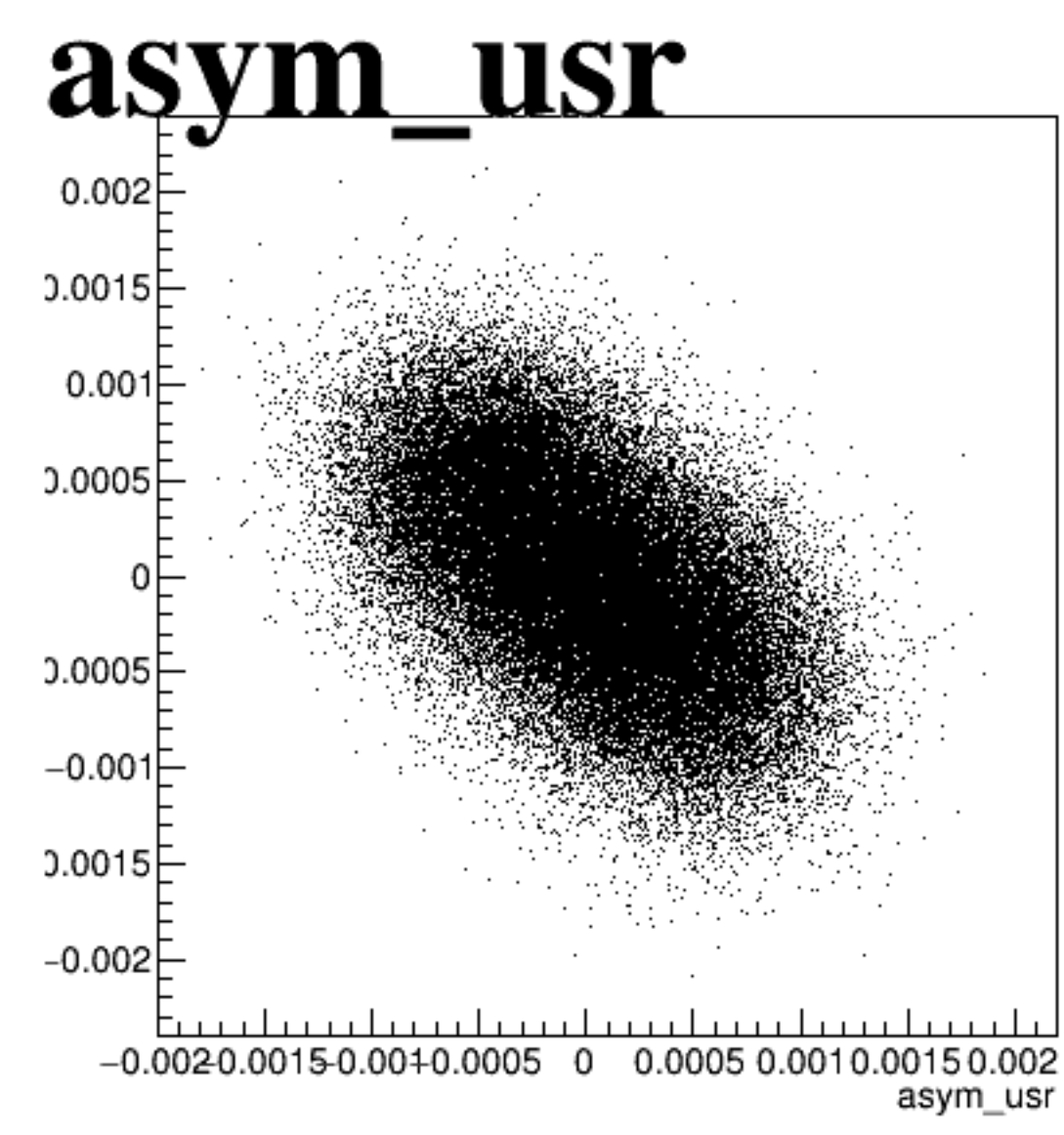
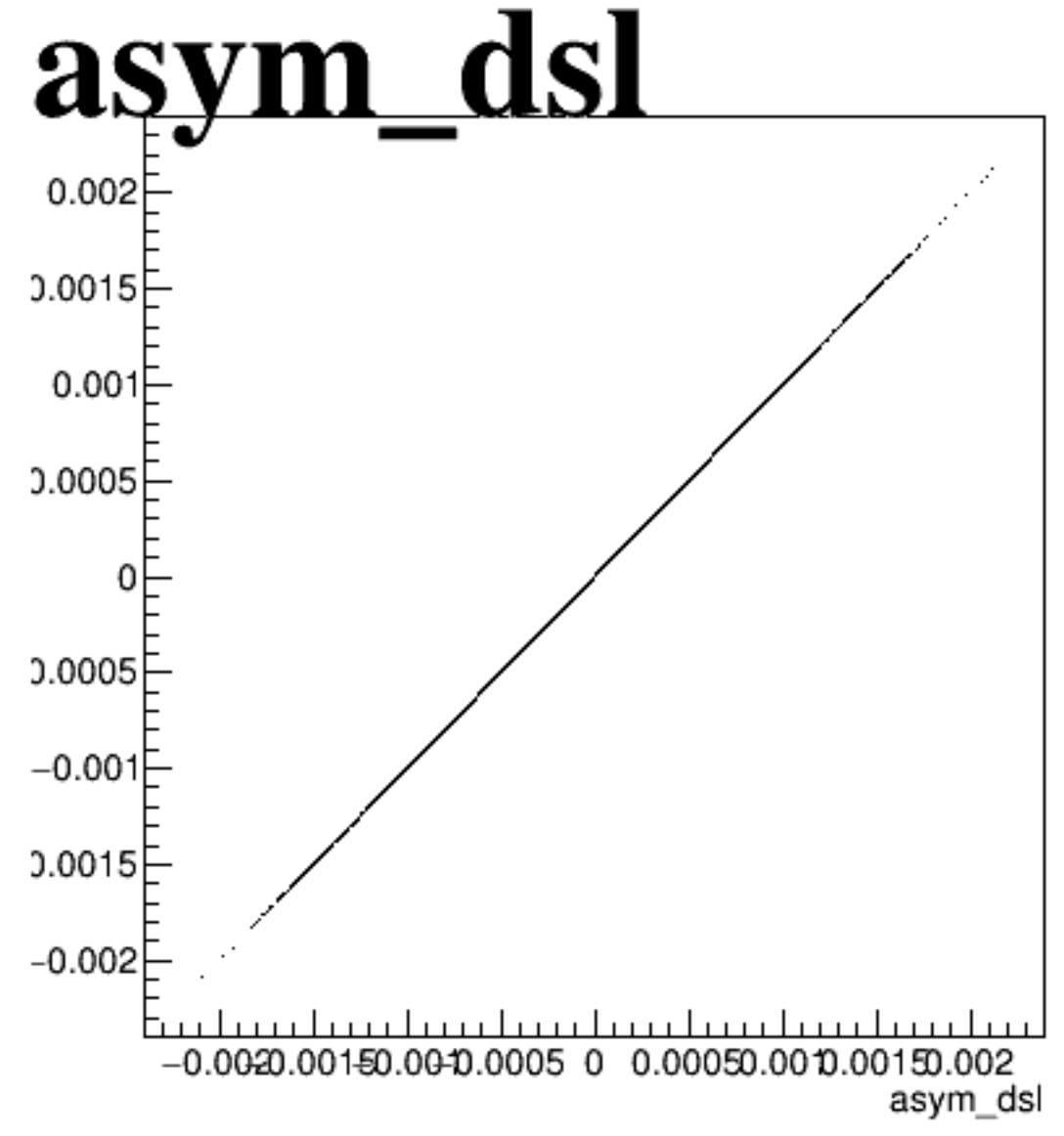
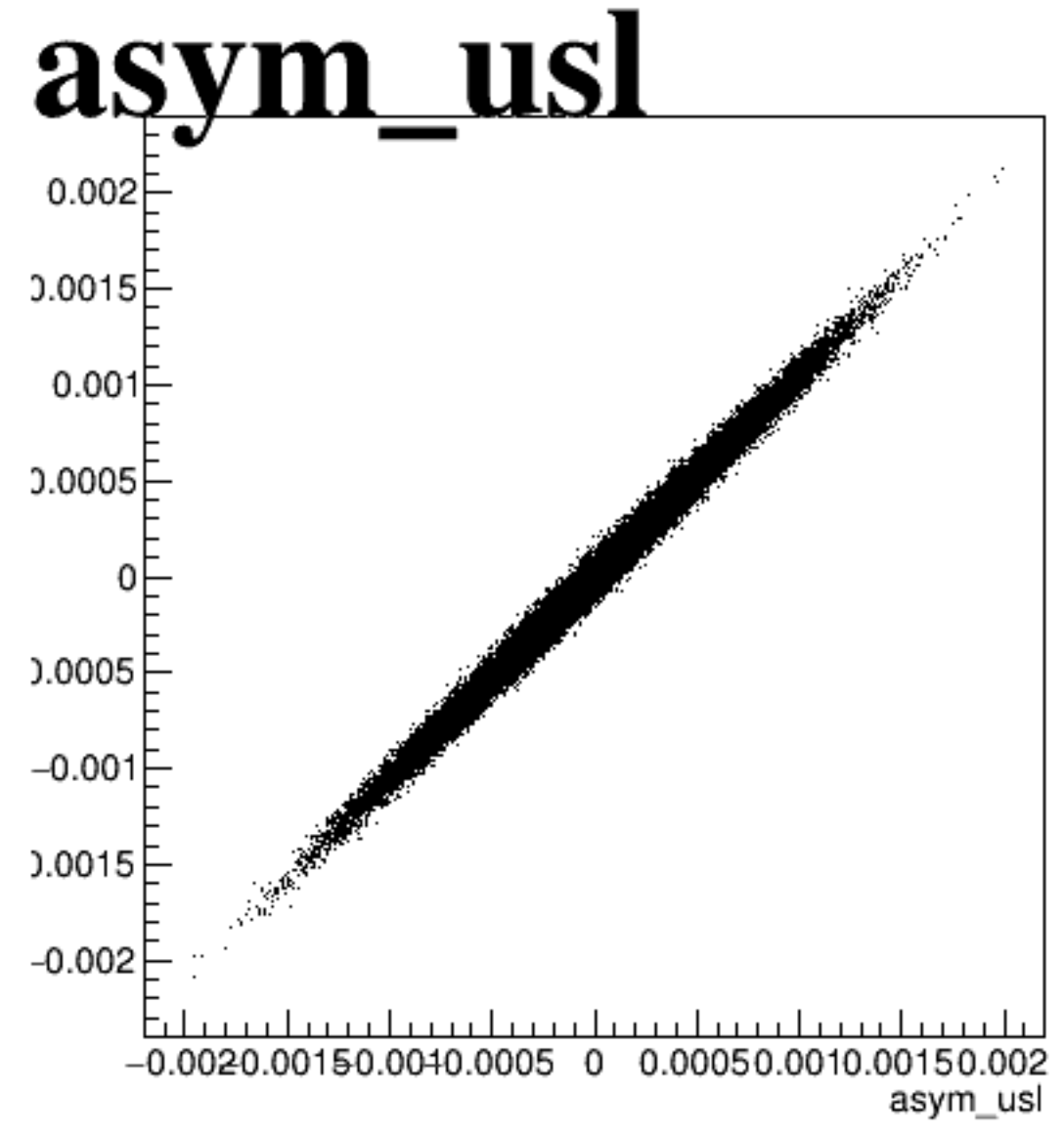
asym_dsr



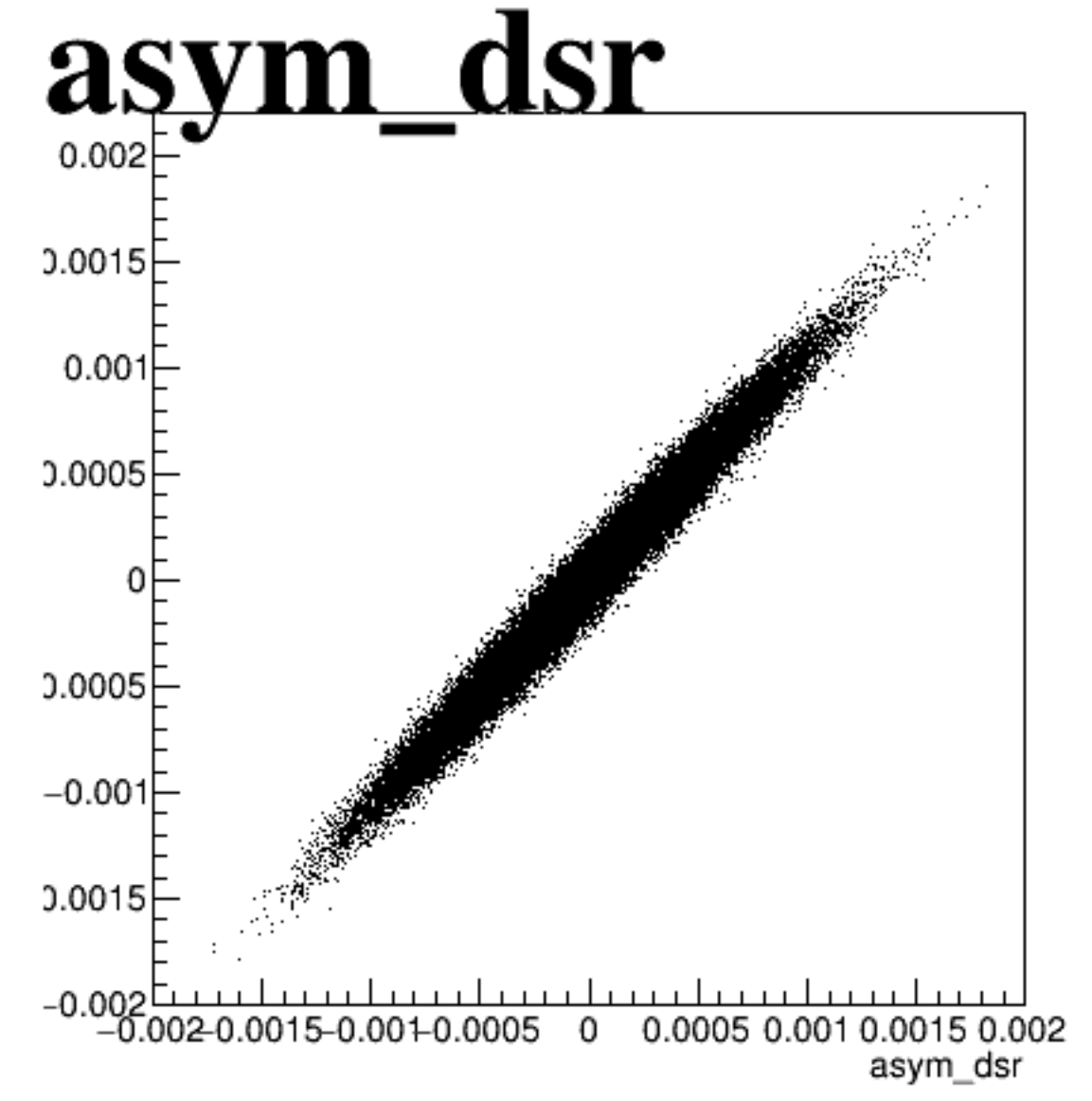
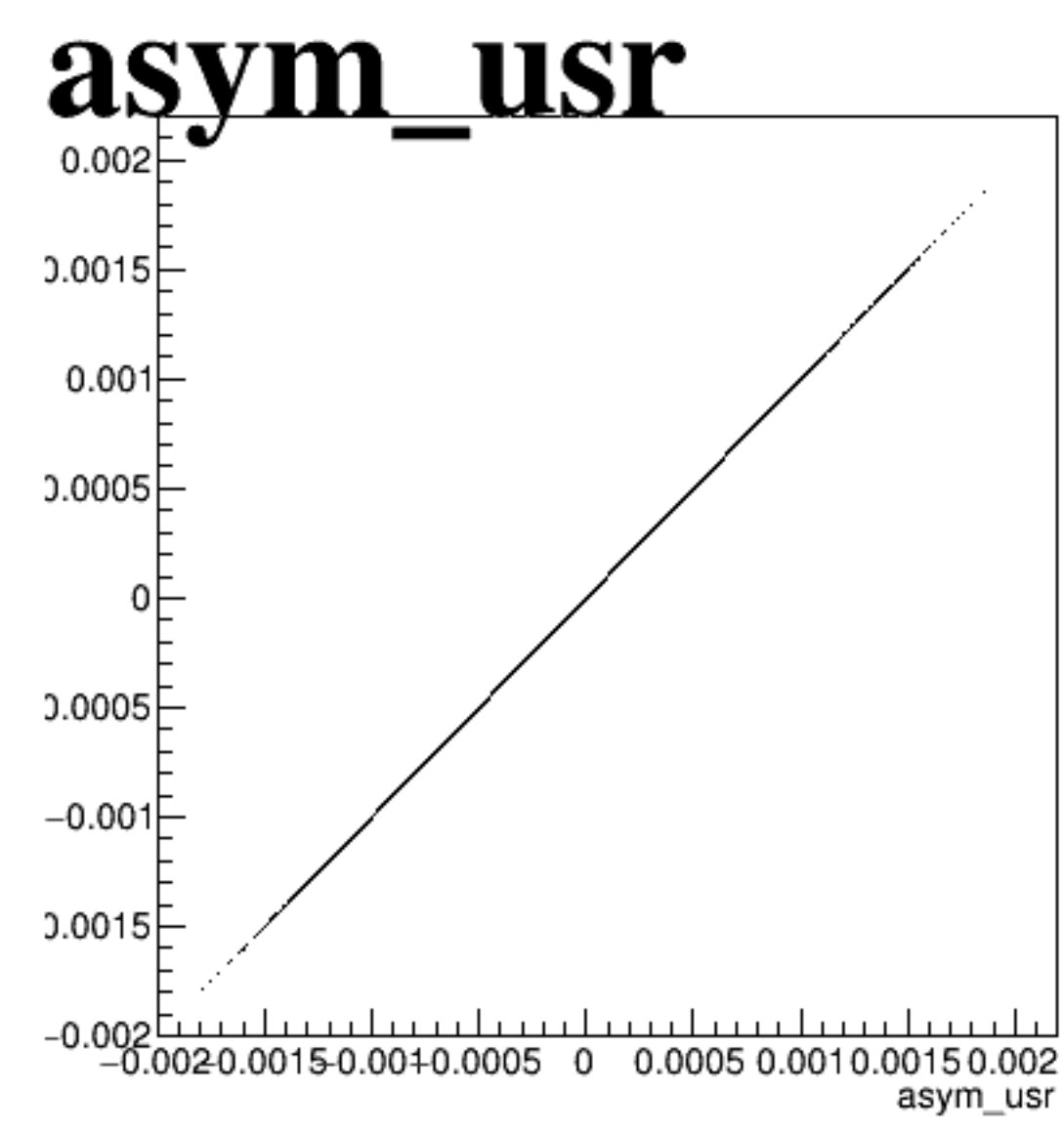
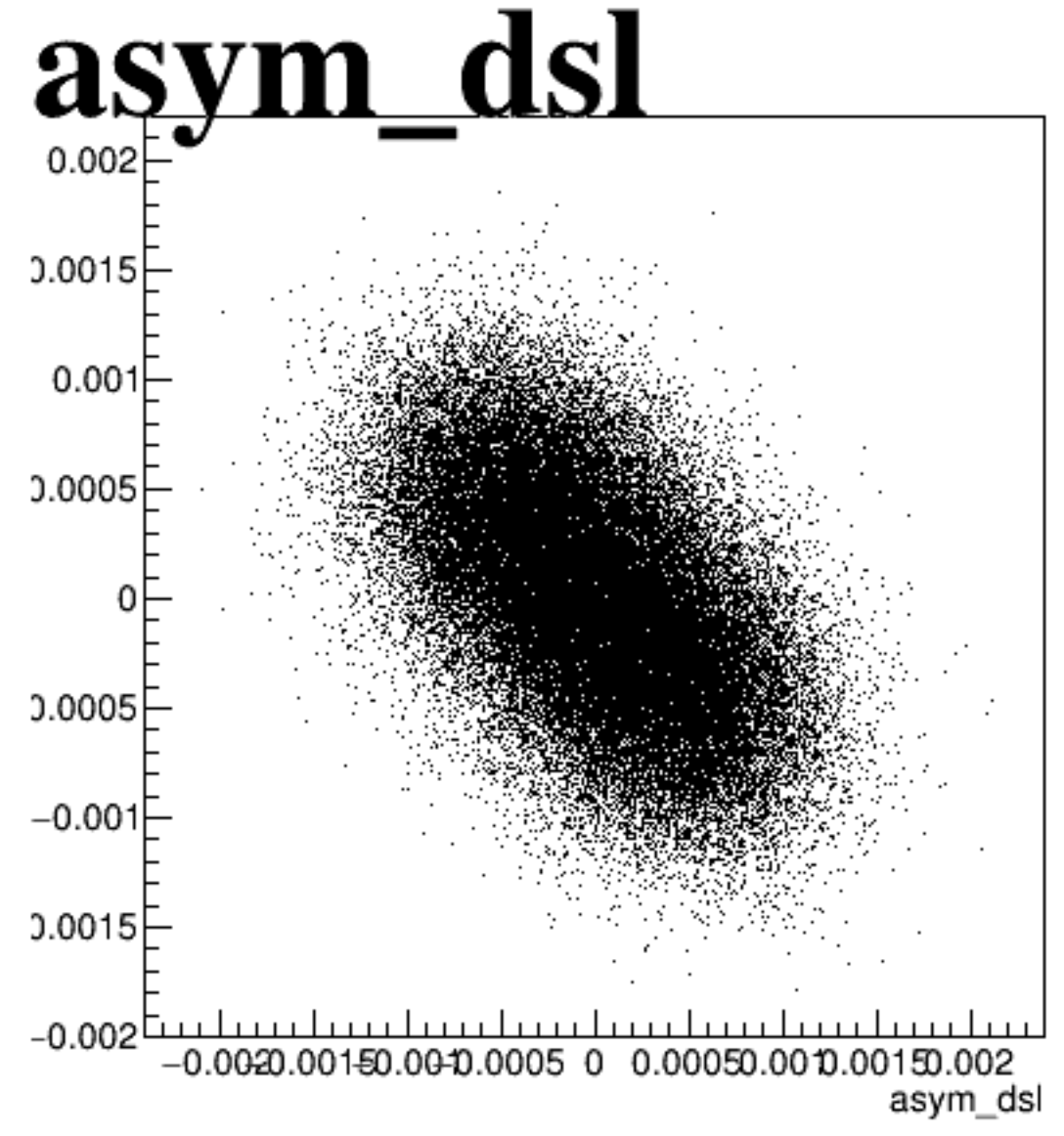
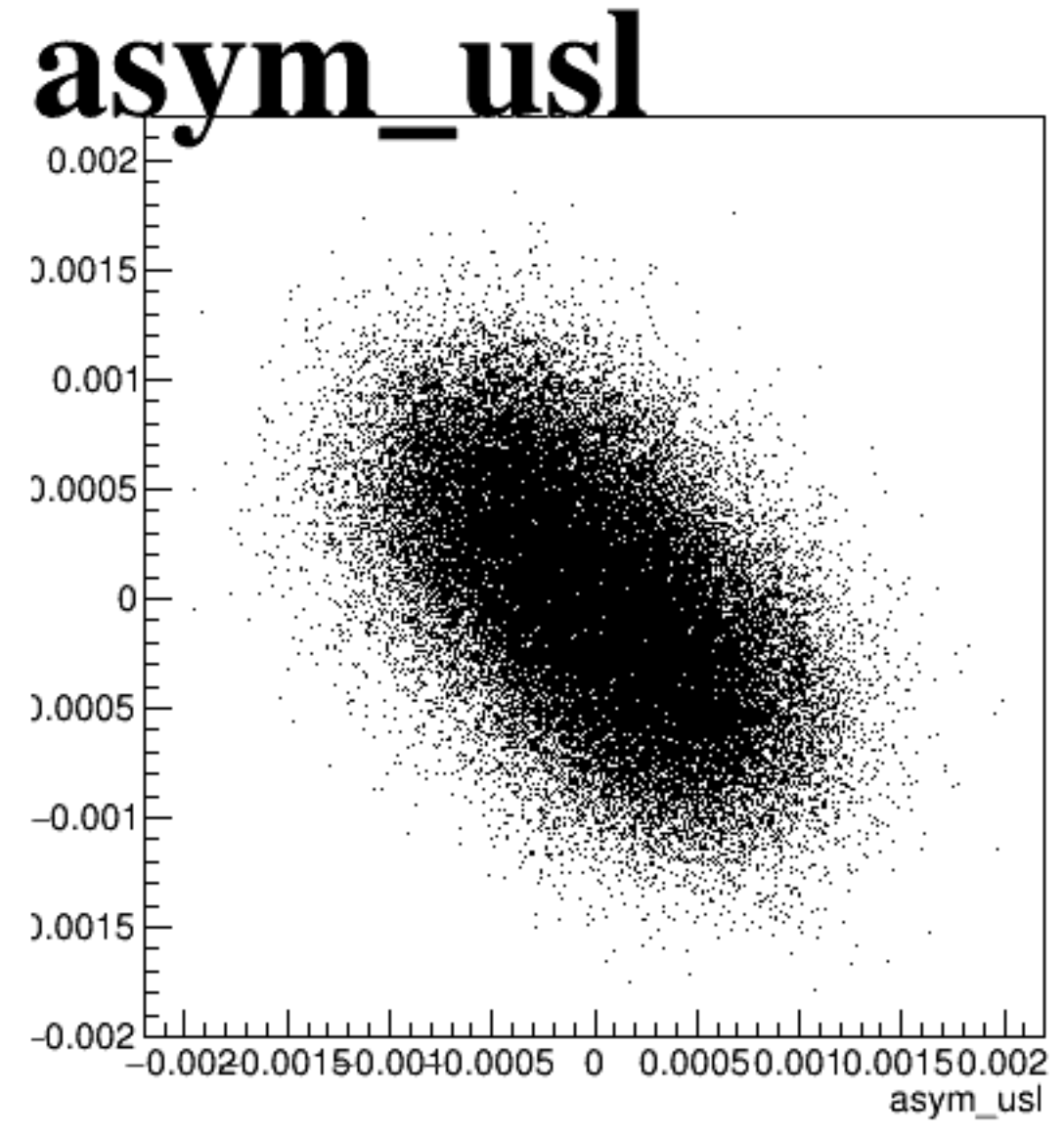
asym_usl



asym_dsl



asym_usr



asym_dsr

