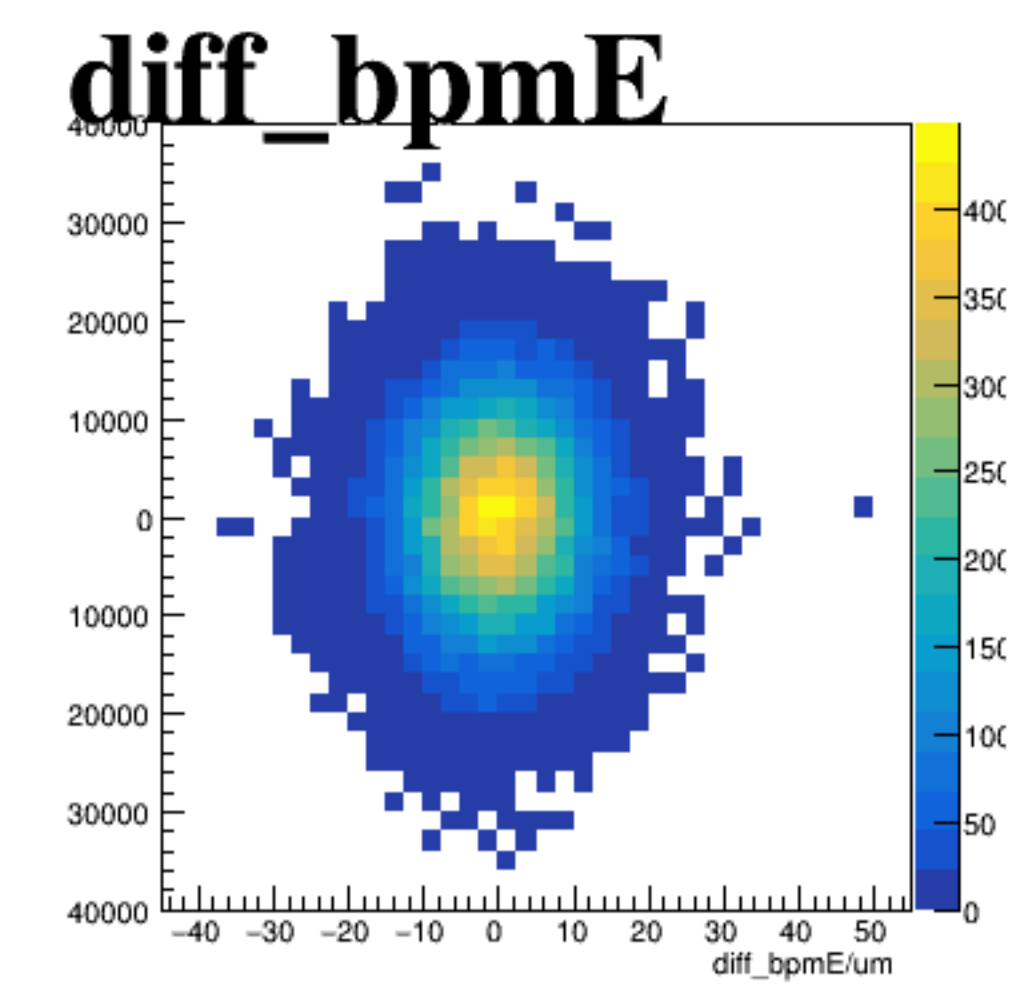
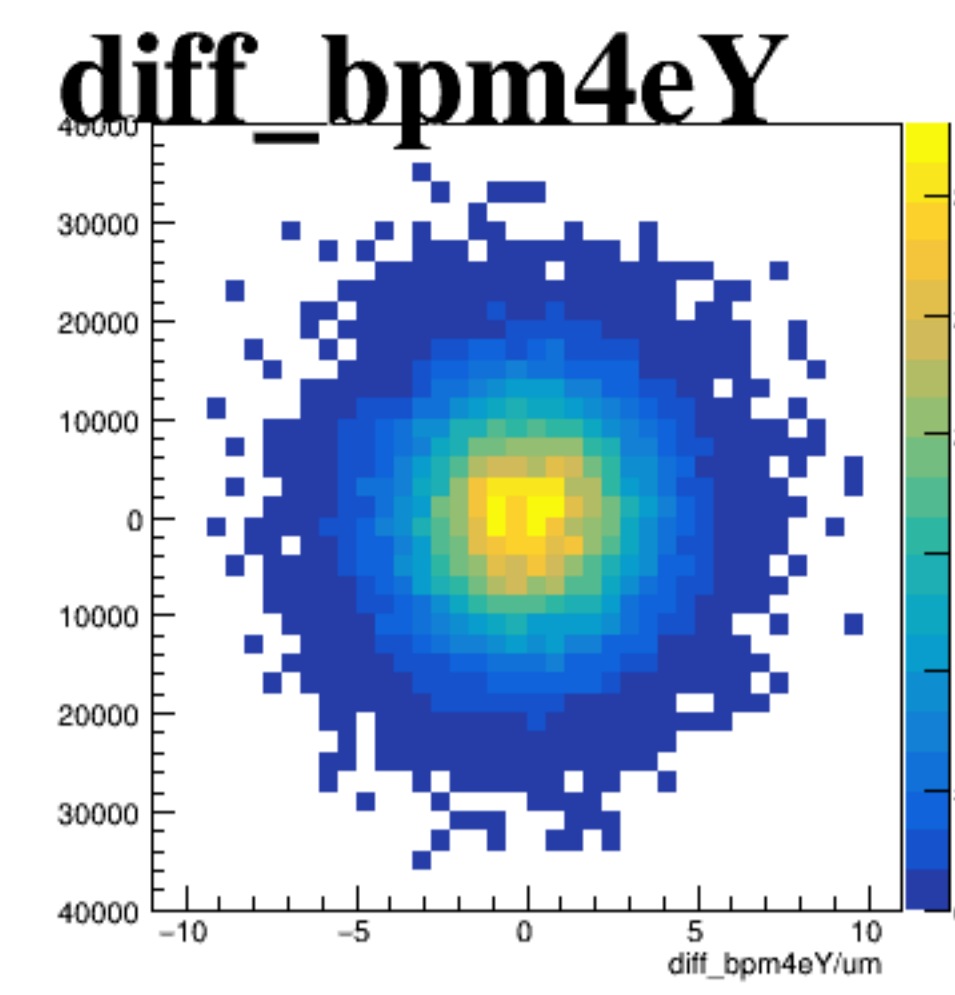
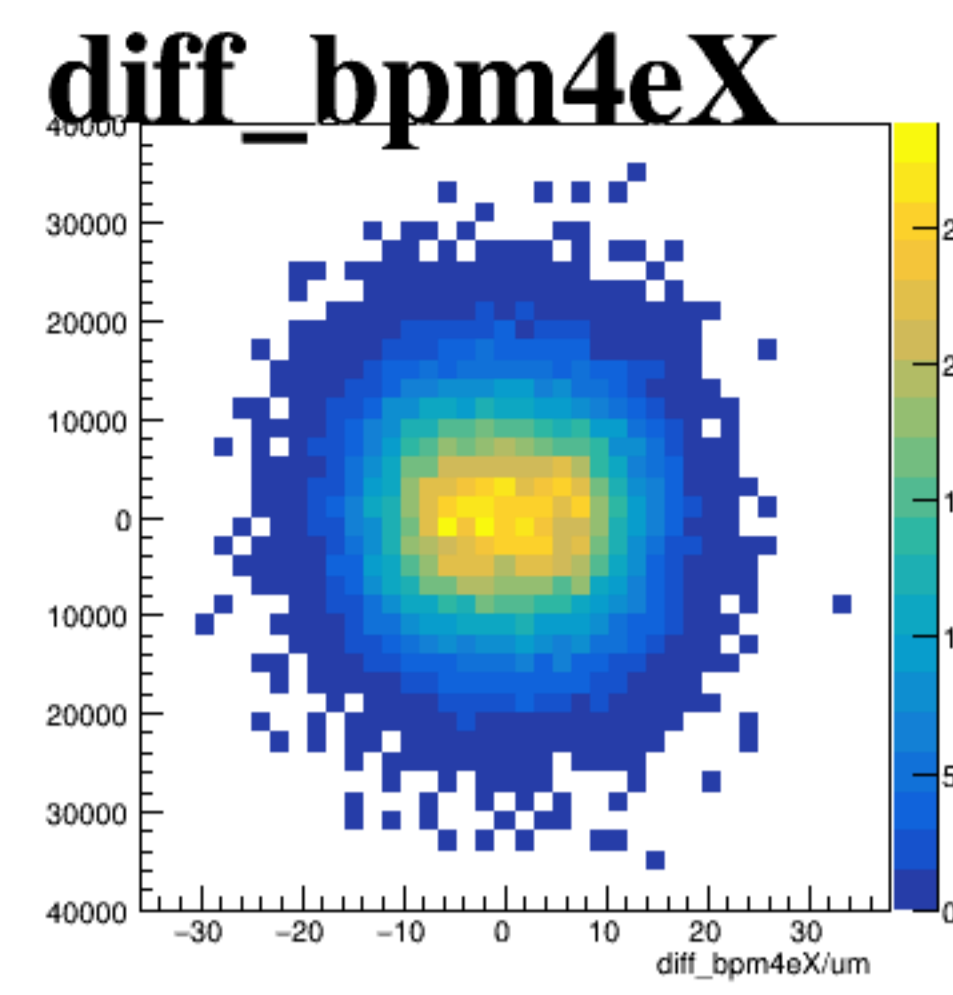
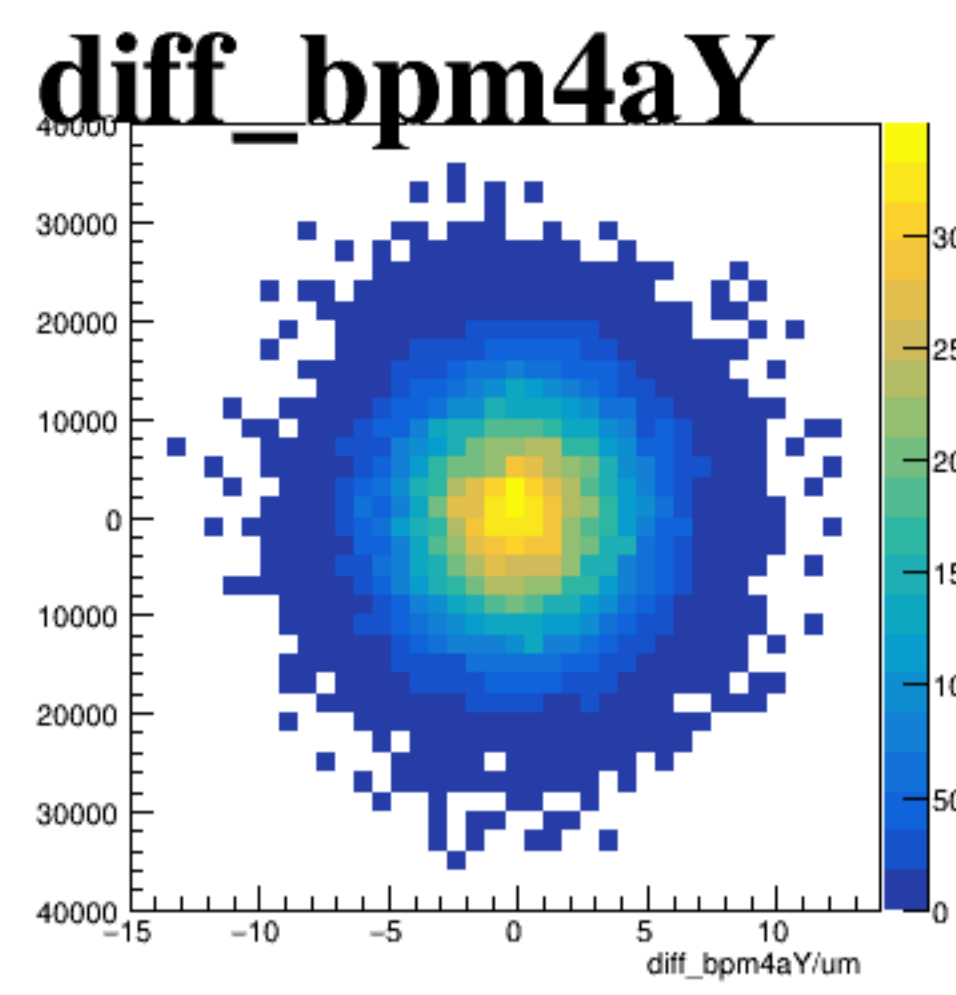
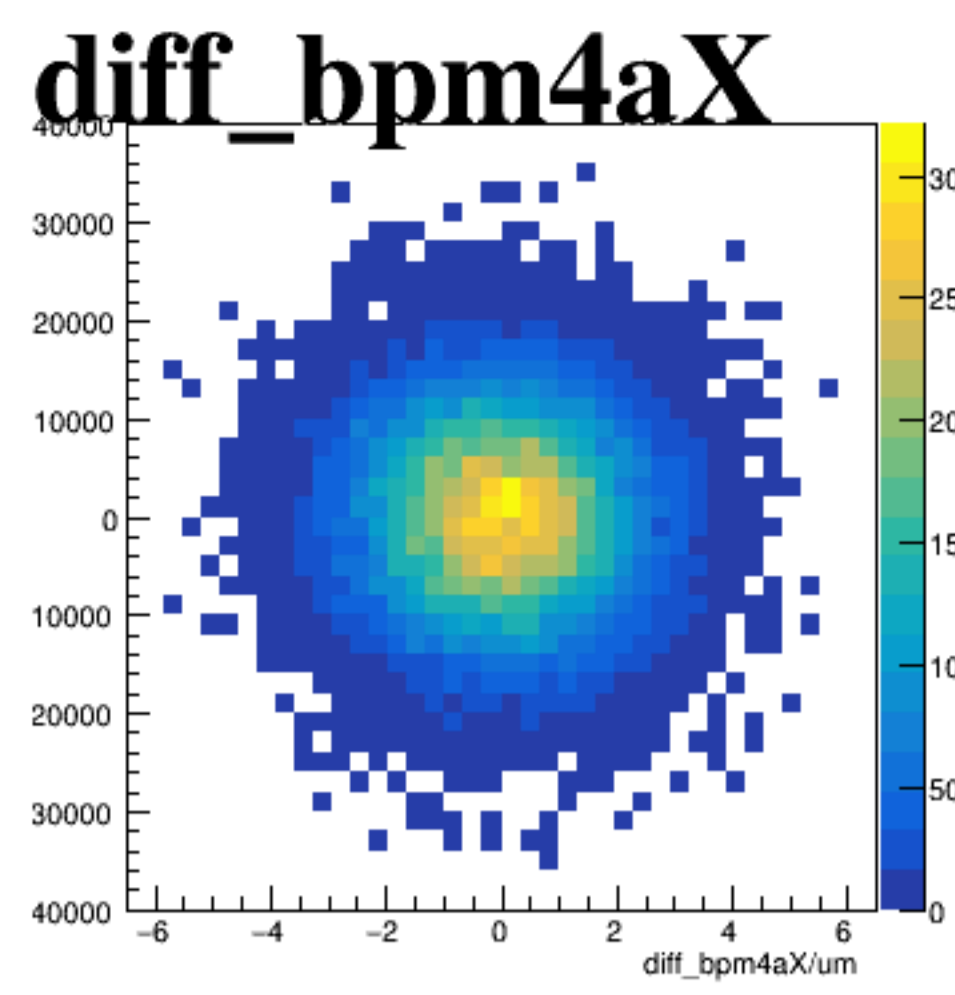
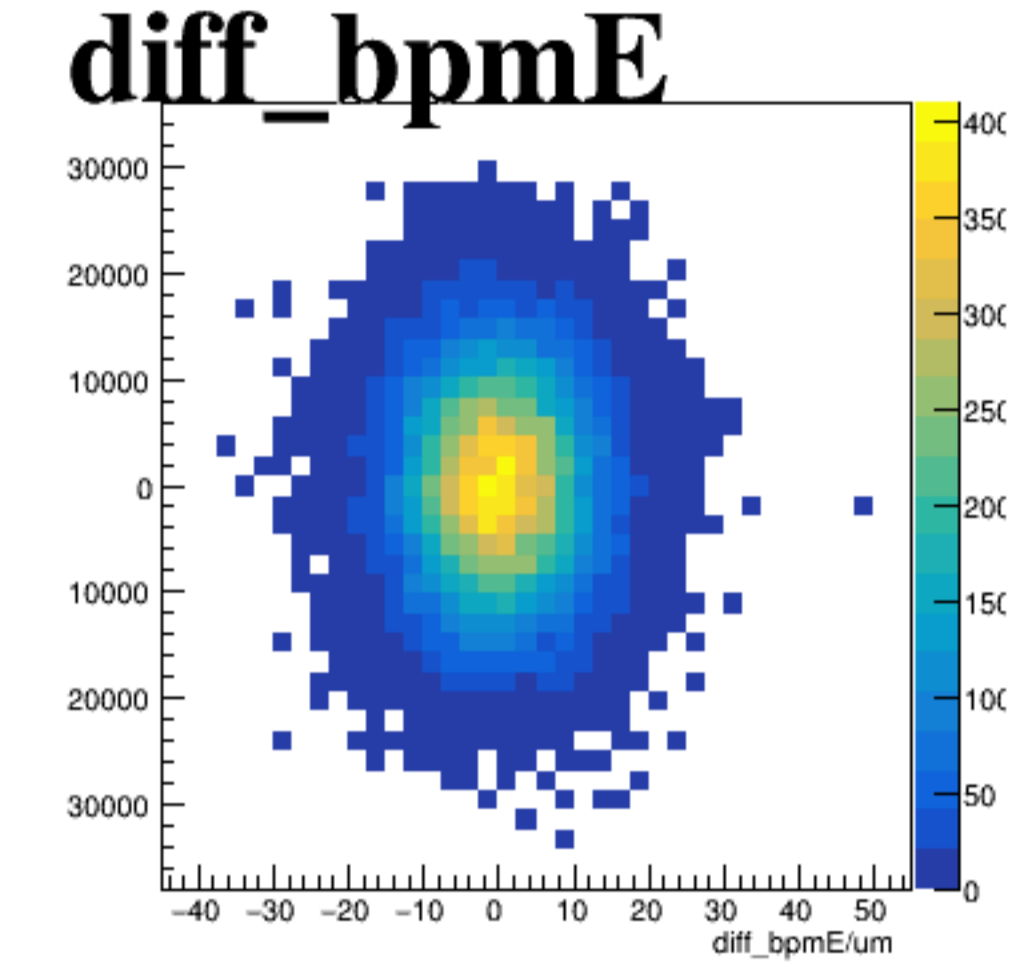
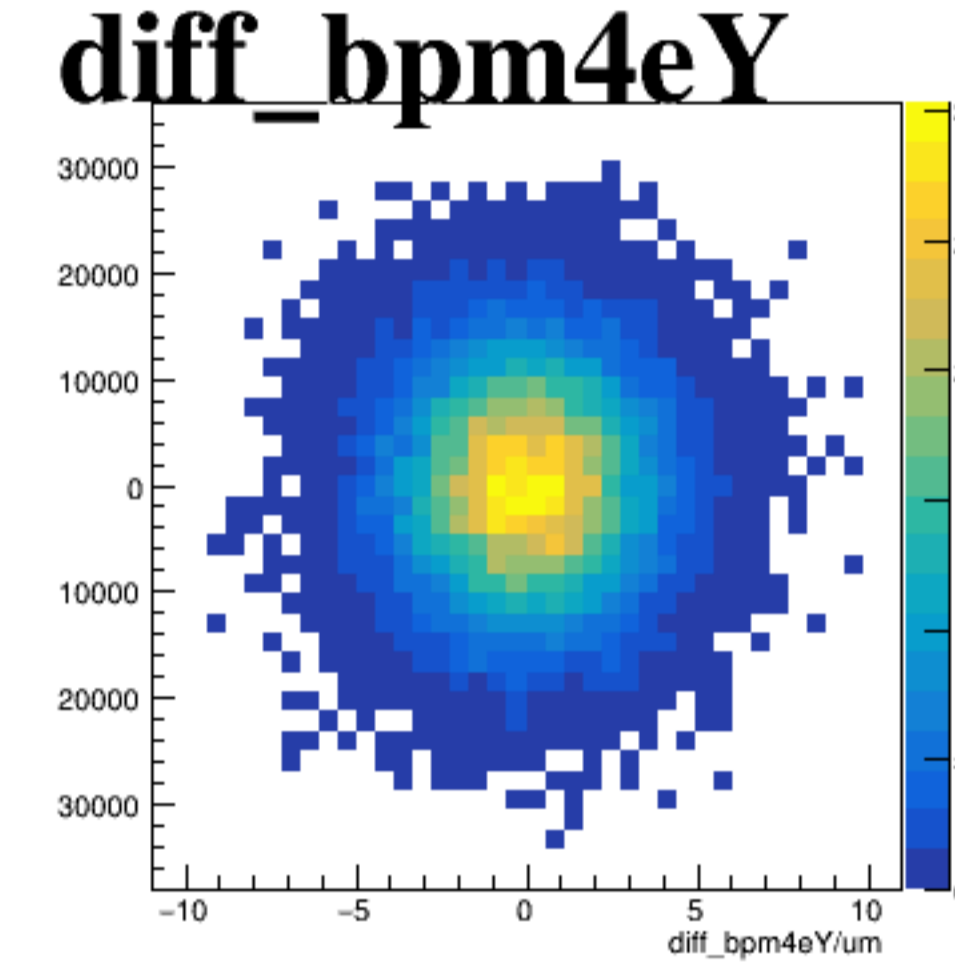
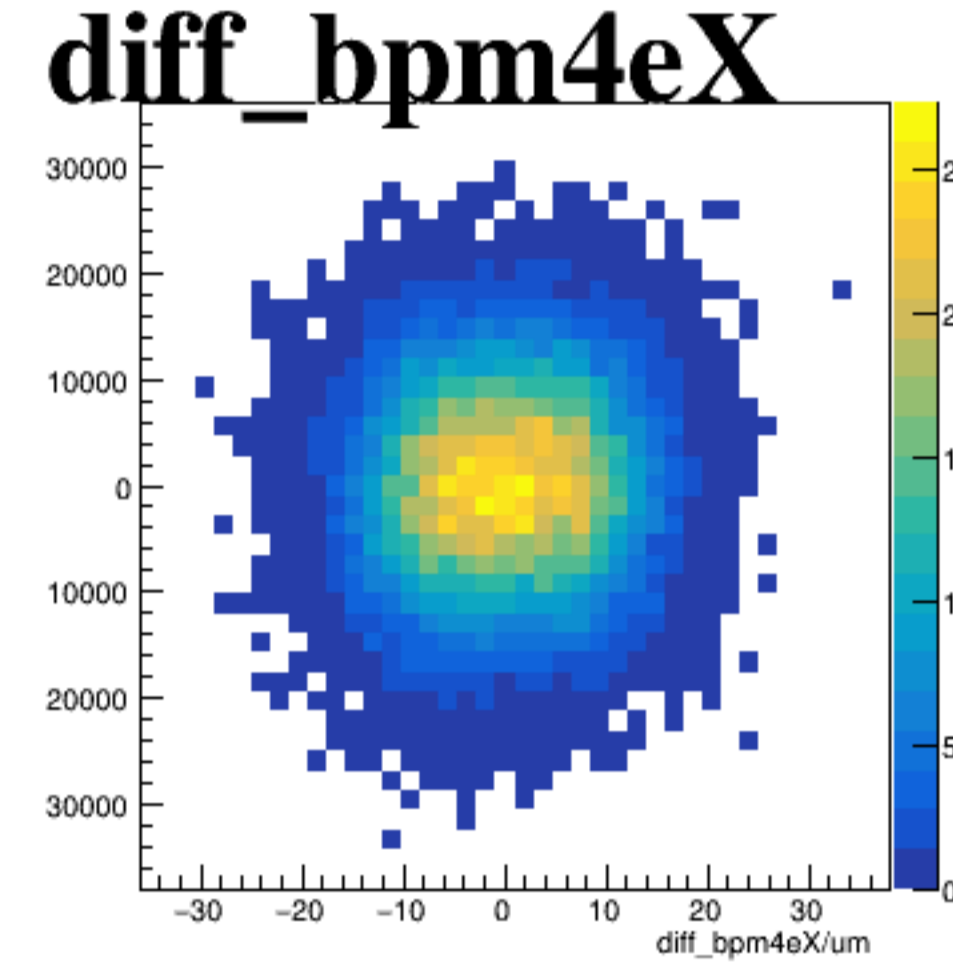
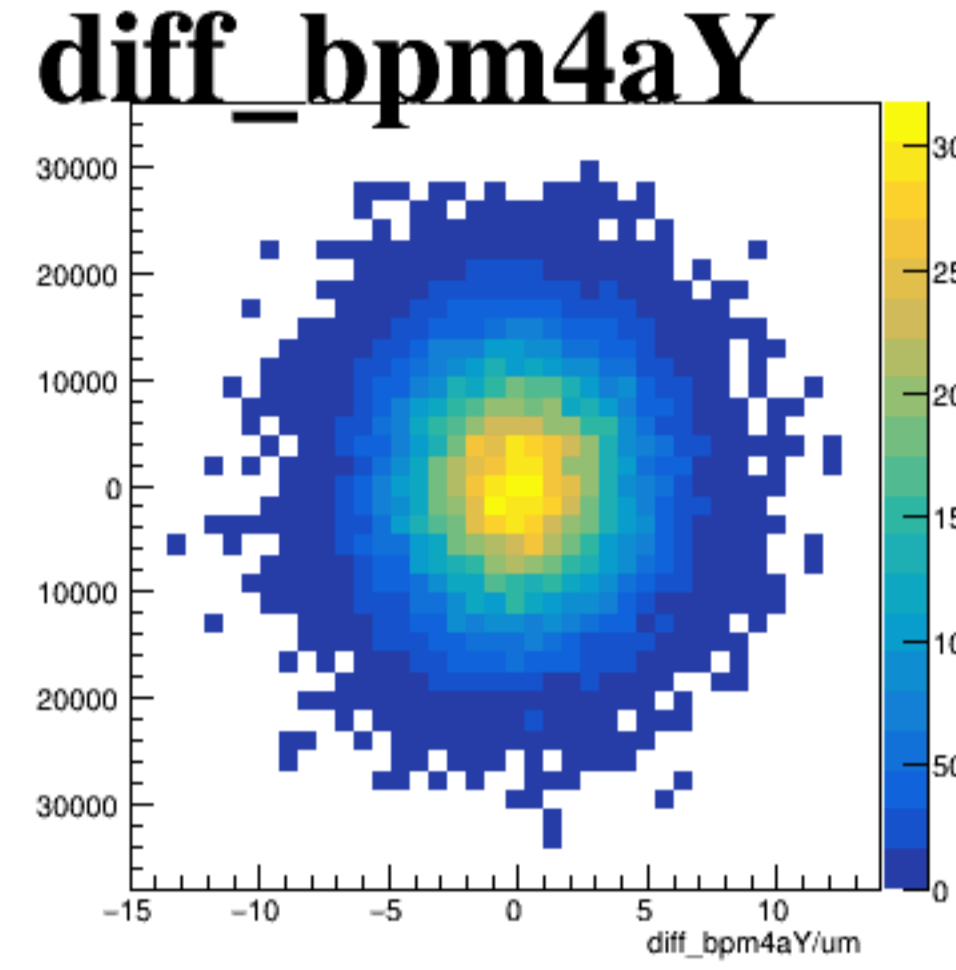
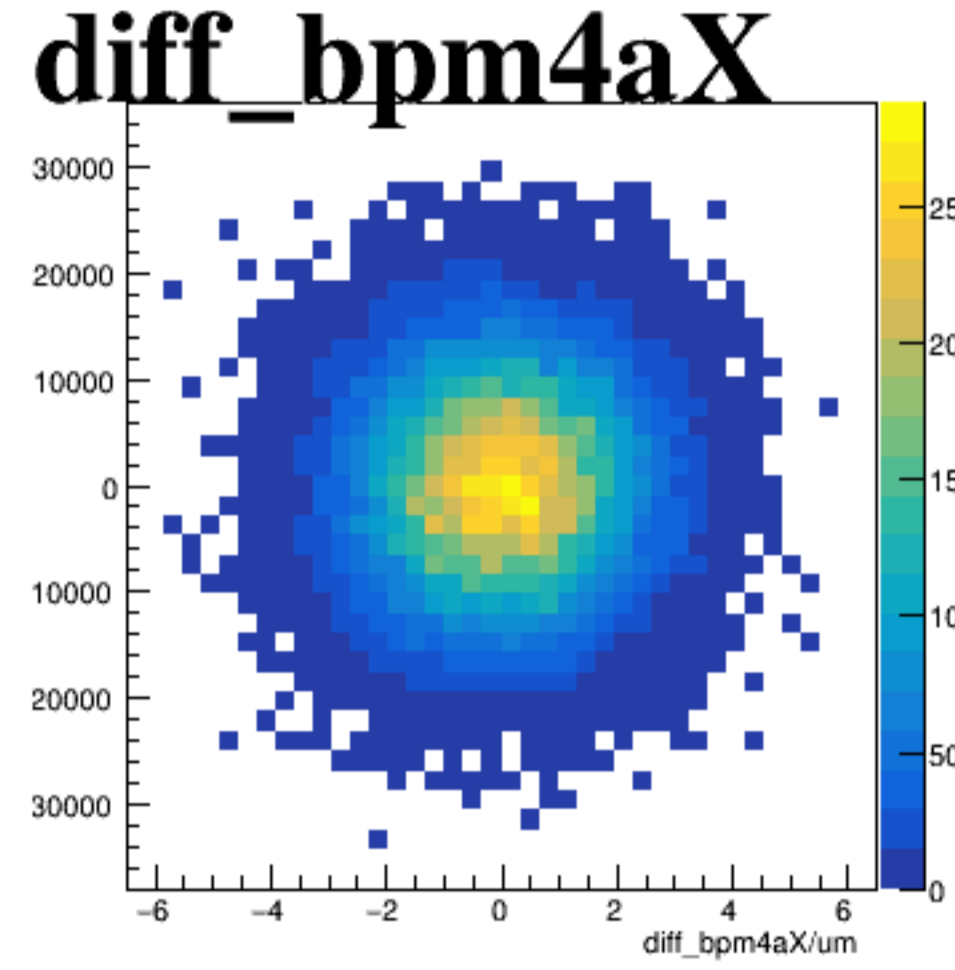


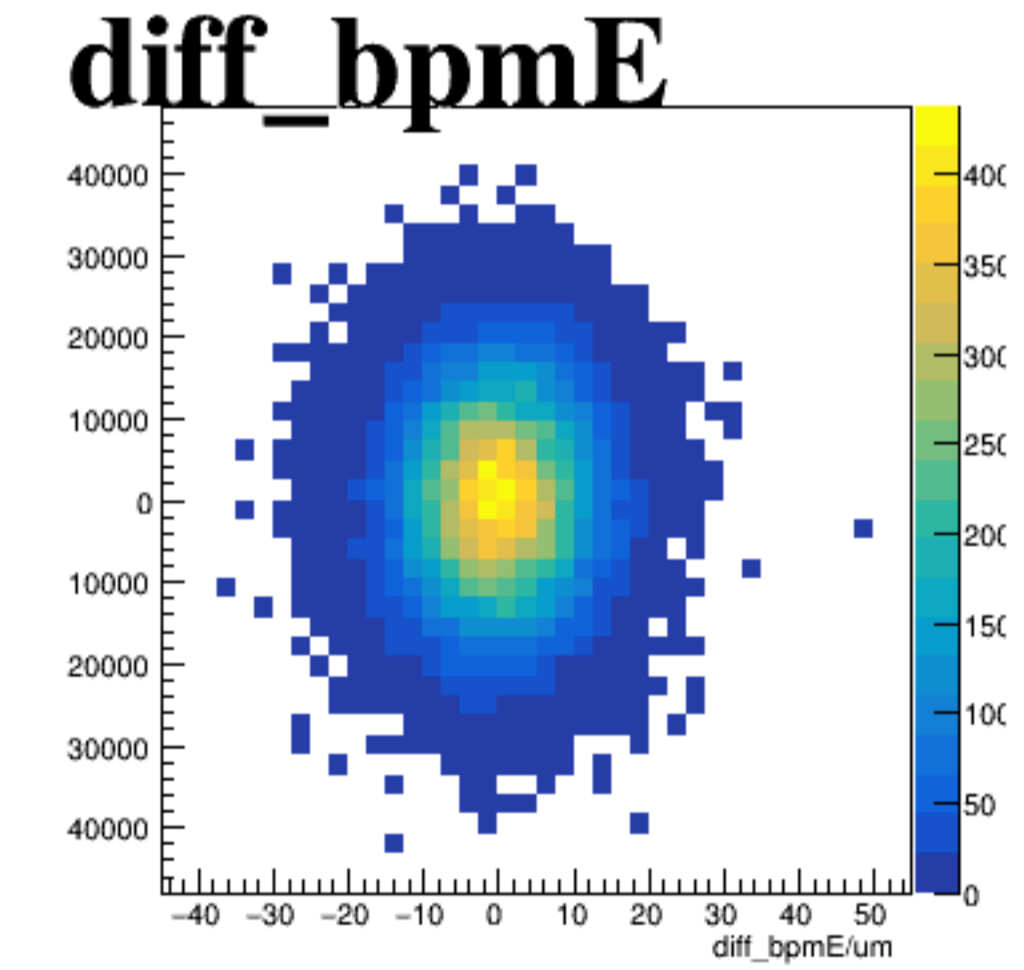
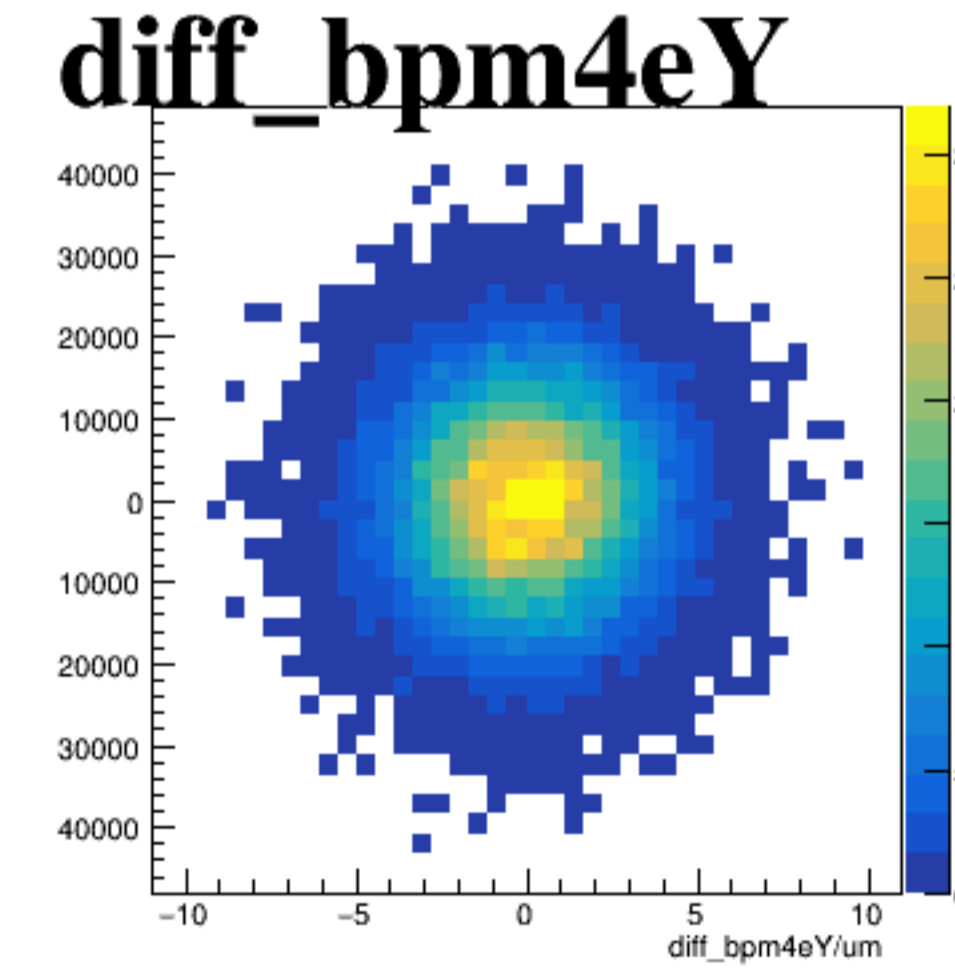
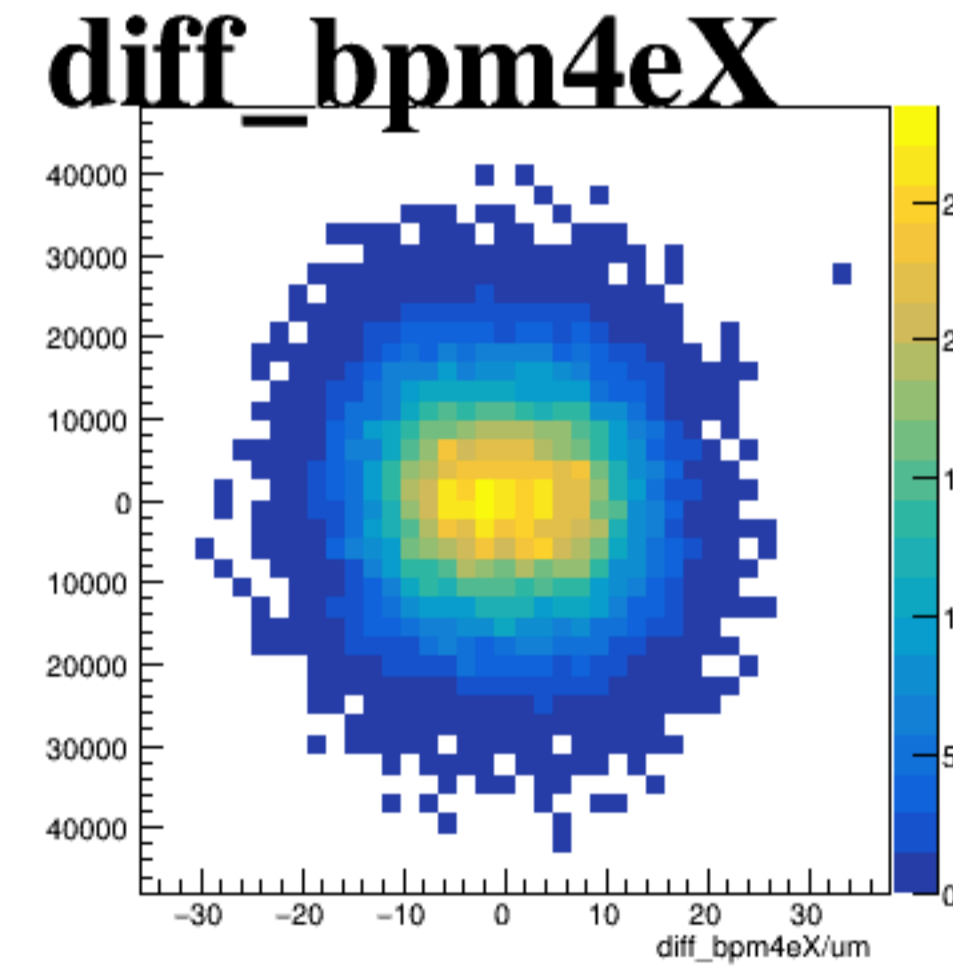
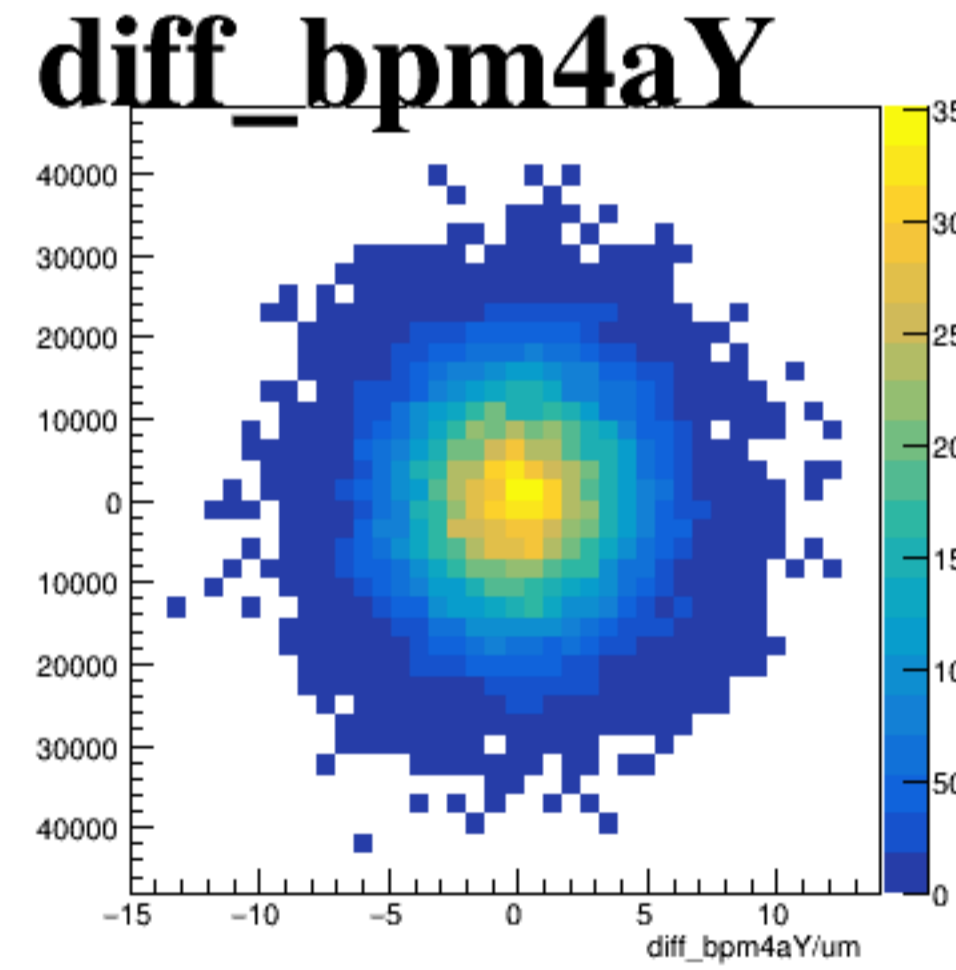
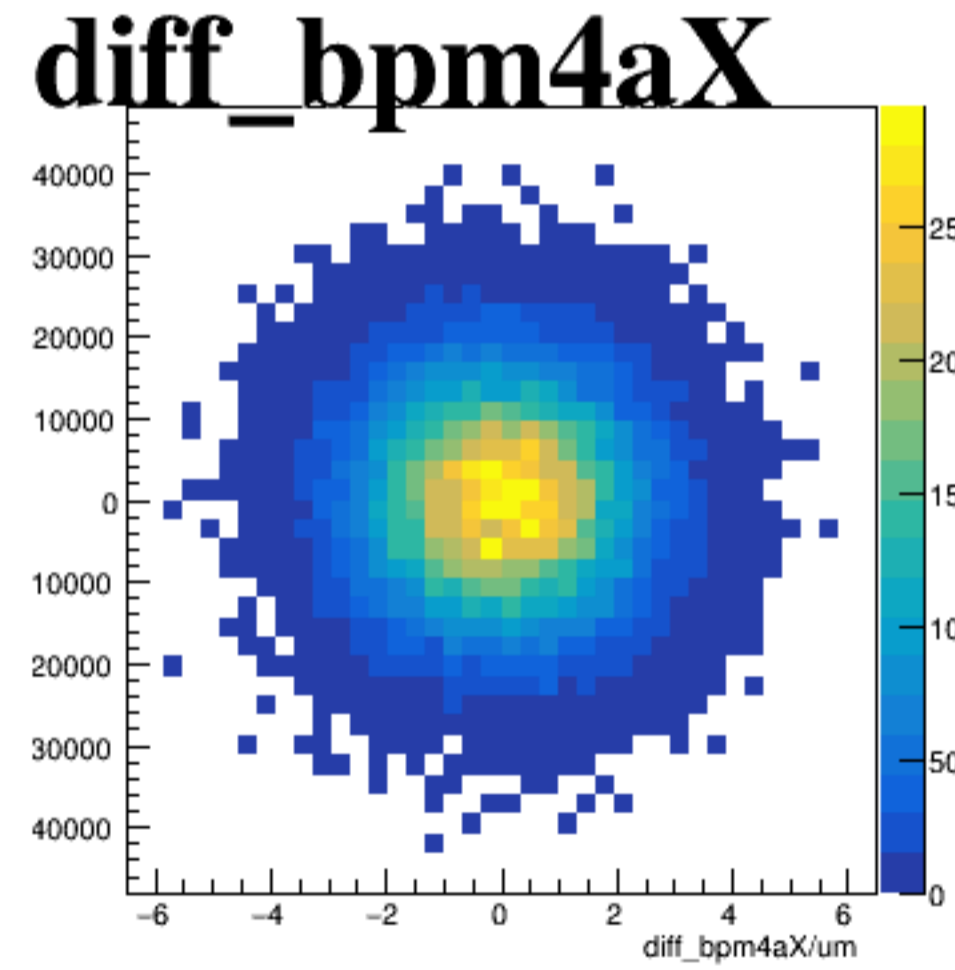
asym_atl1



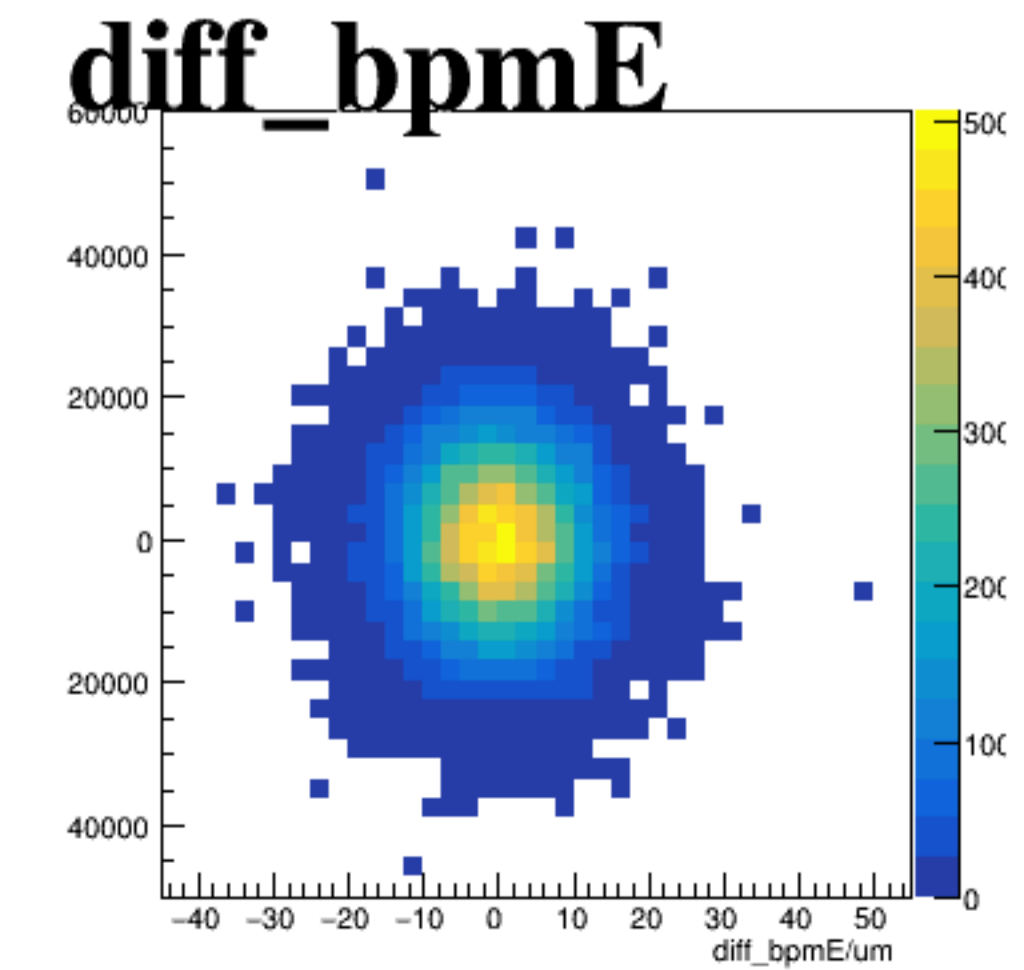
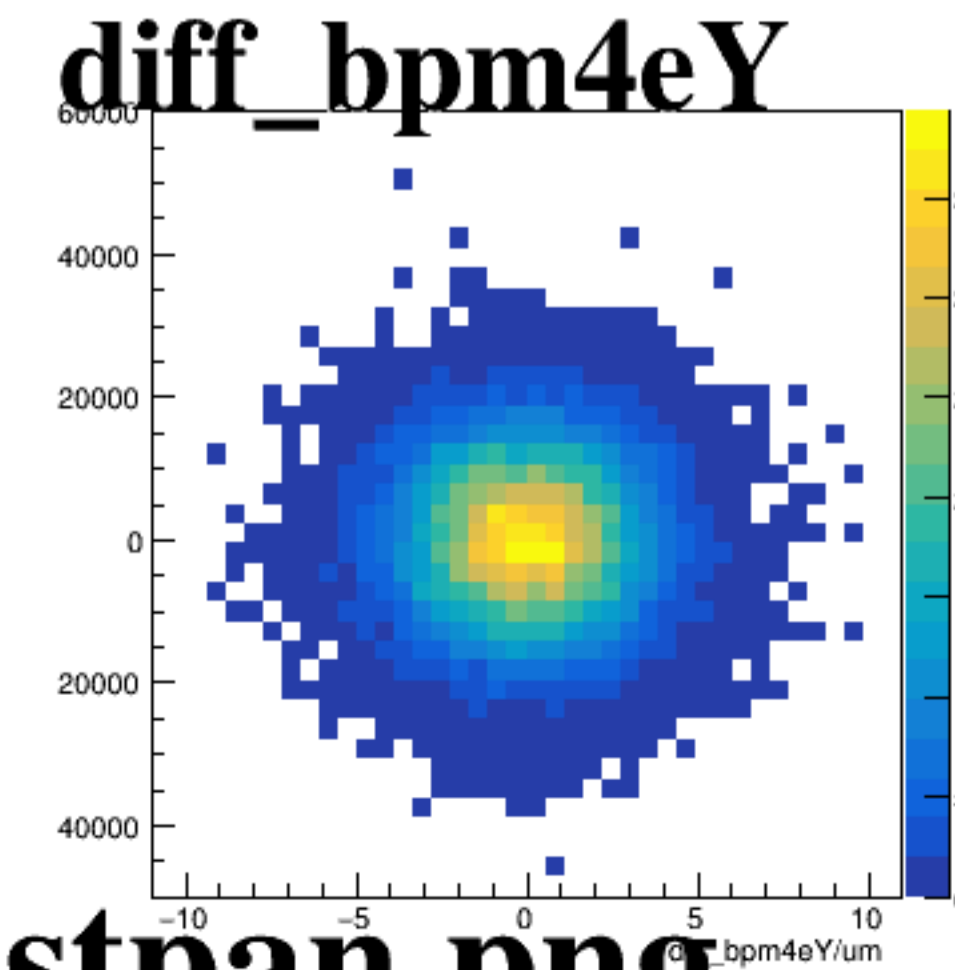
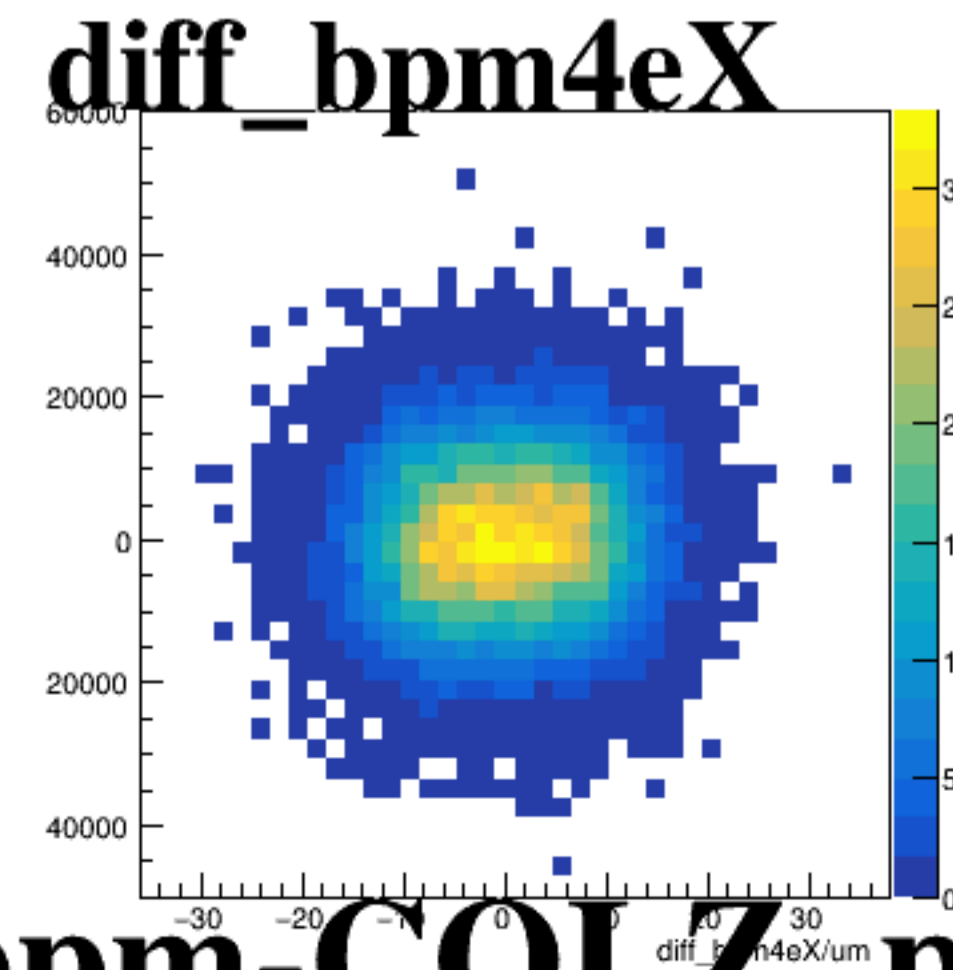
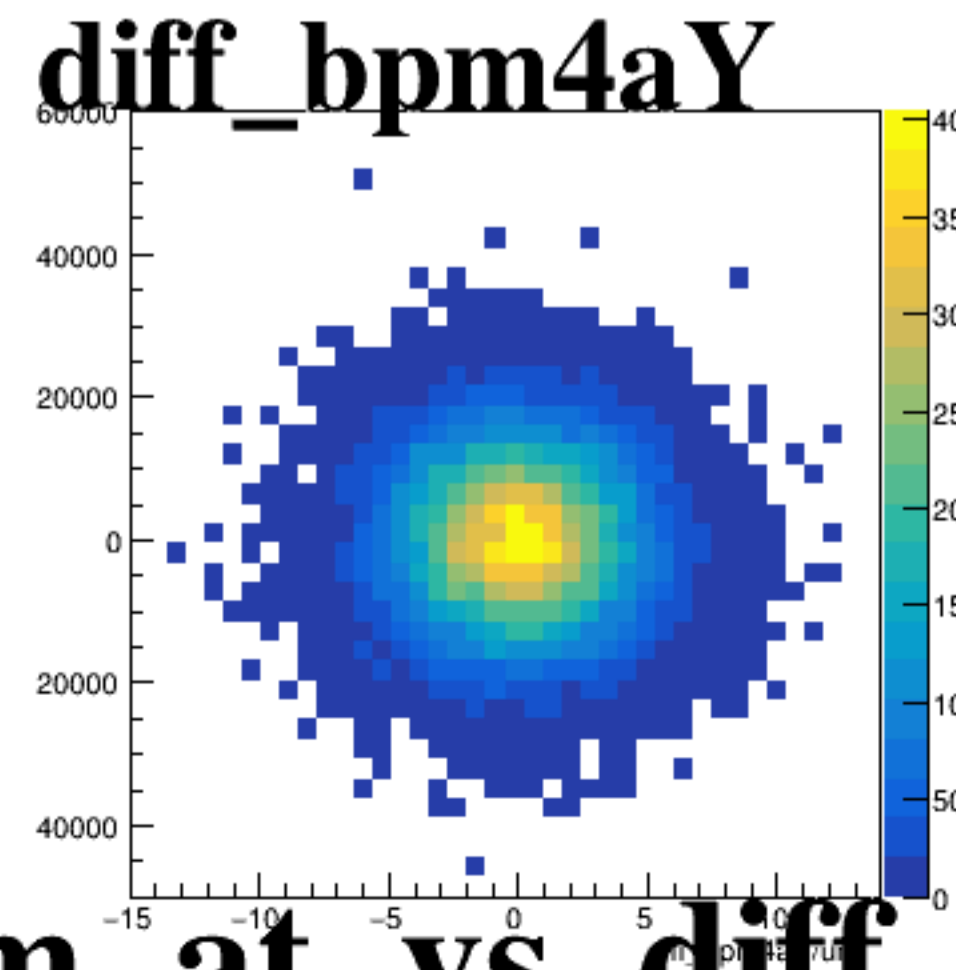
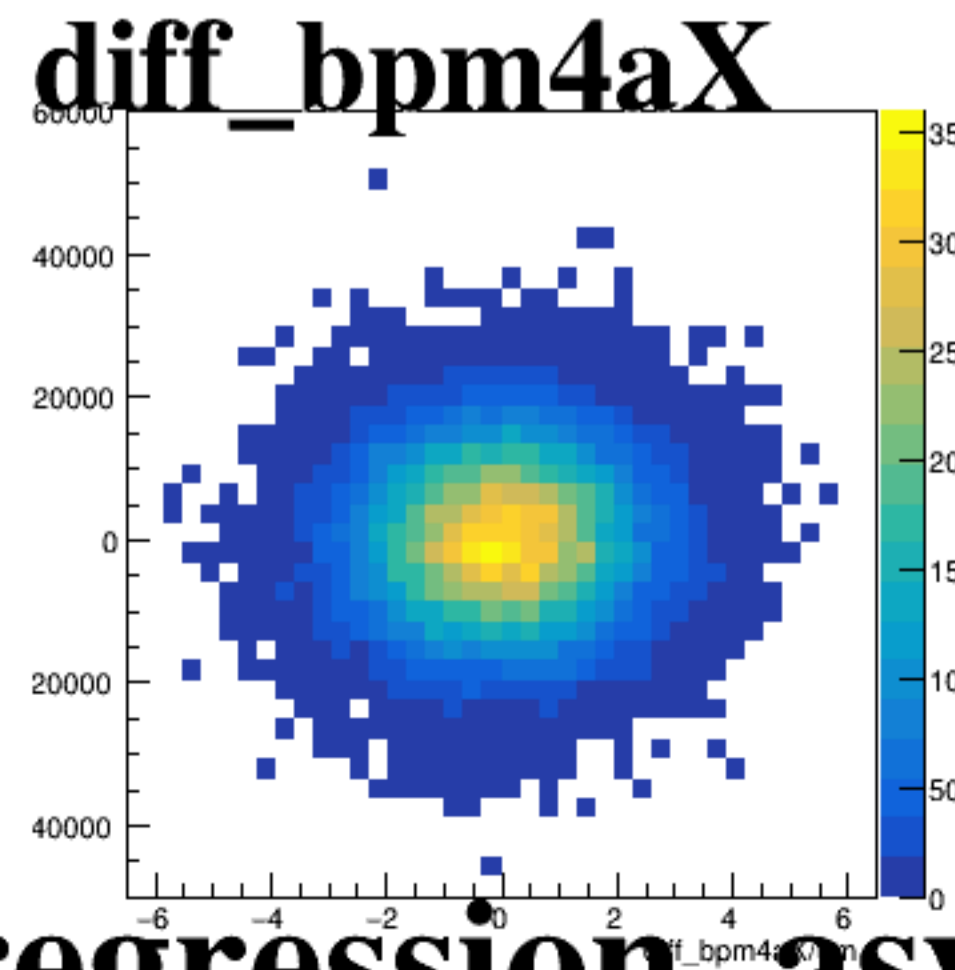
asym_atr1



asym_atl2

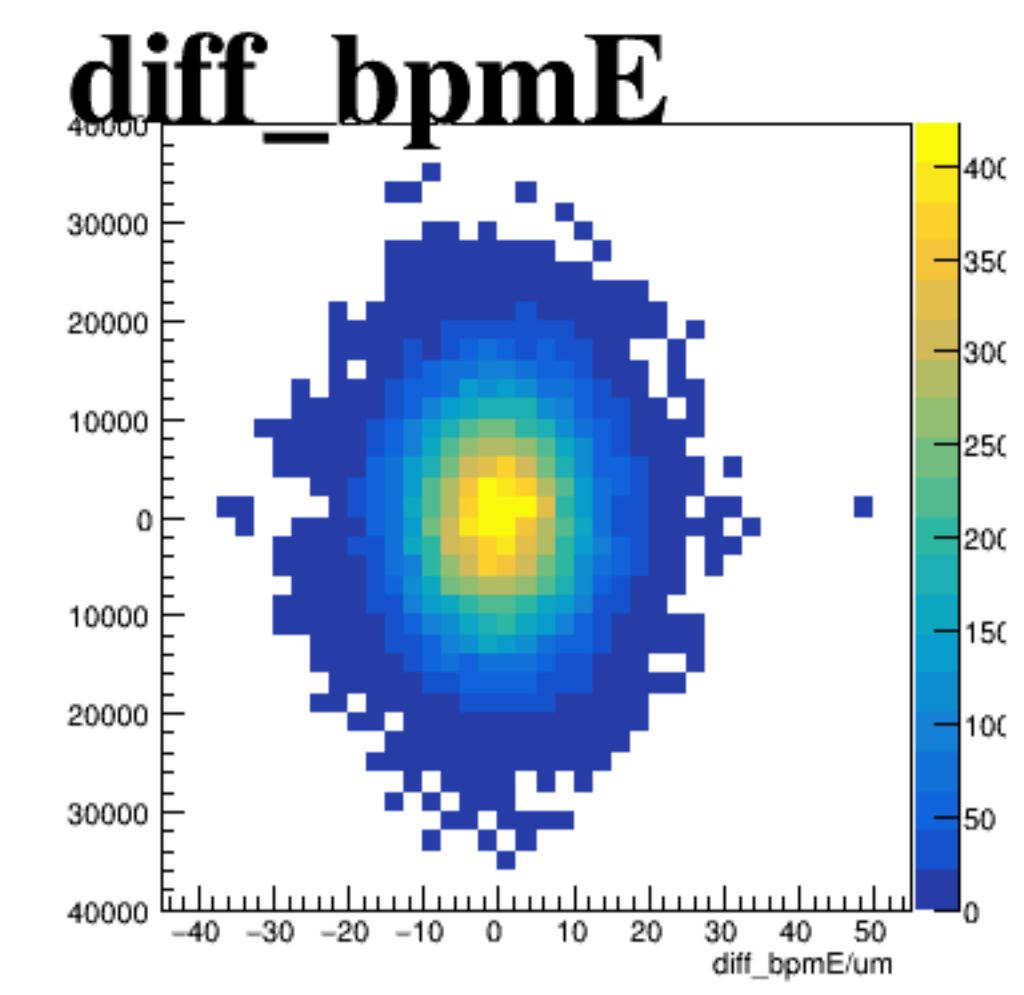
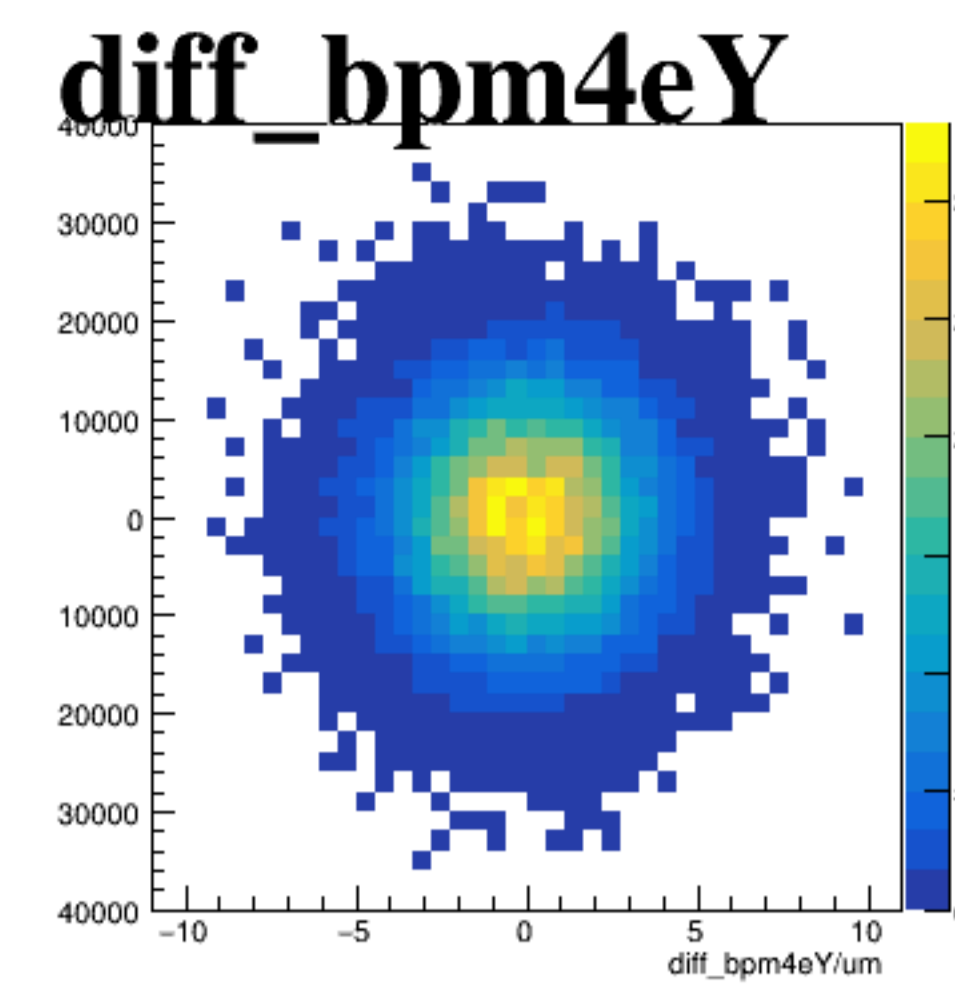
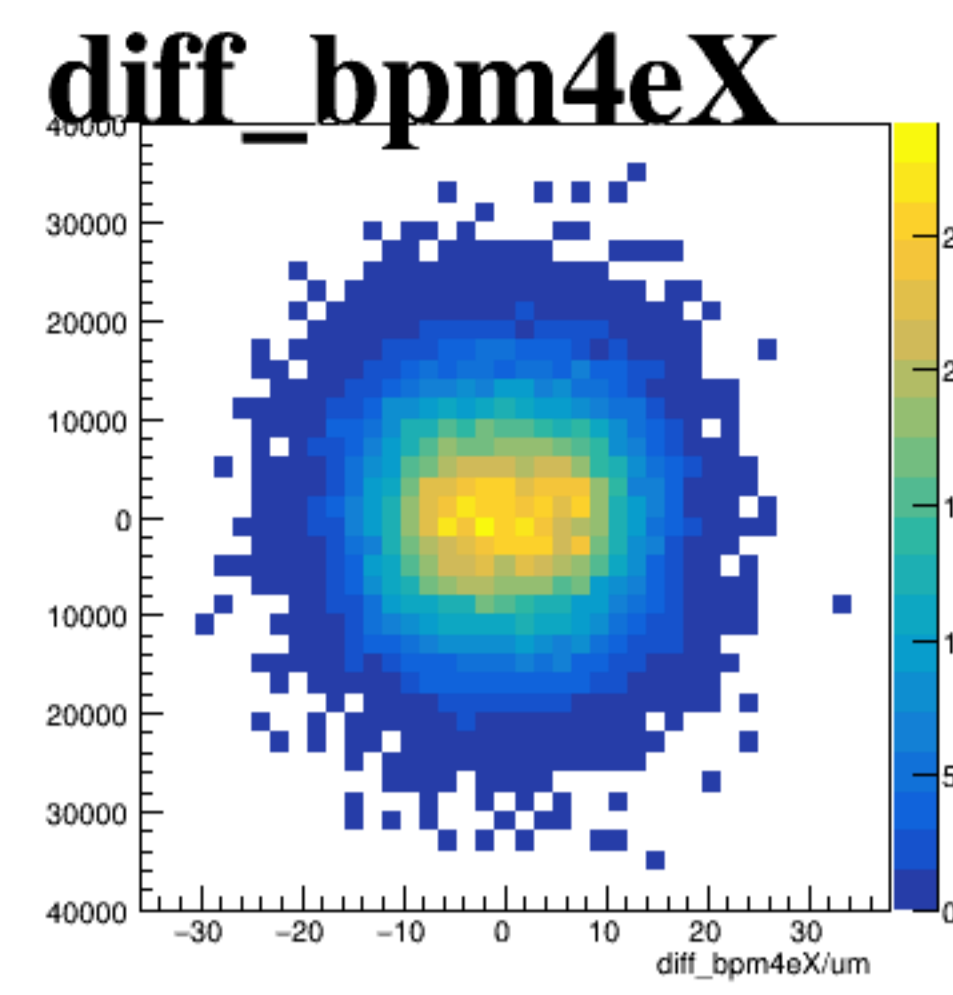
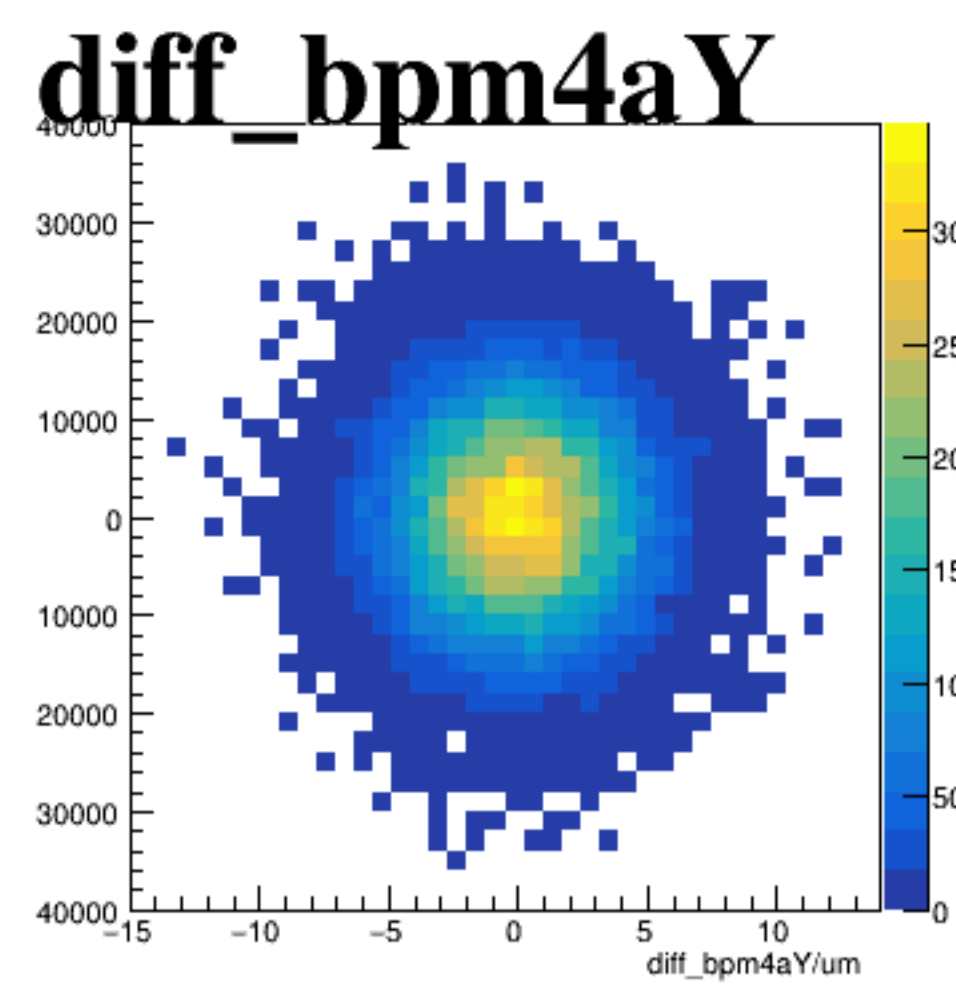
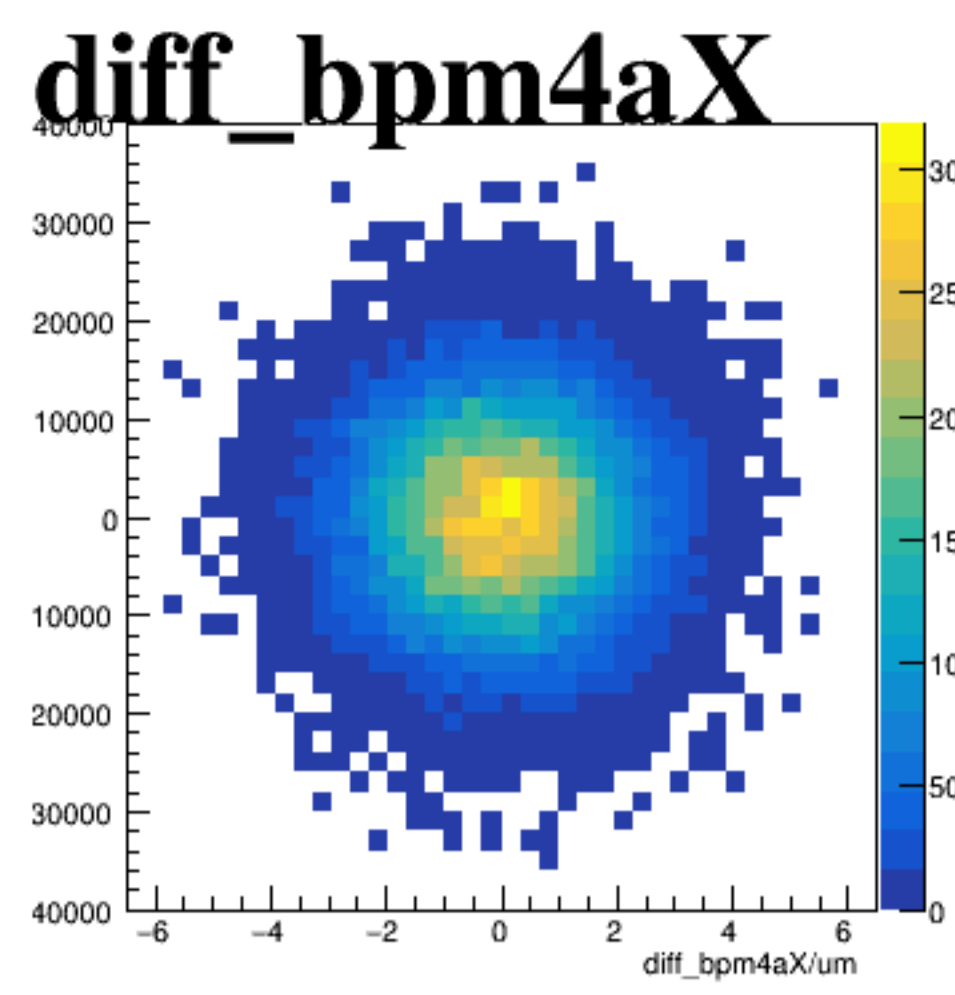


asym_atr2

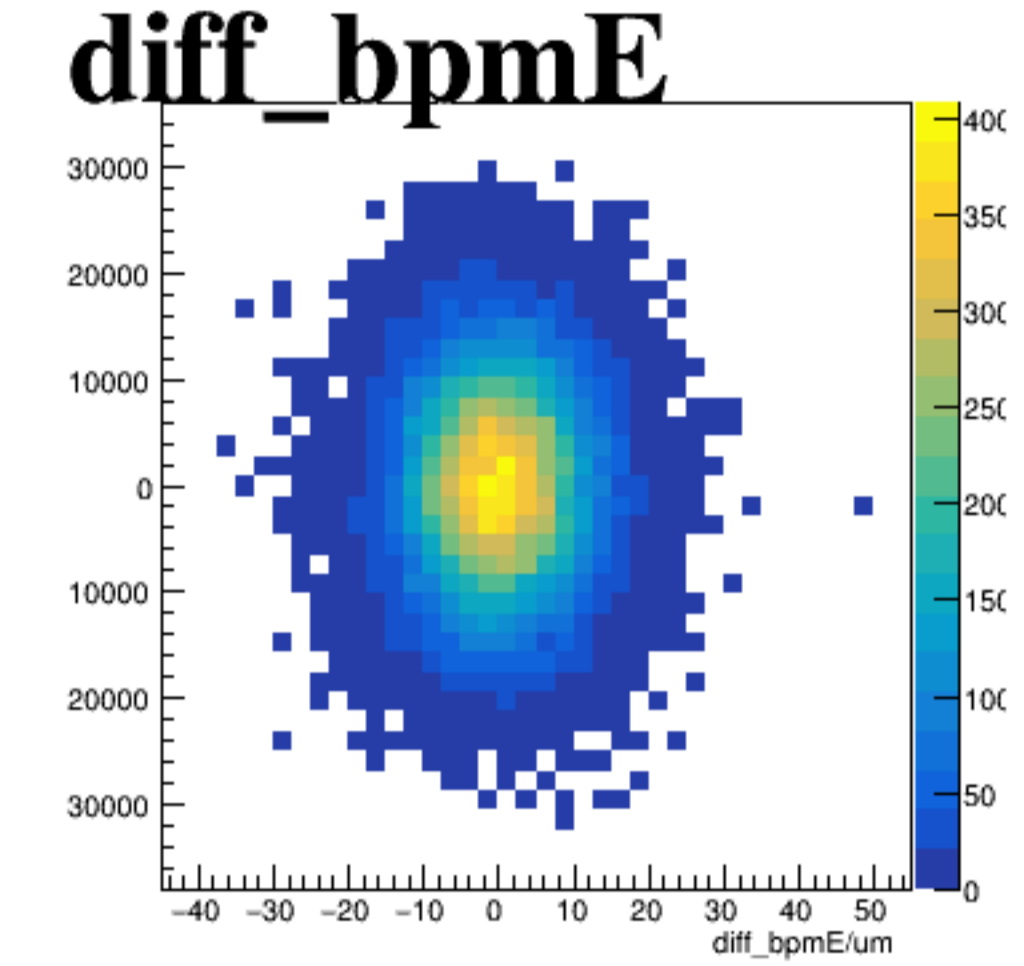
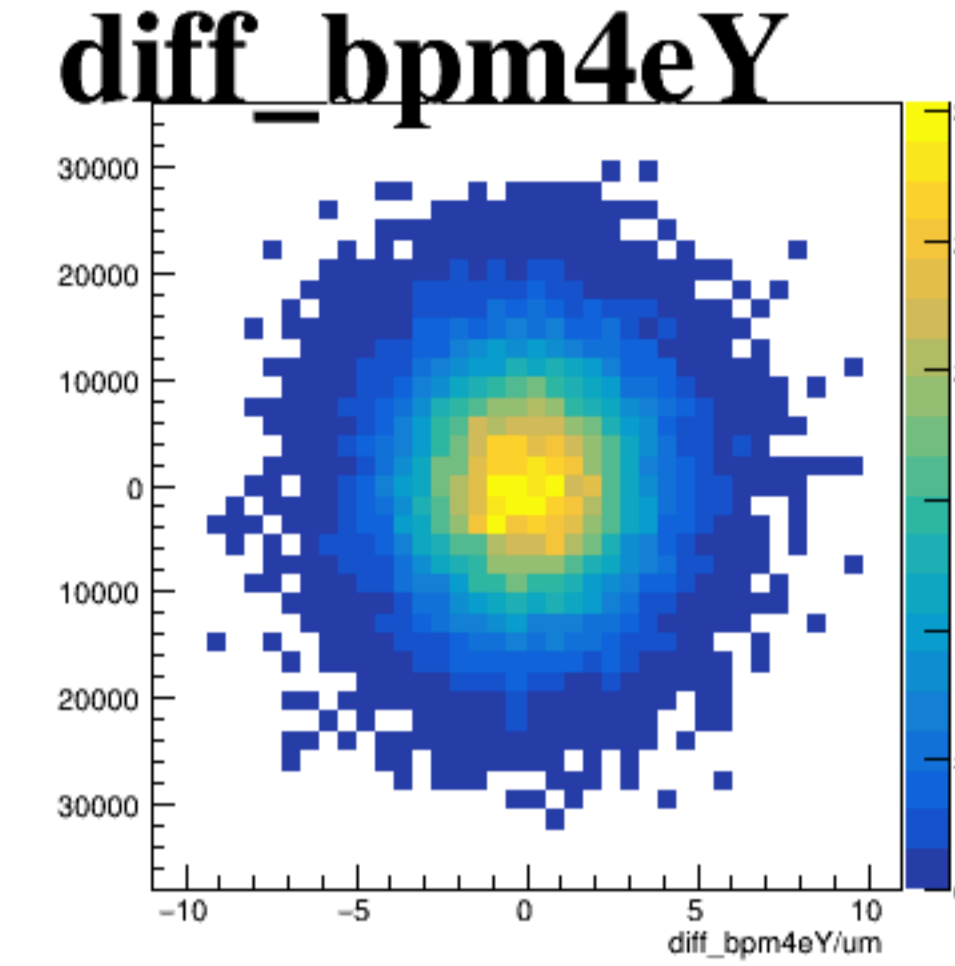
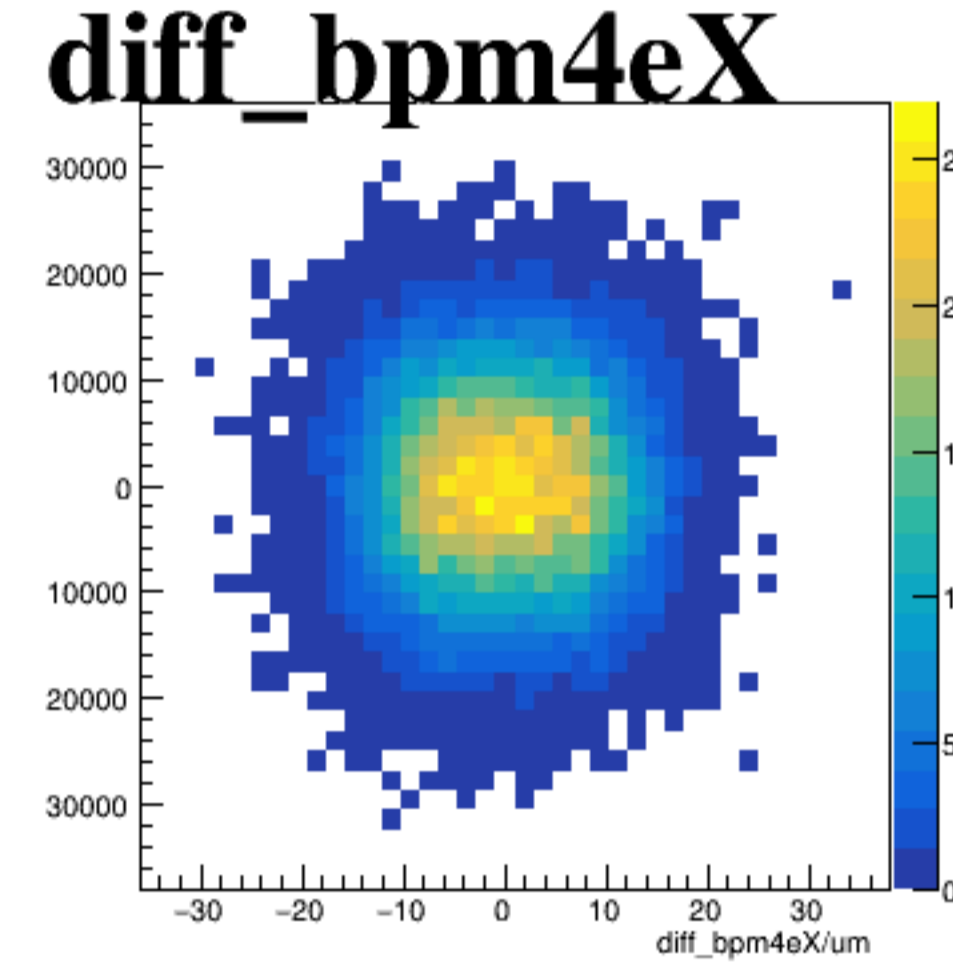
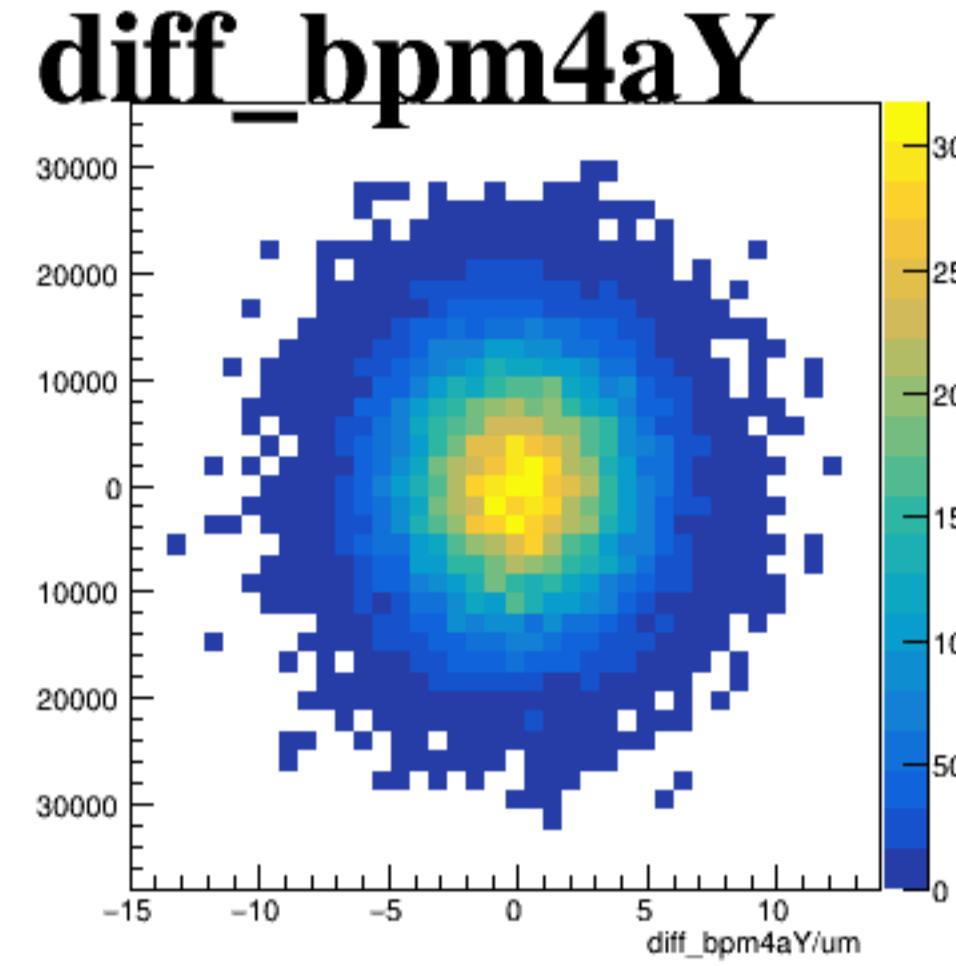
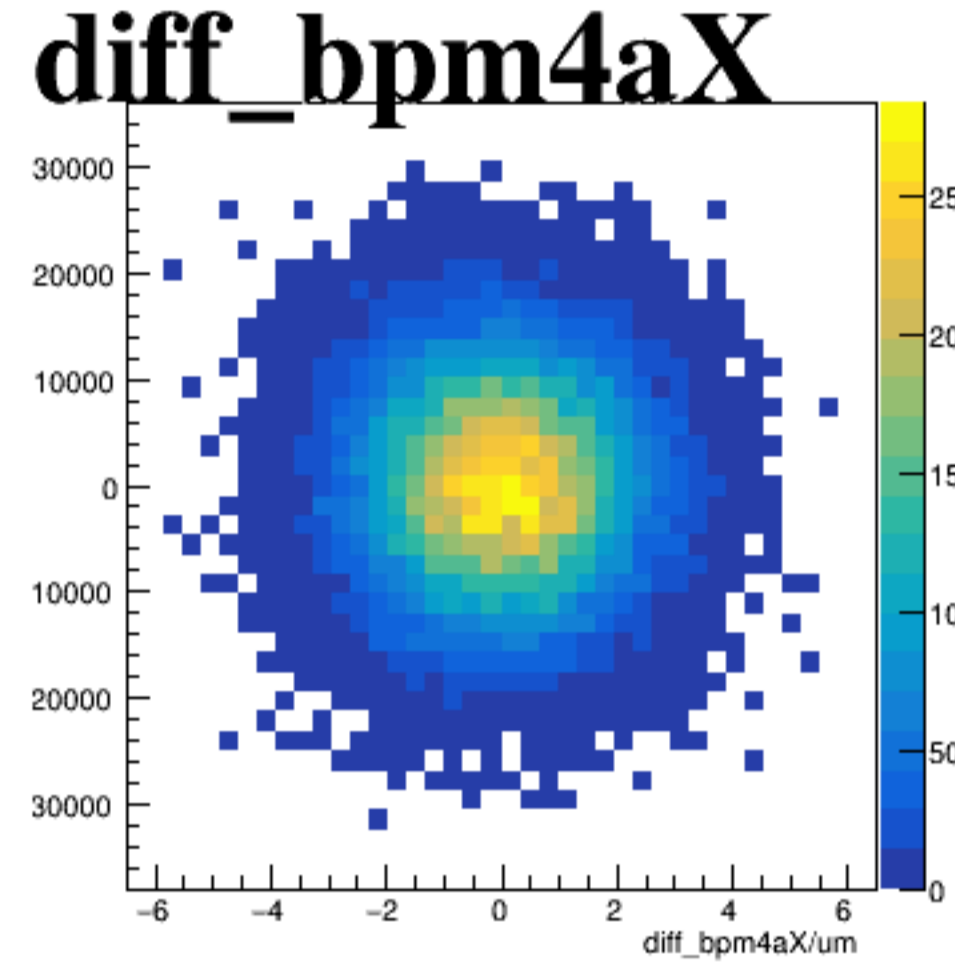


run4816_000_regression_asym_at vs diff bpm-COLZ_postpan.png

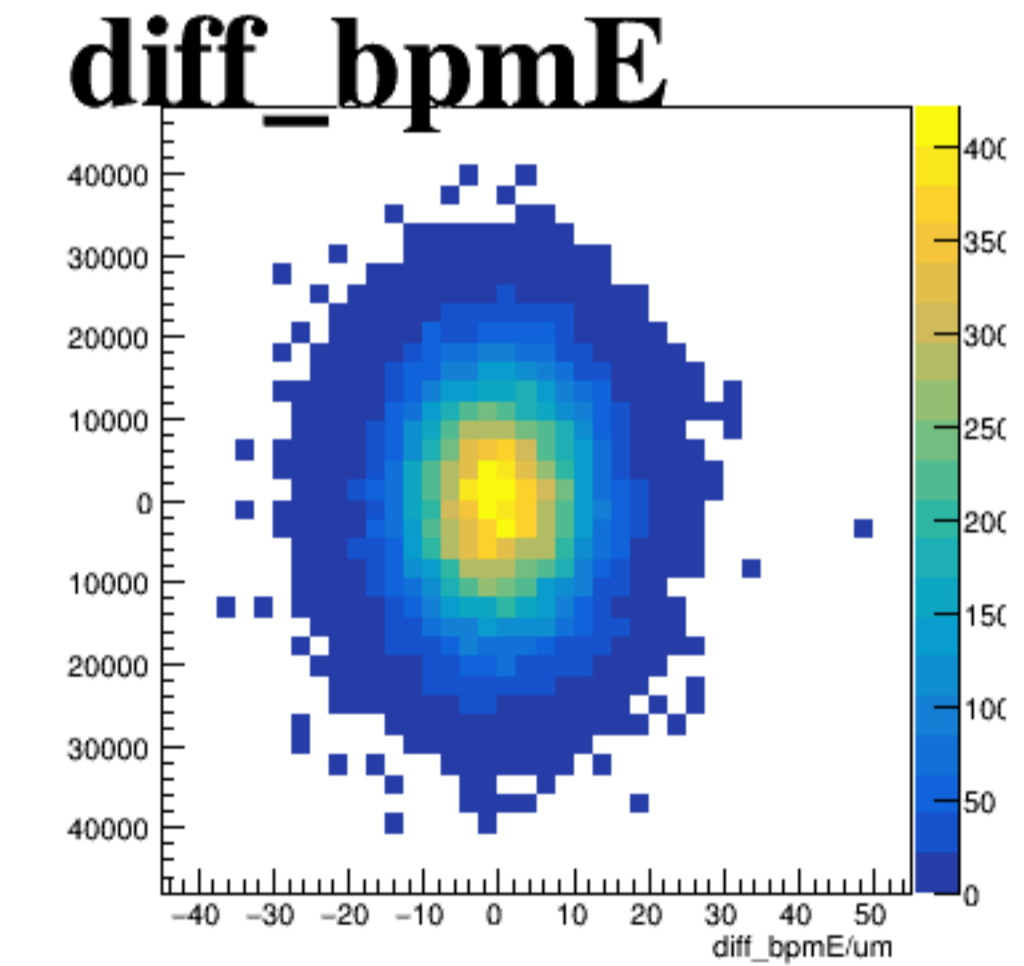
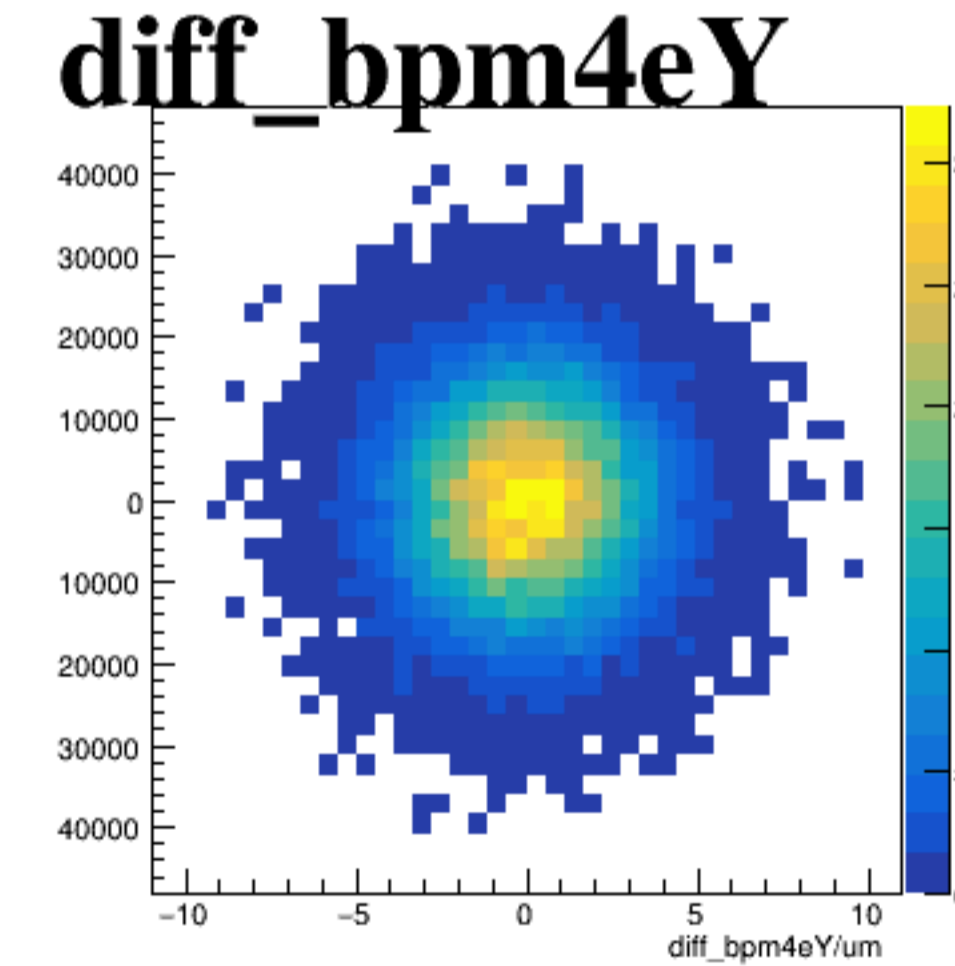
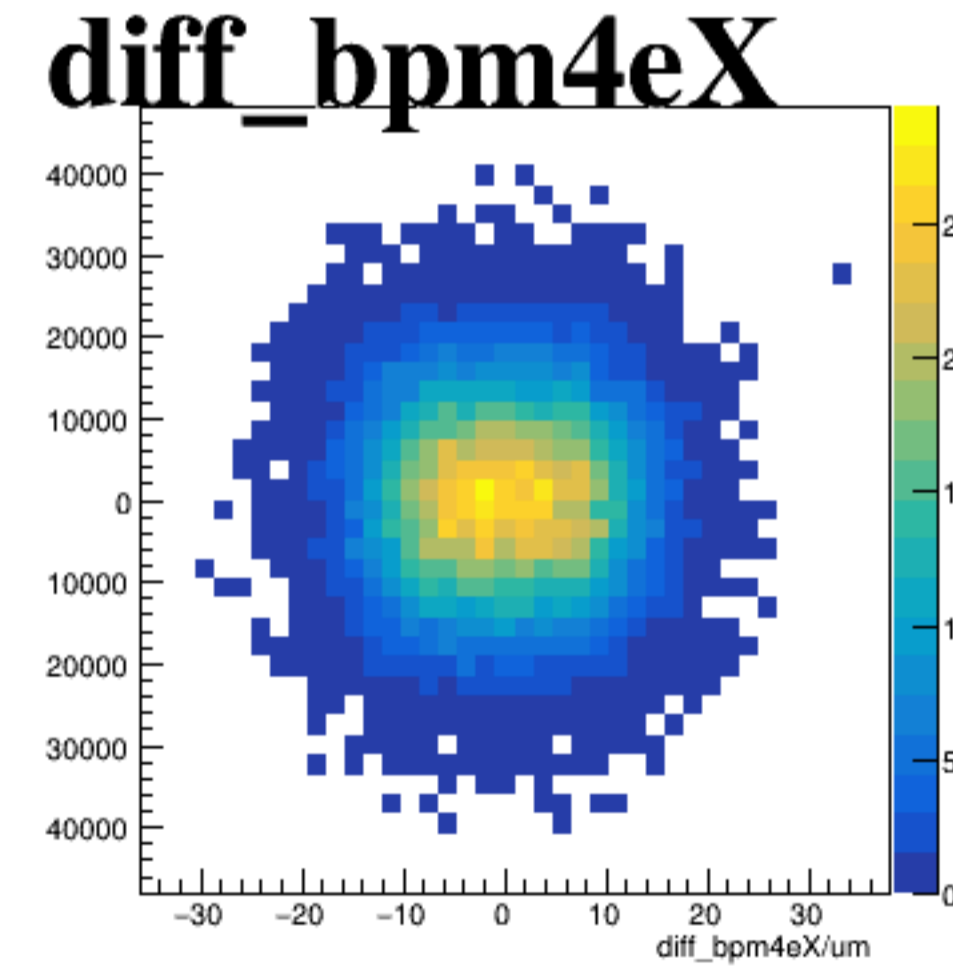
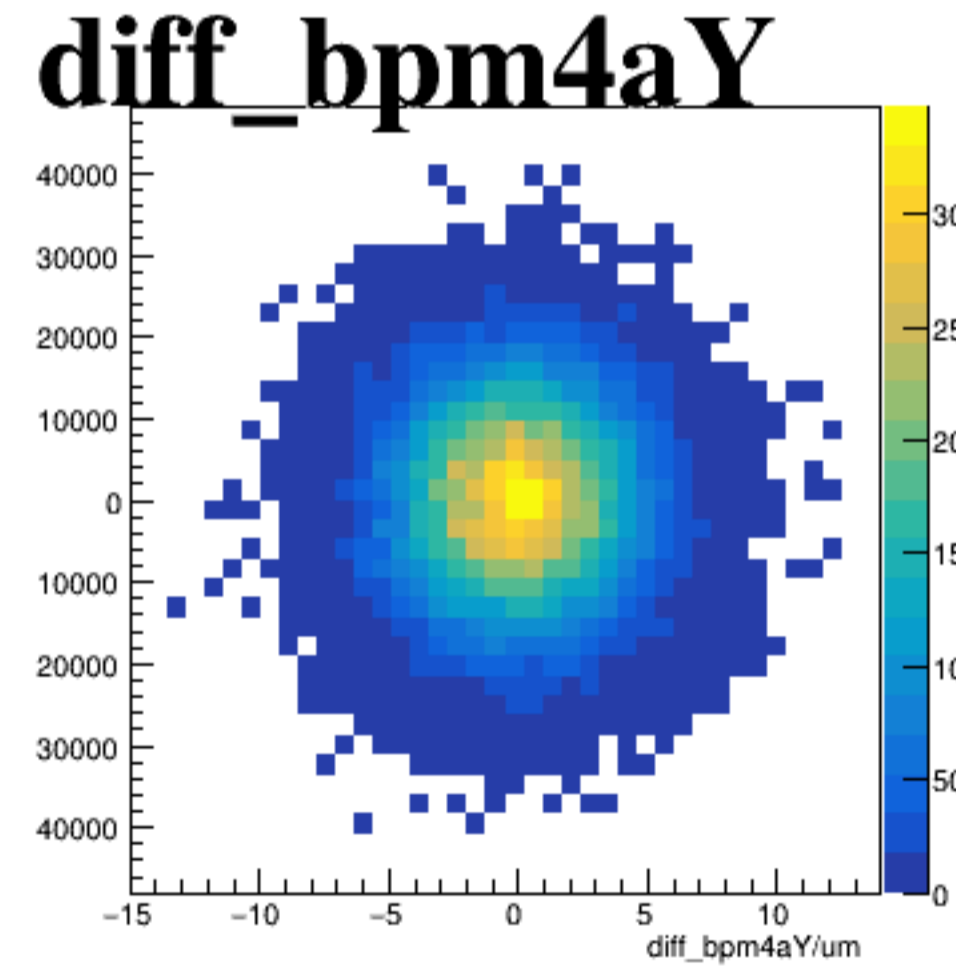
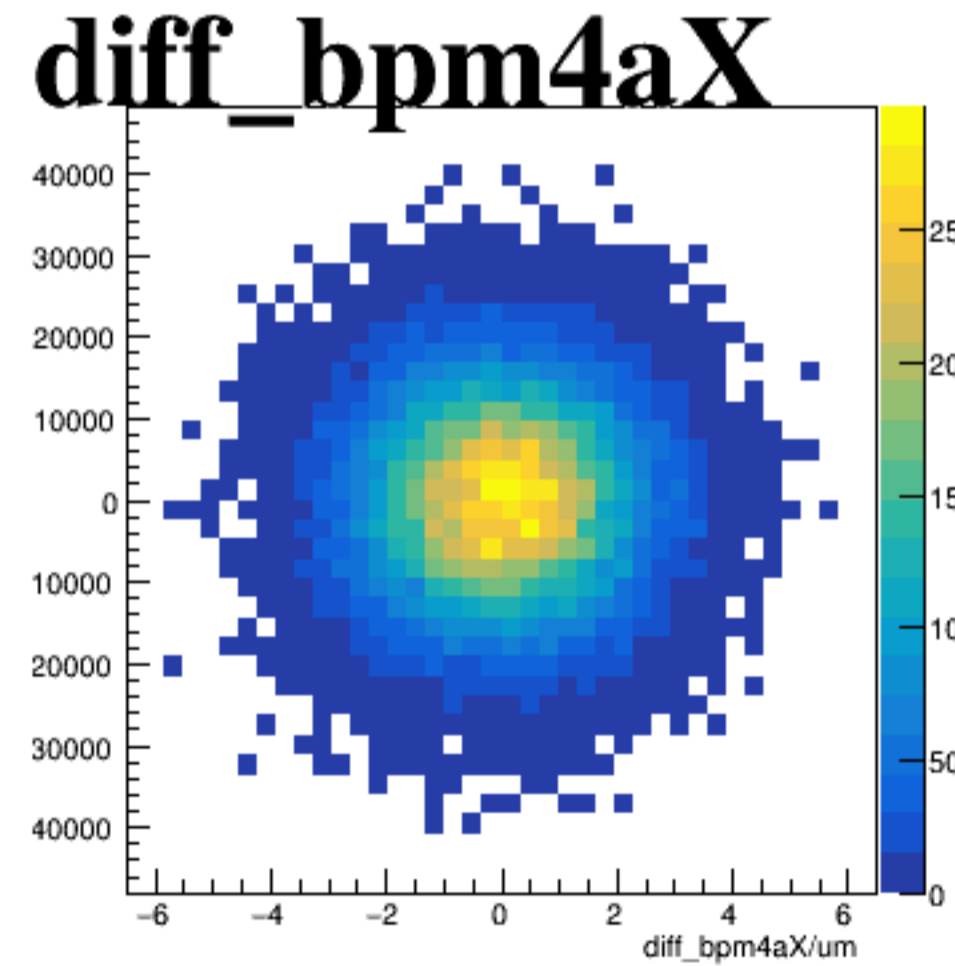
reg_asym_atl1



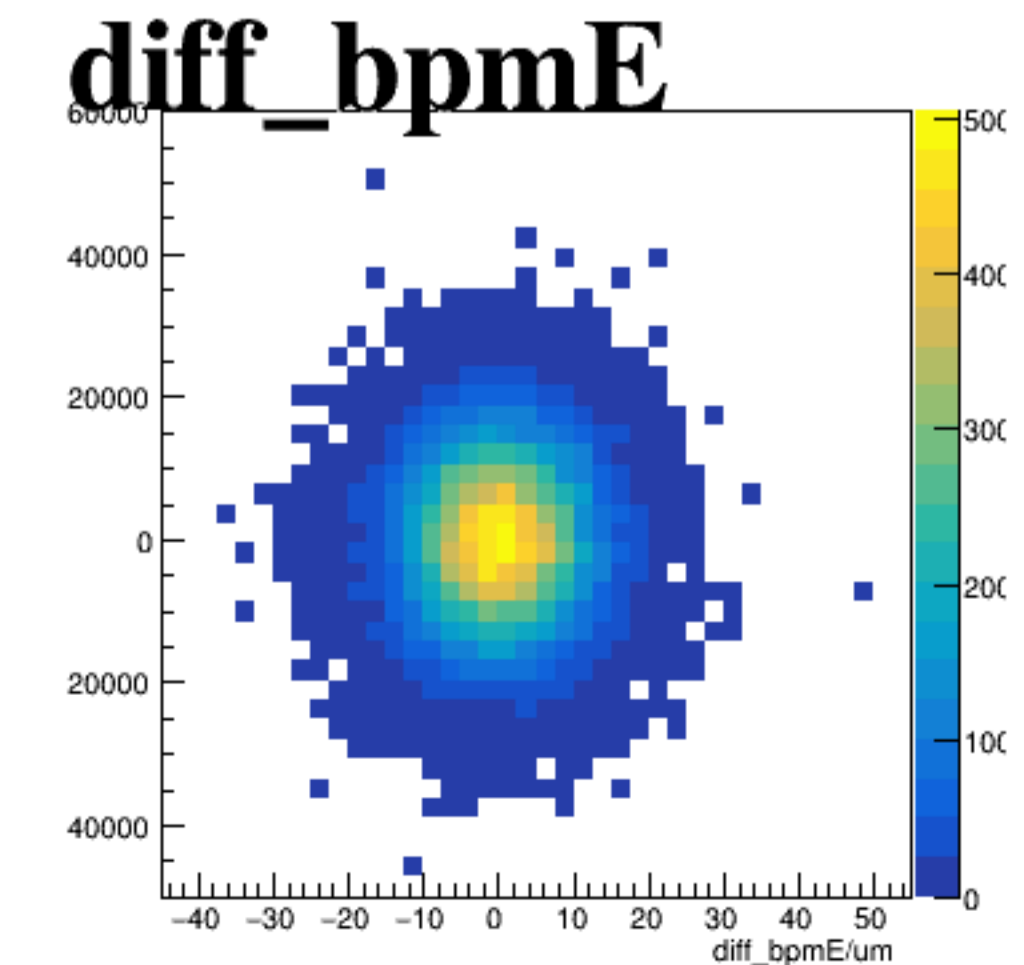
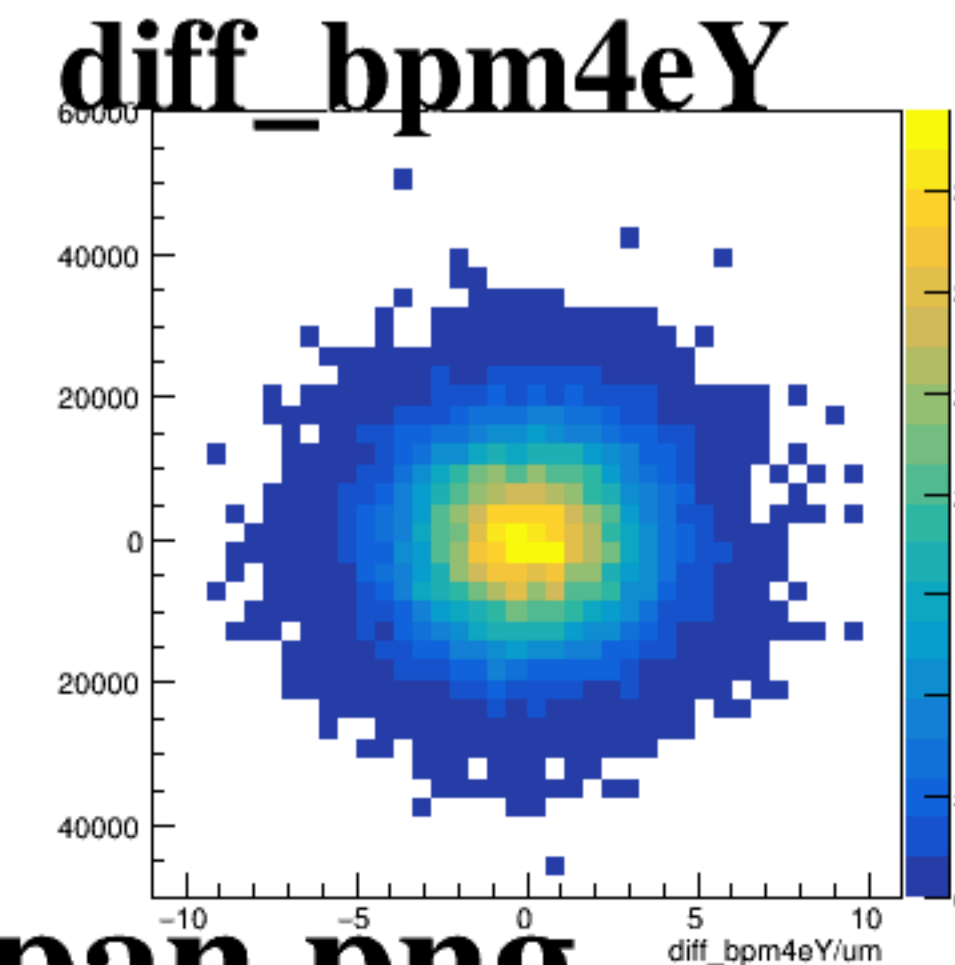
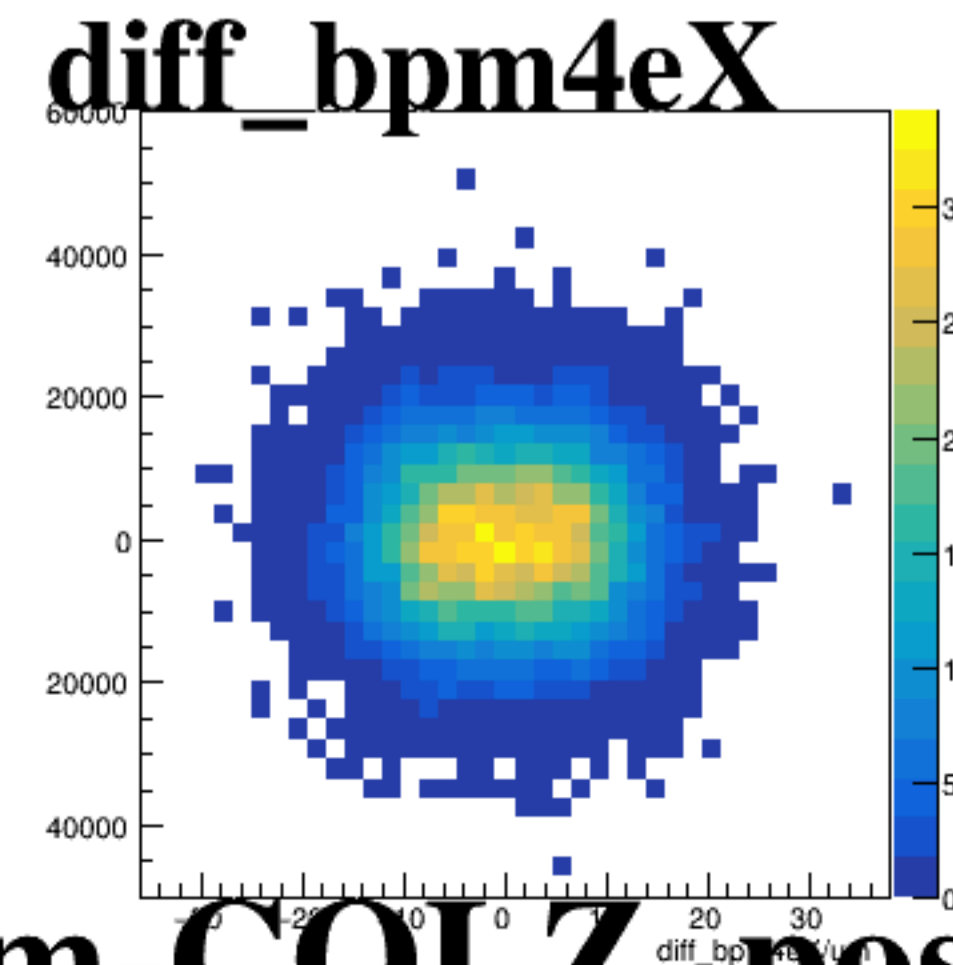
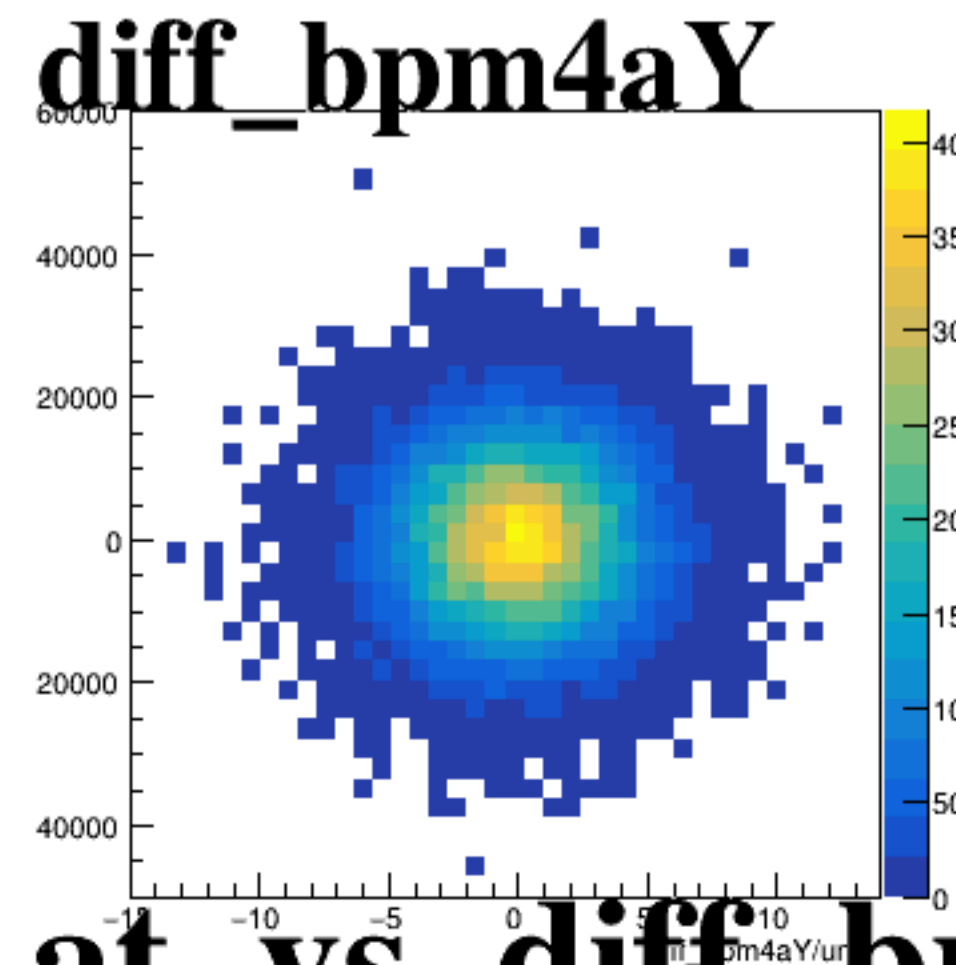
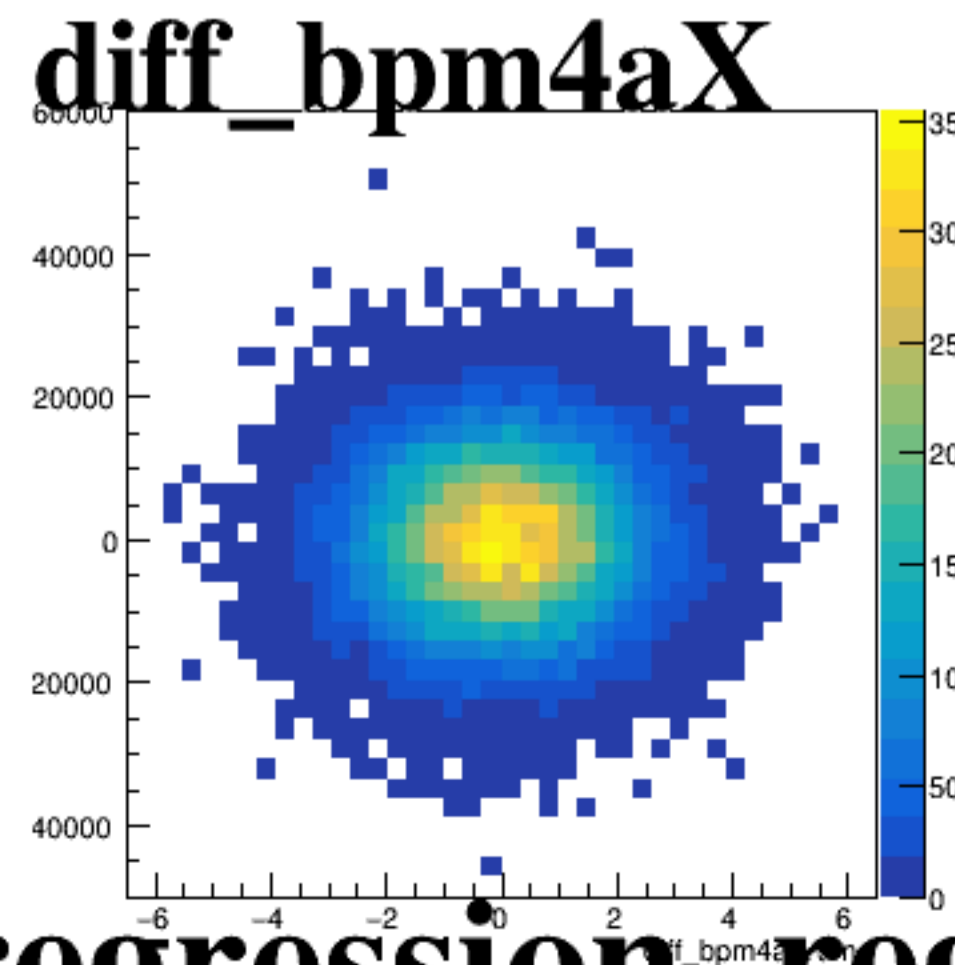
reg_asym_atr1



reg_asym_atl2

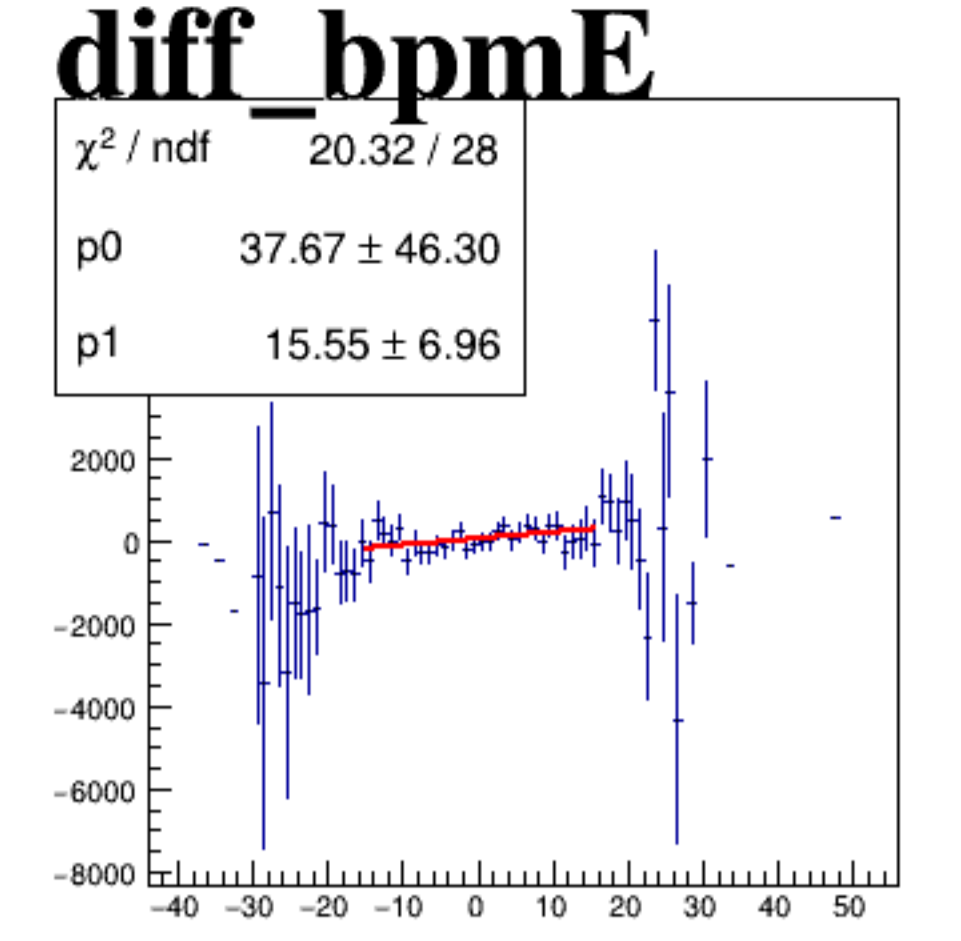
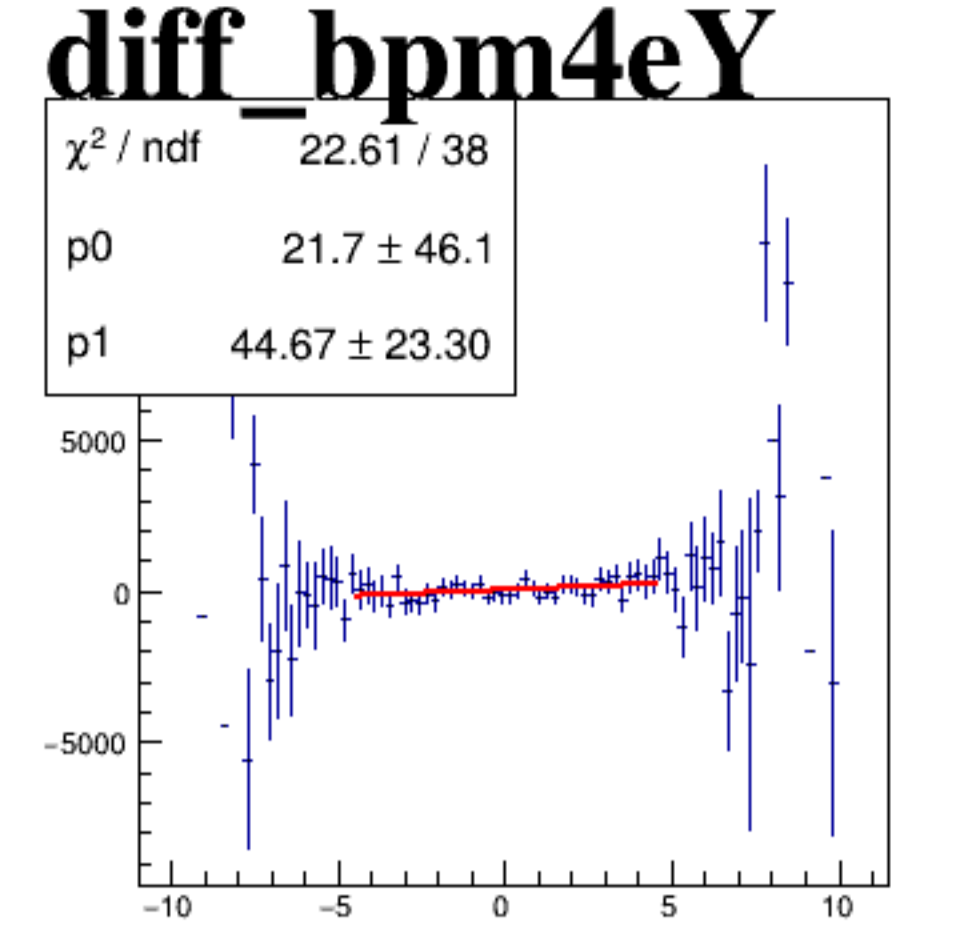
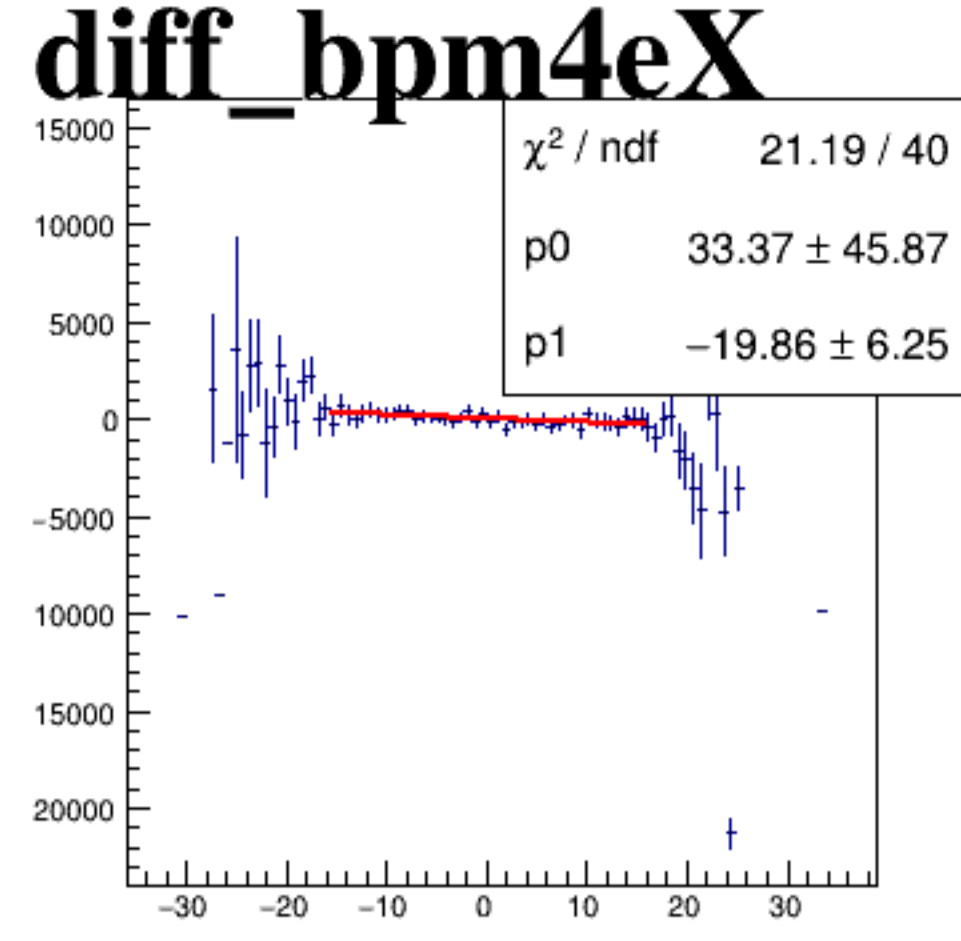
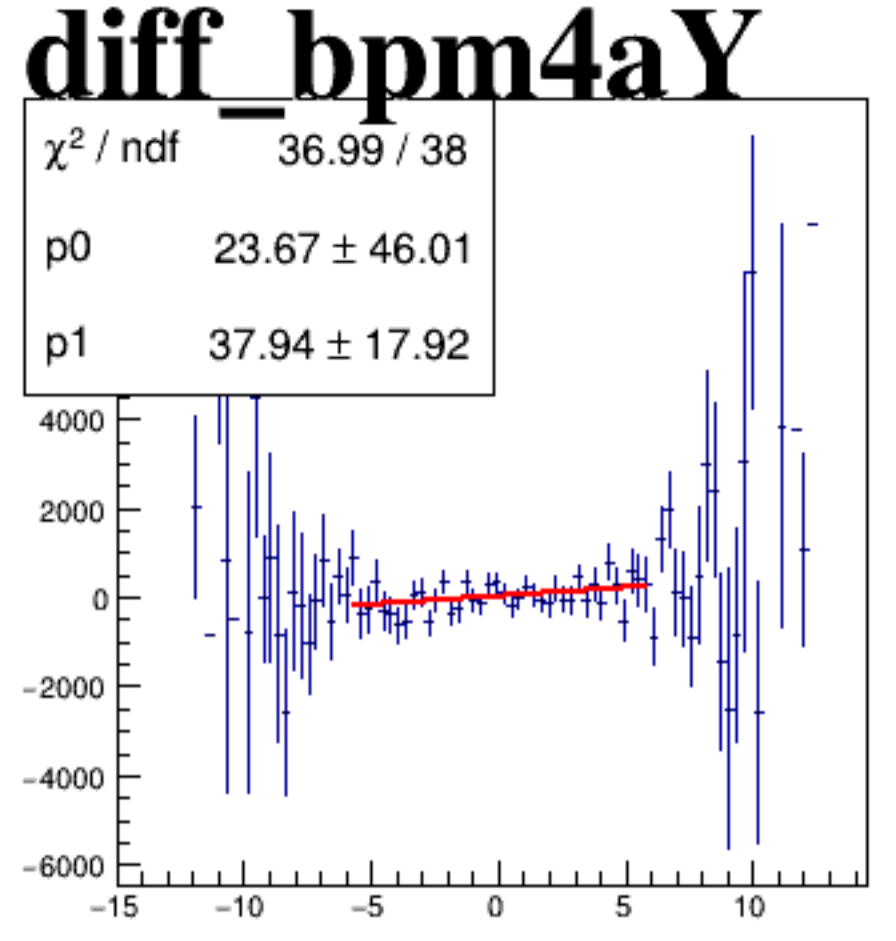
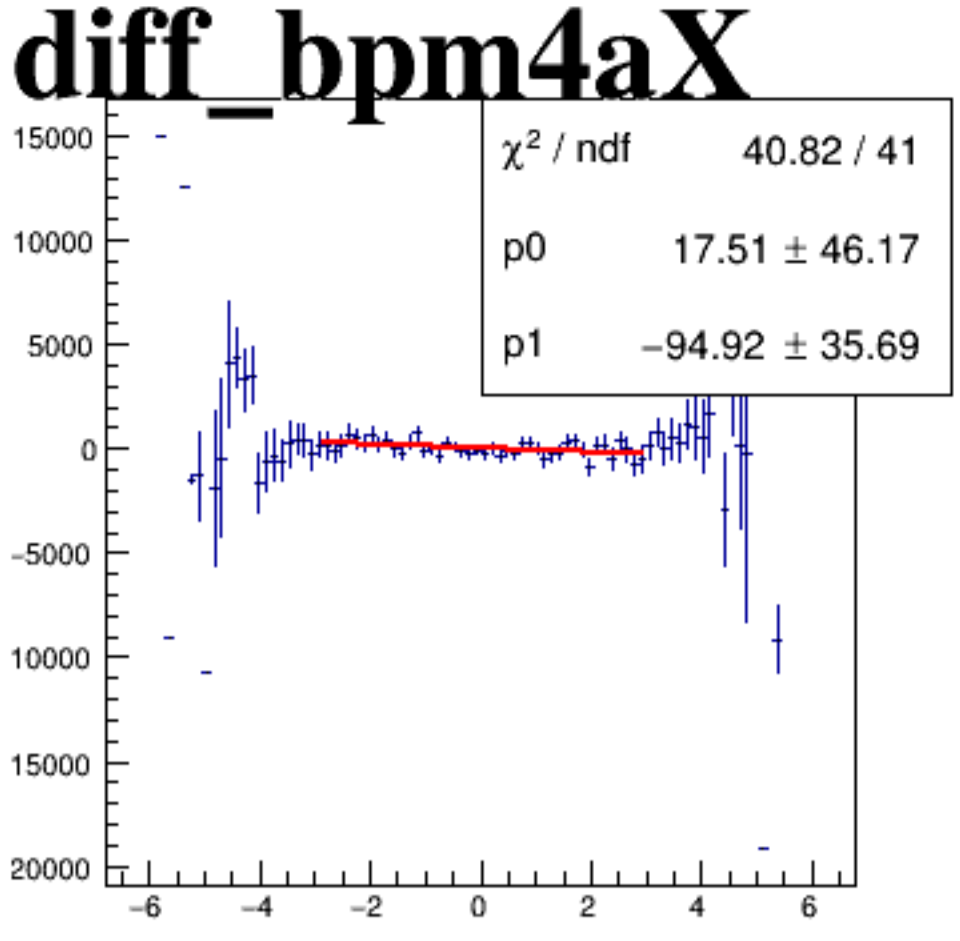


reg_asym_atr2

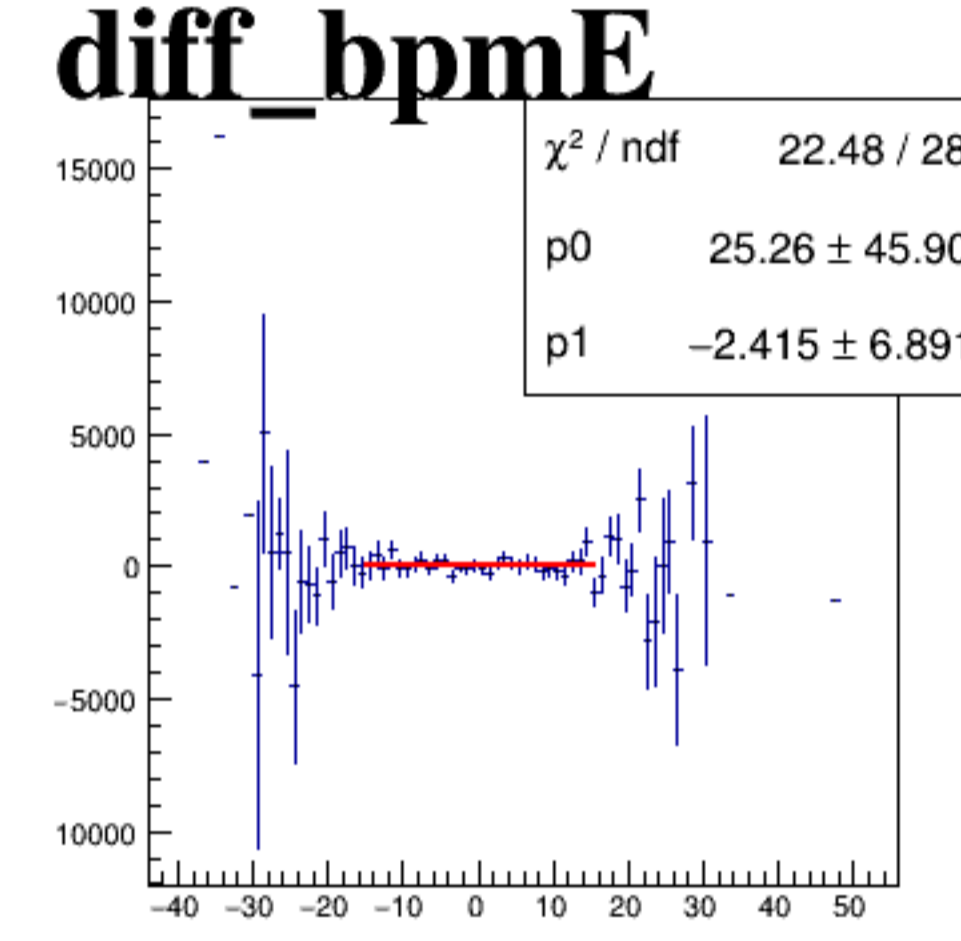
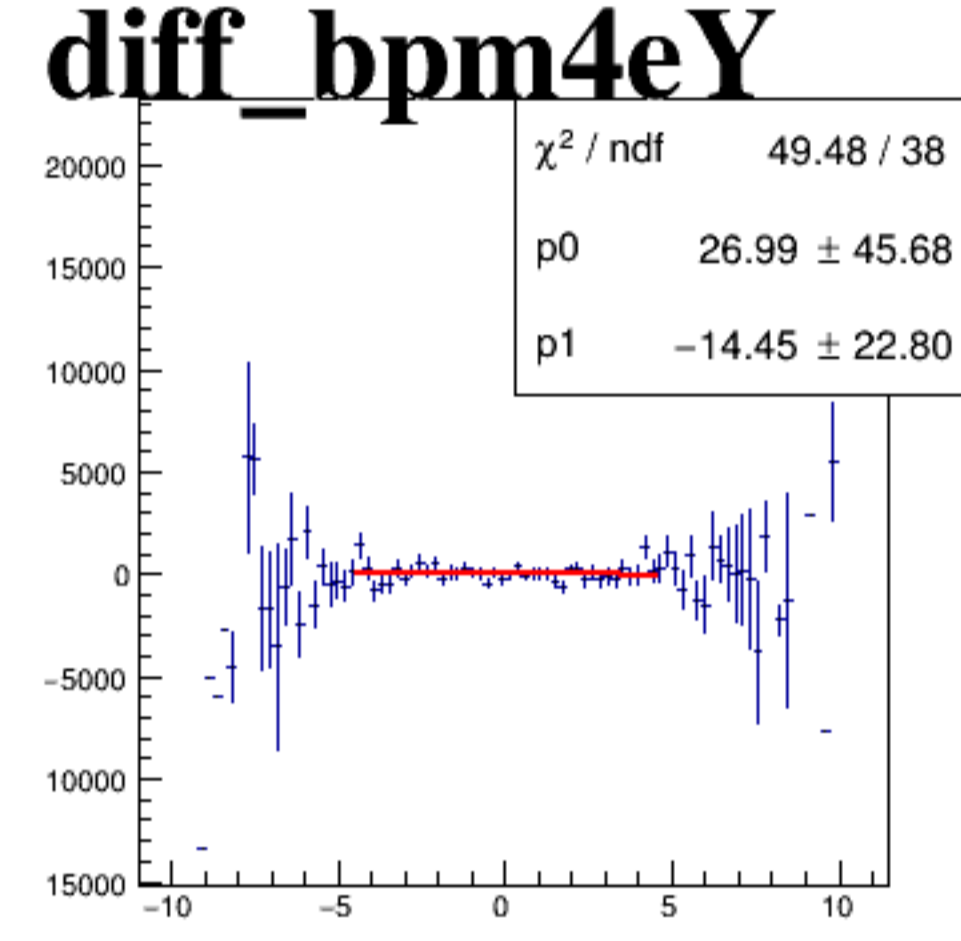
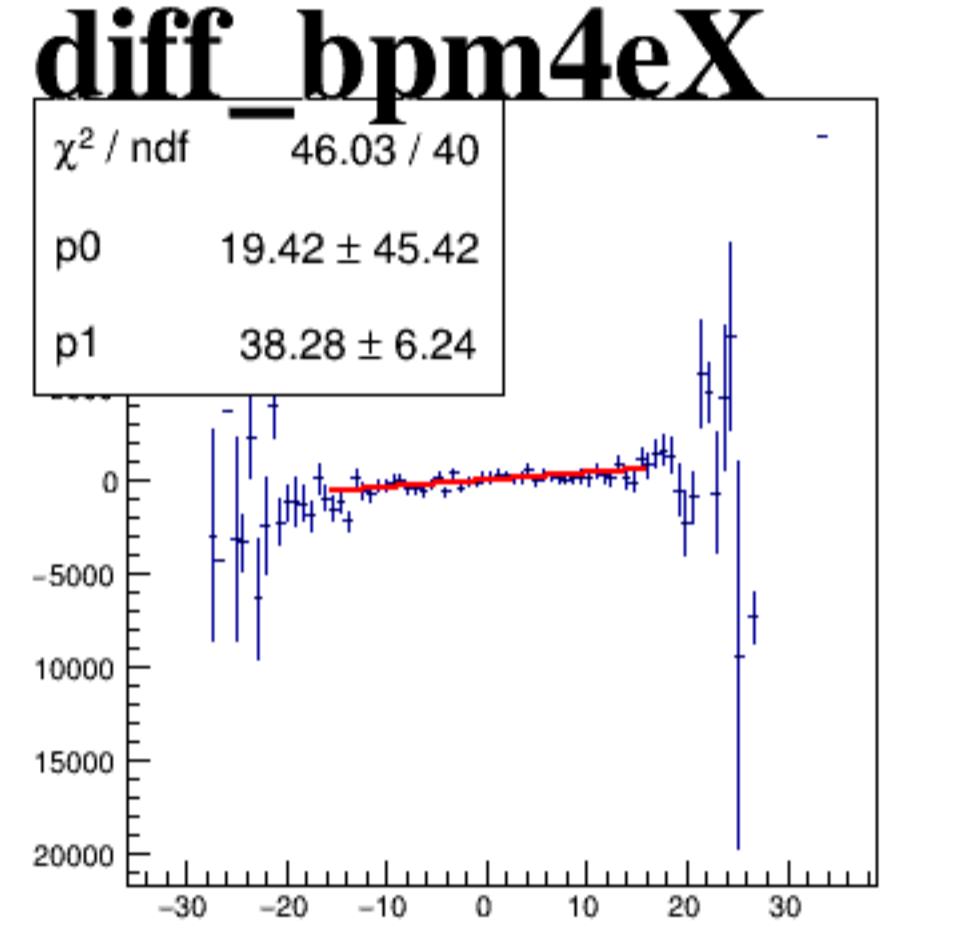
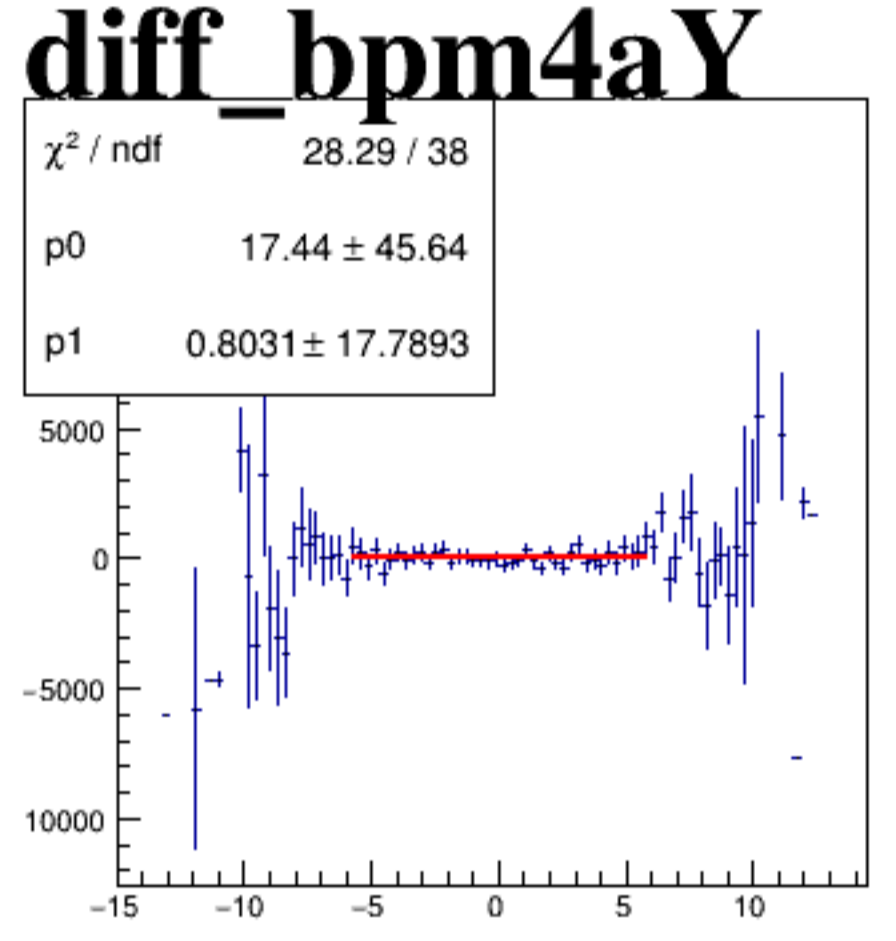
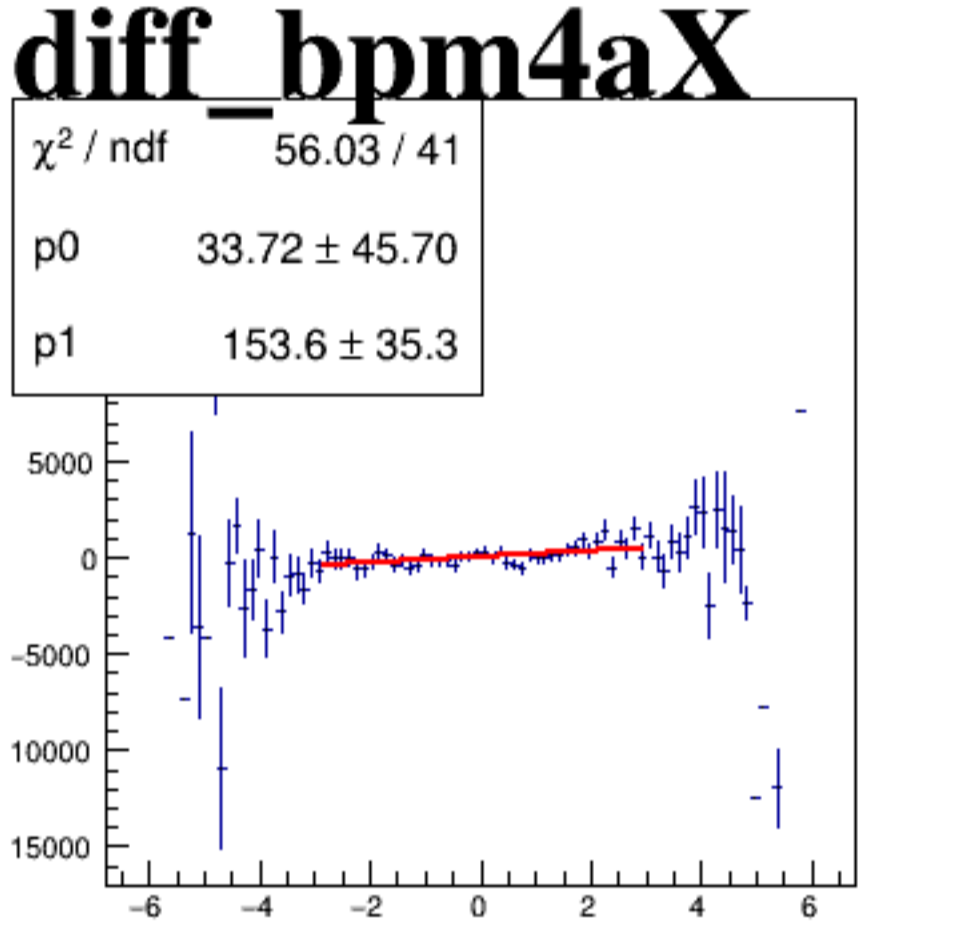


run4816_000_regression_reg_at_vs_diff_bpm-COLZ_postpan.png

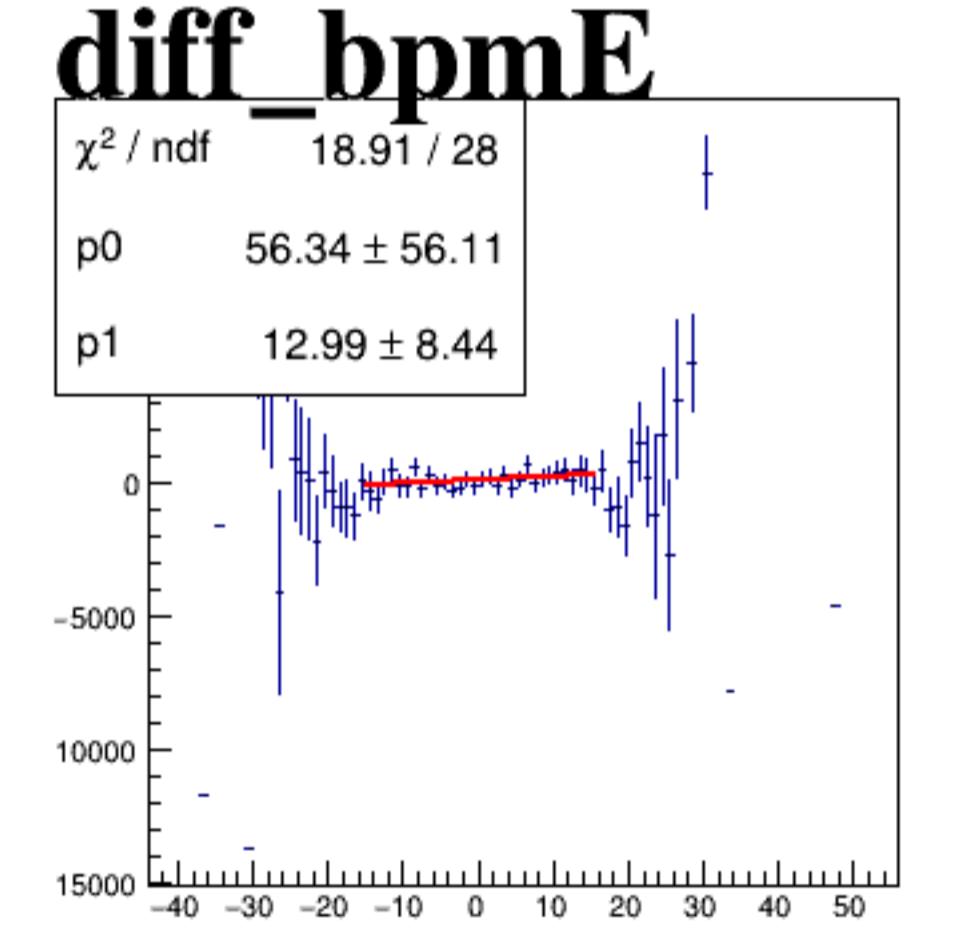
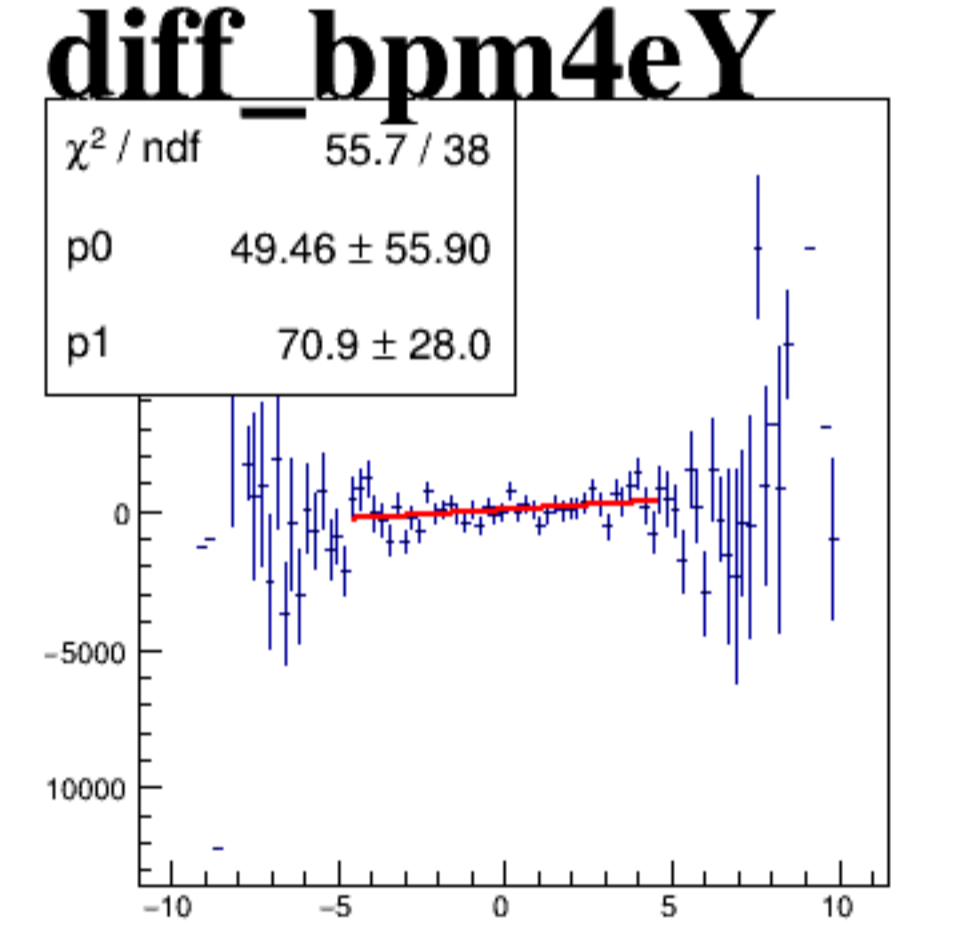
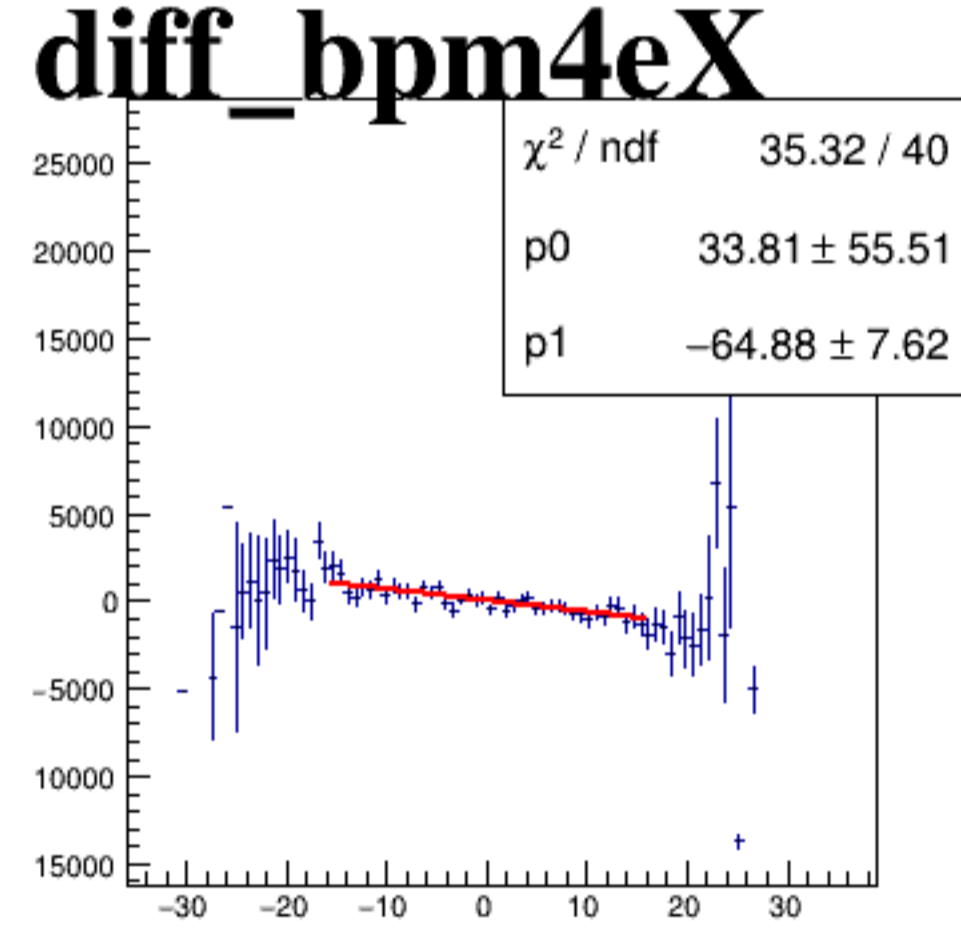
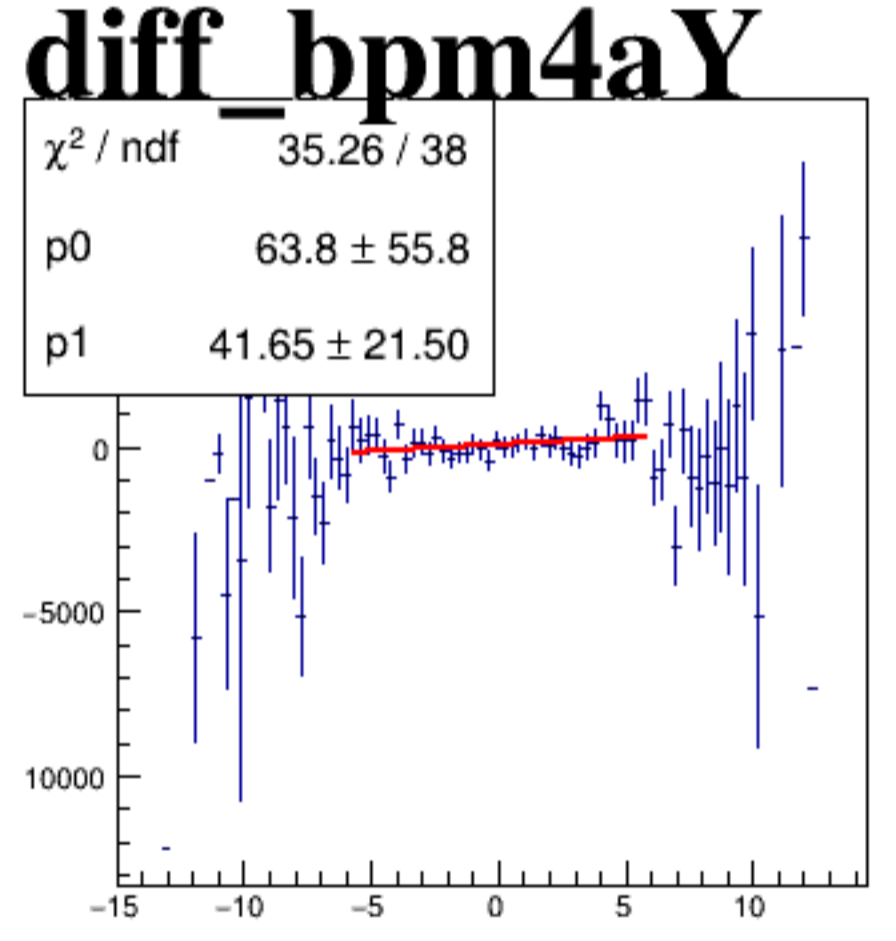
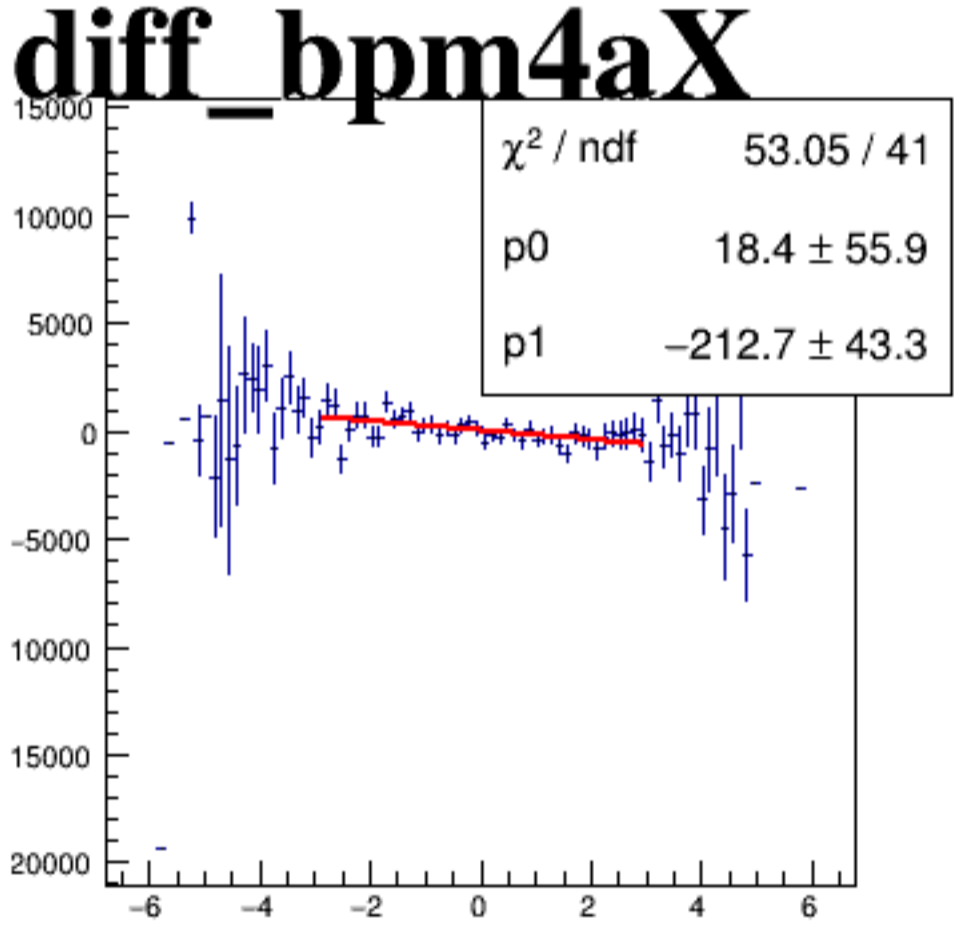
asym_atl1



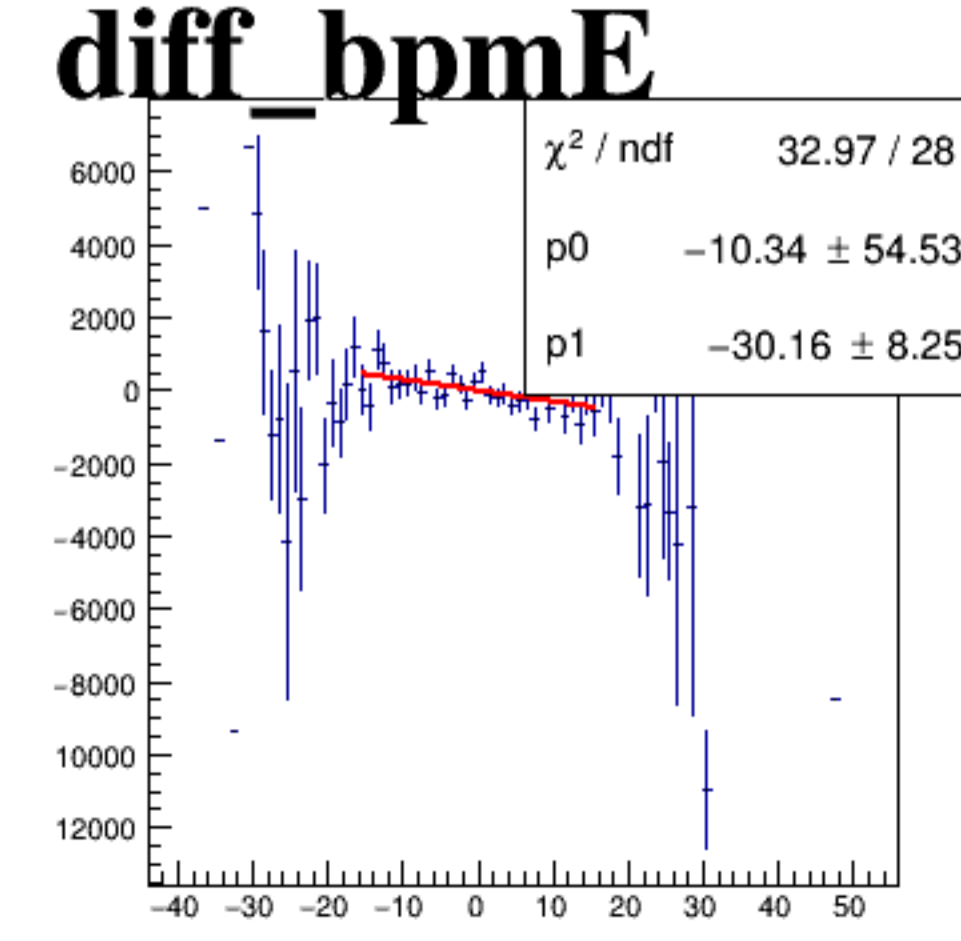
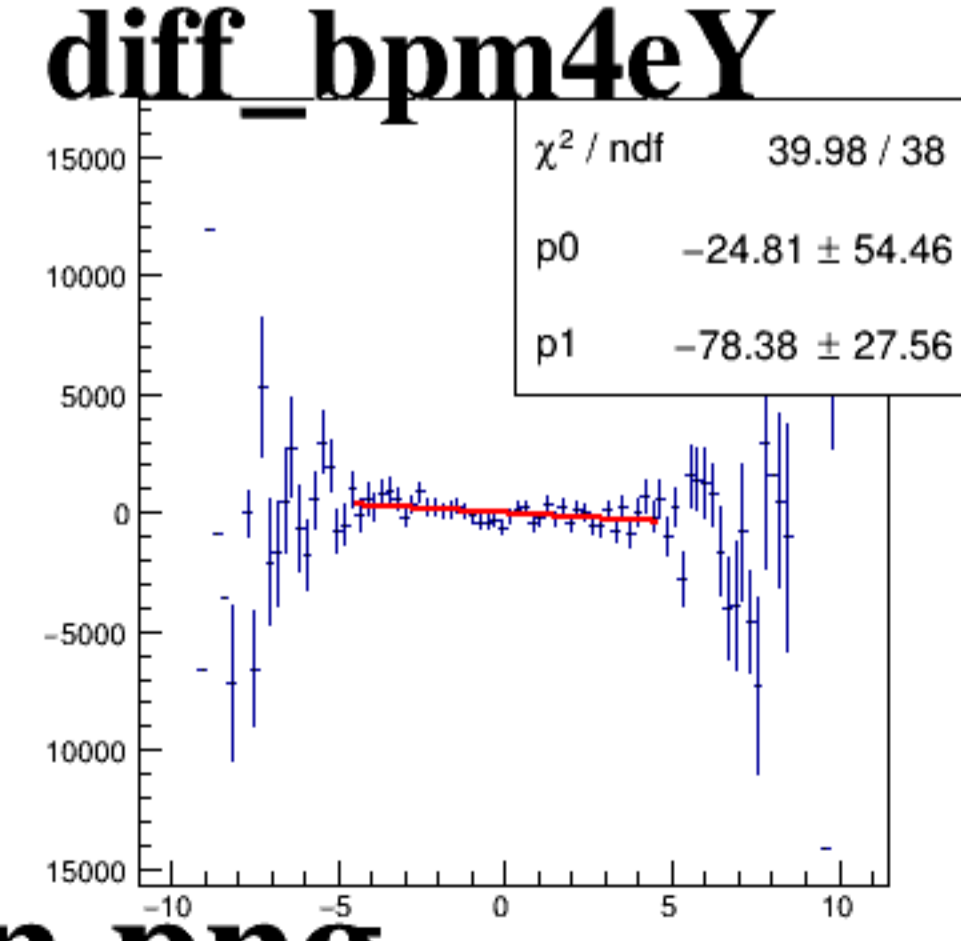
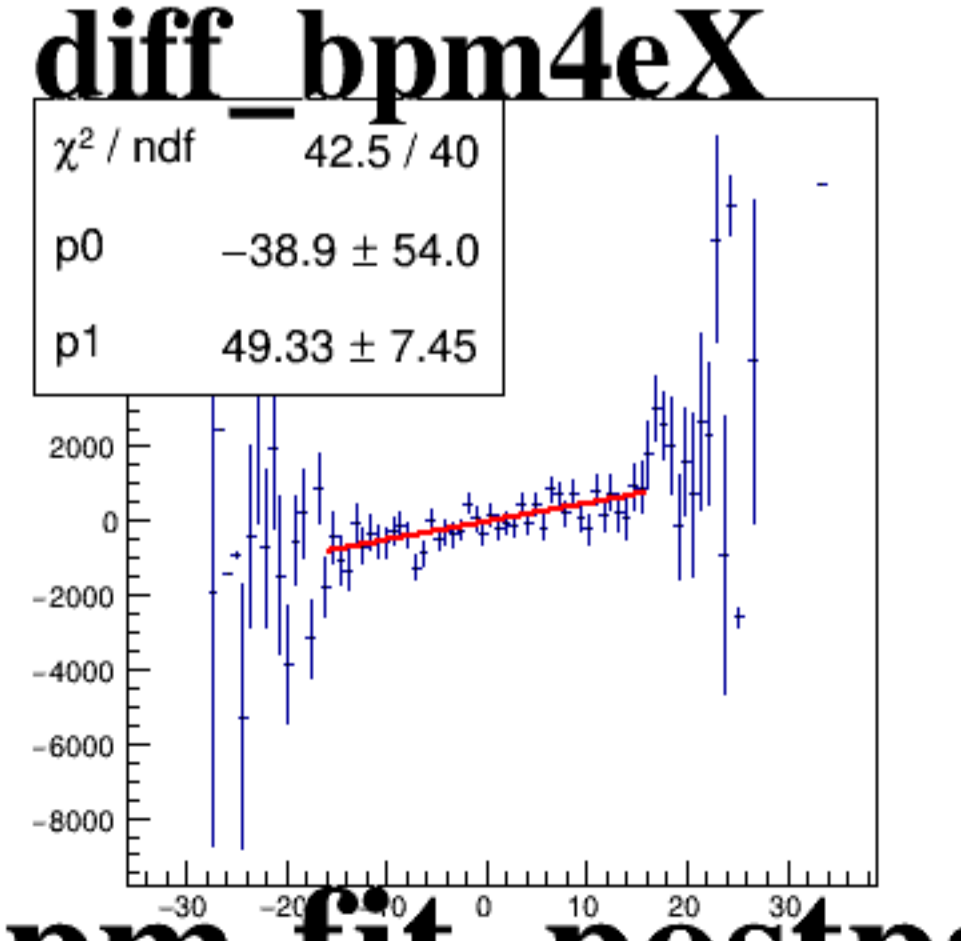
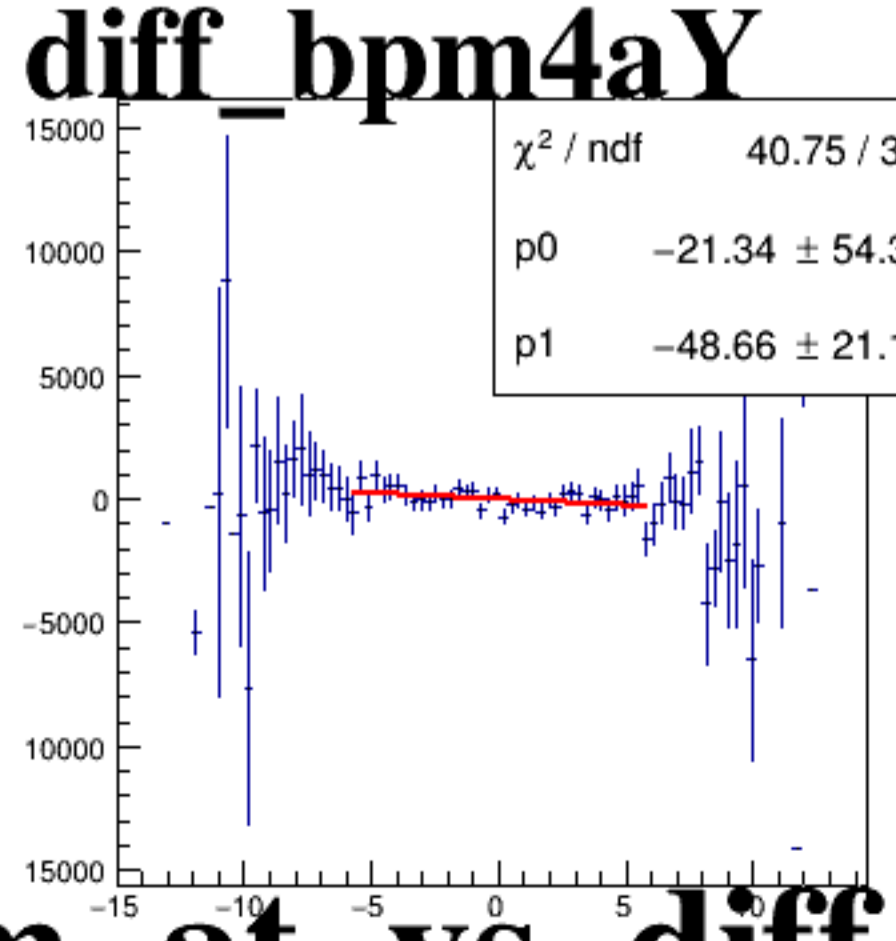
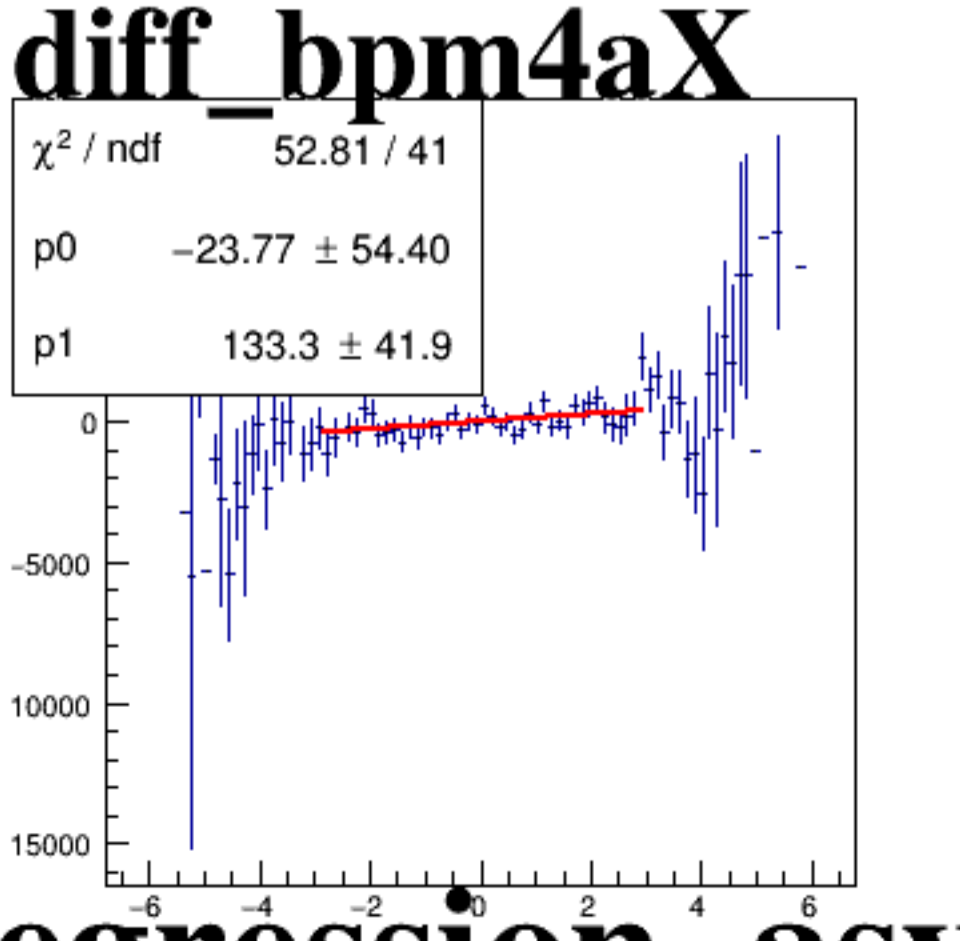
asym_atr1



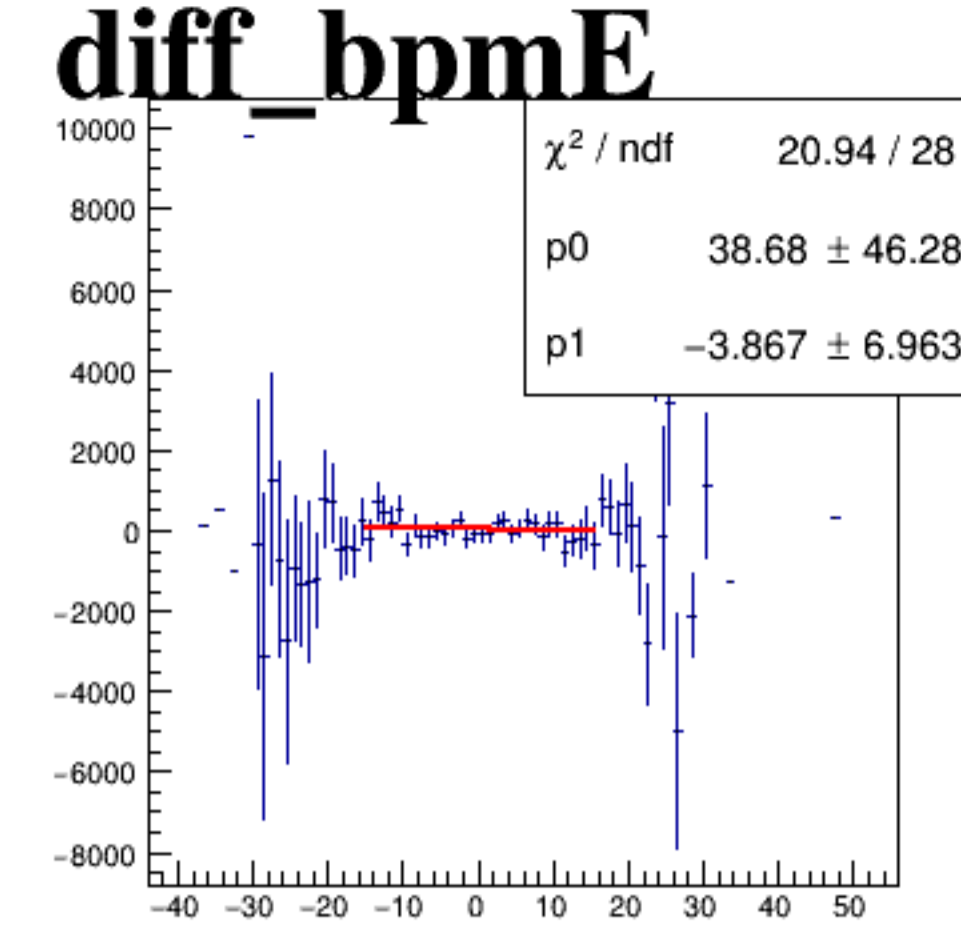
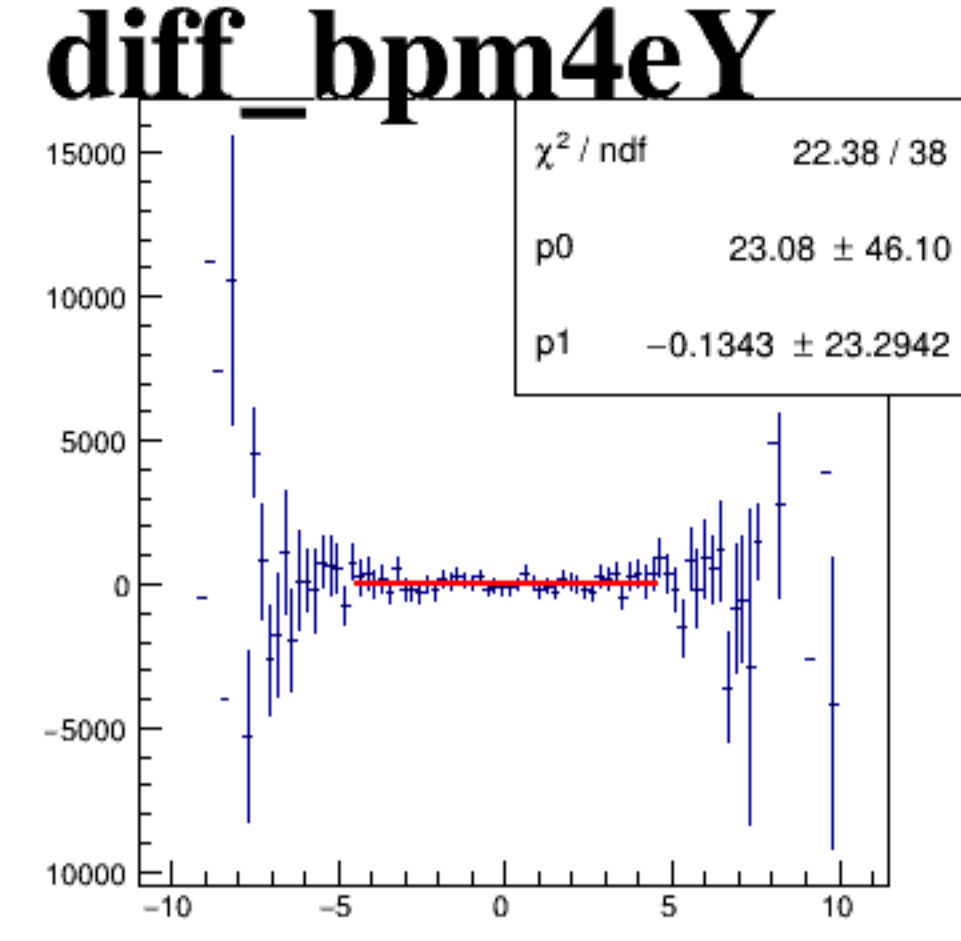
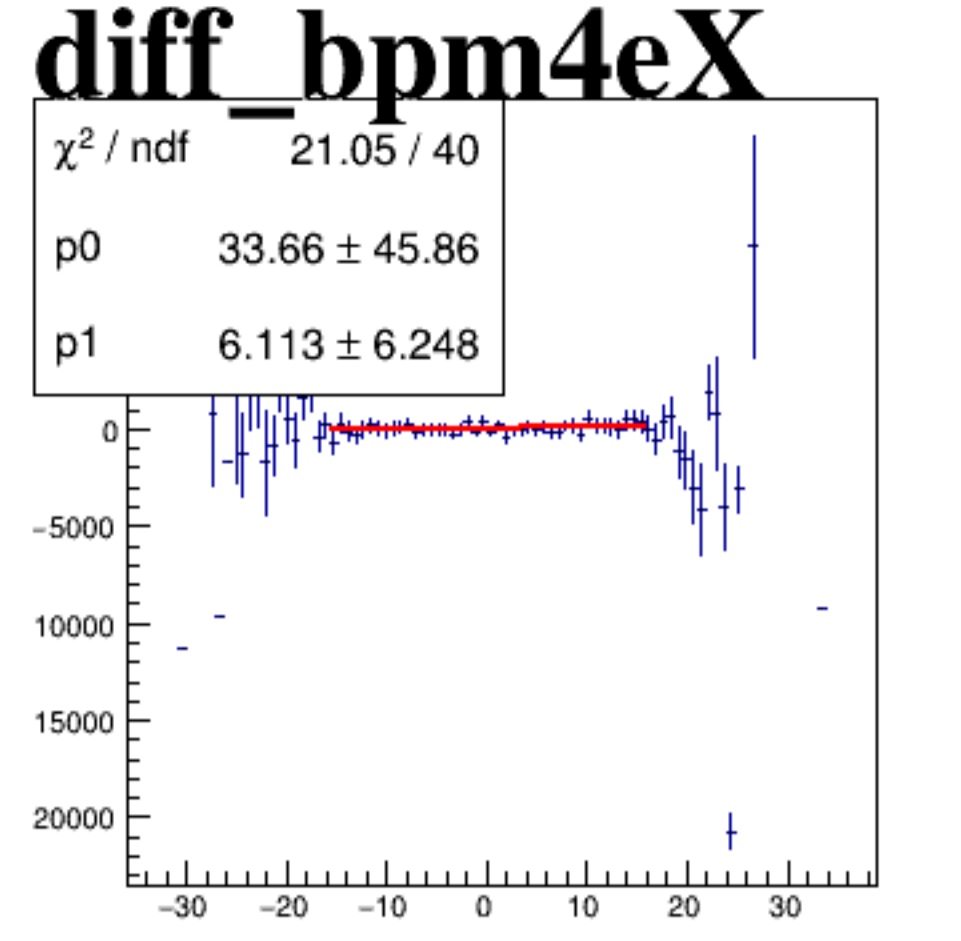
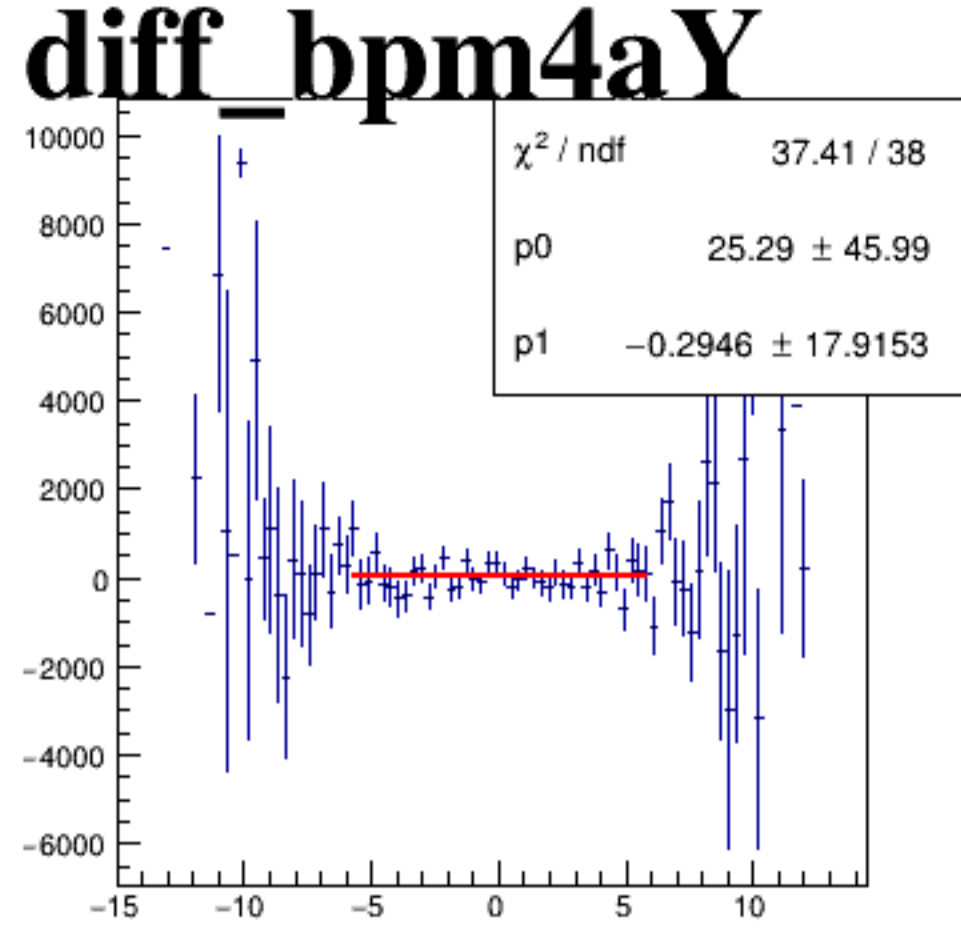
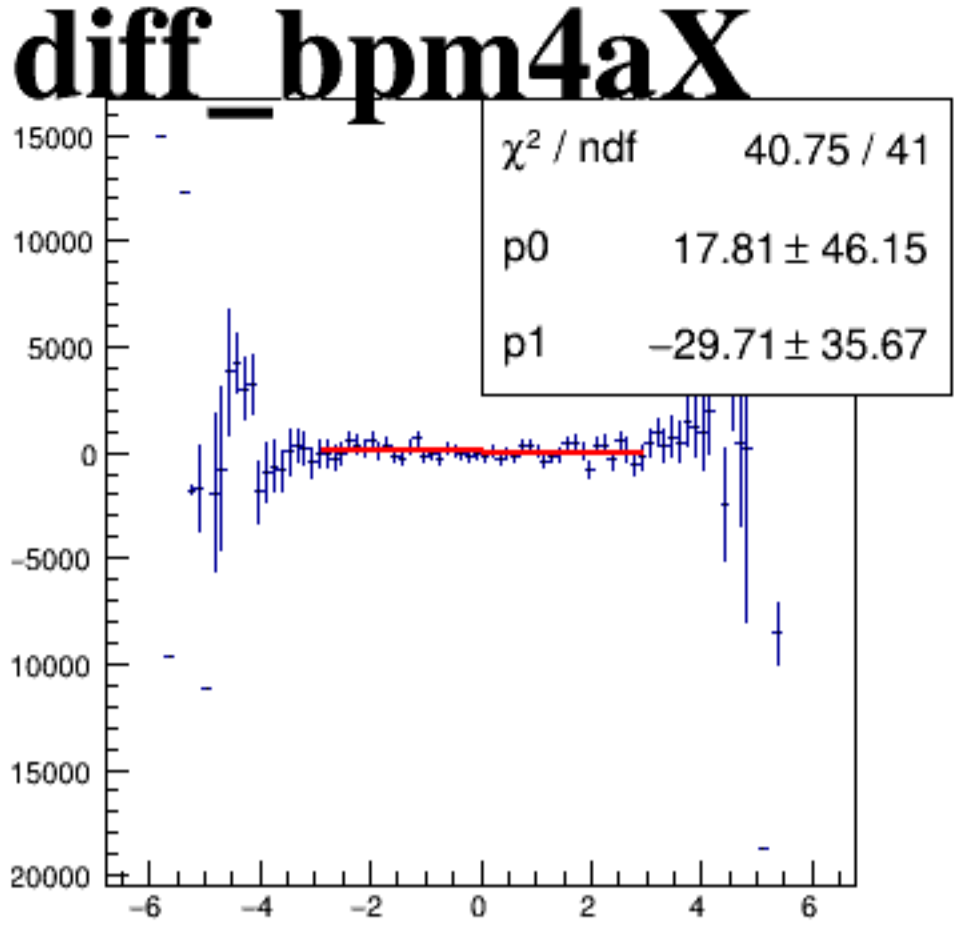
asym_atl2



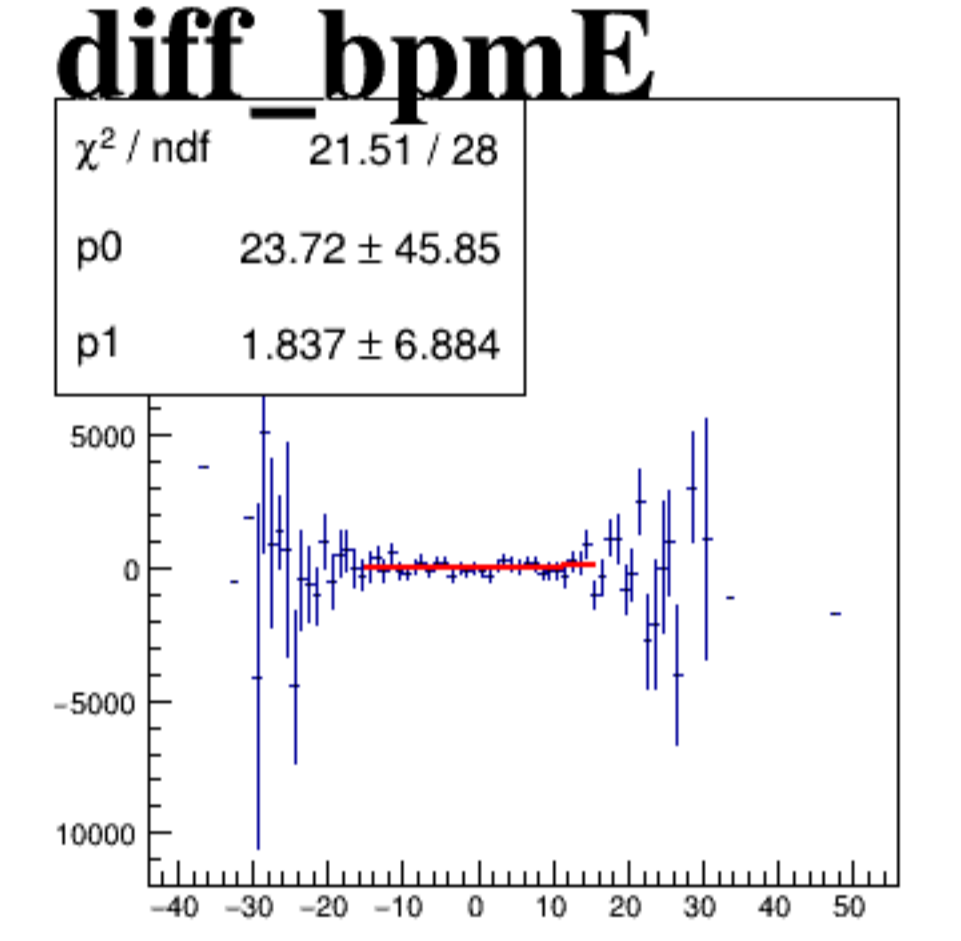
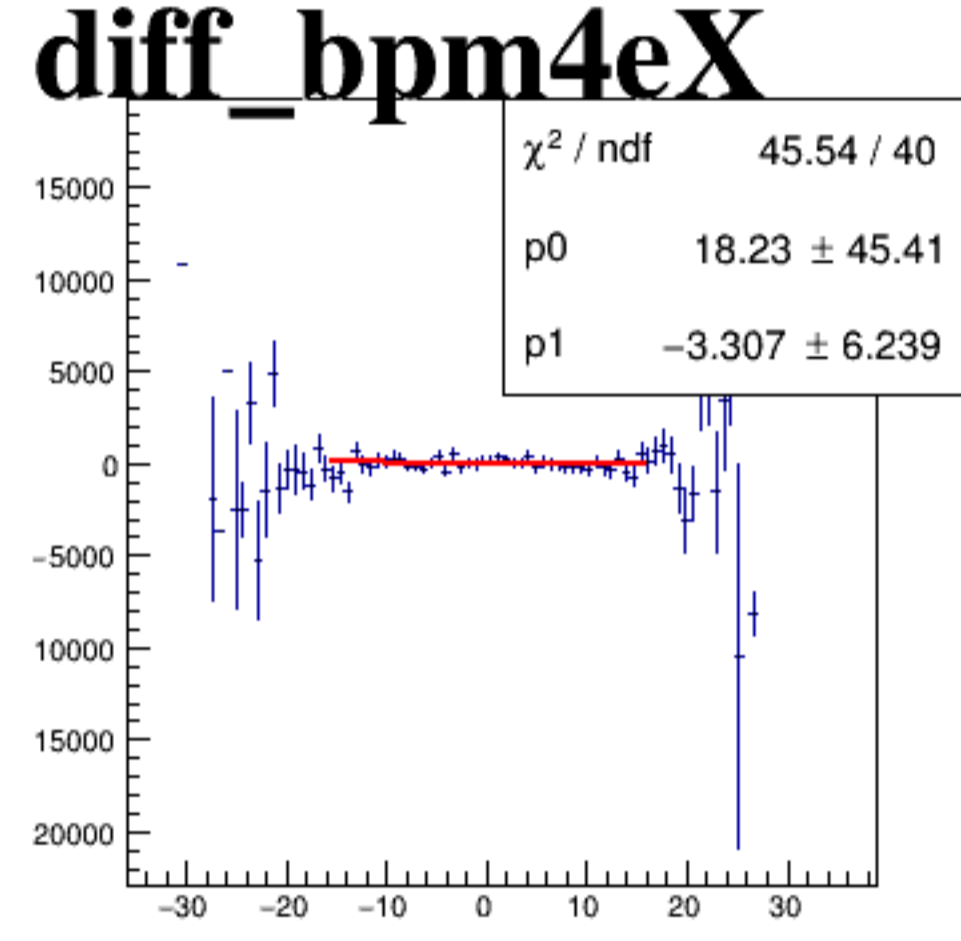
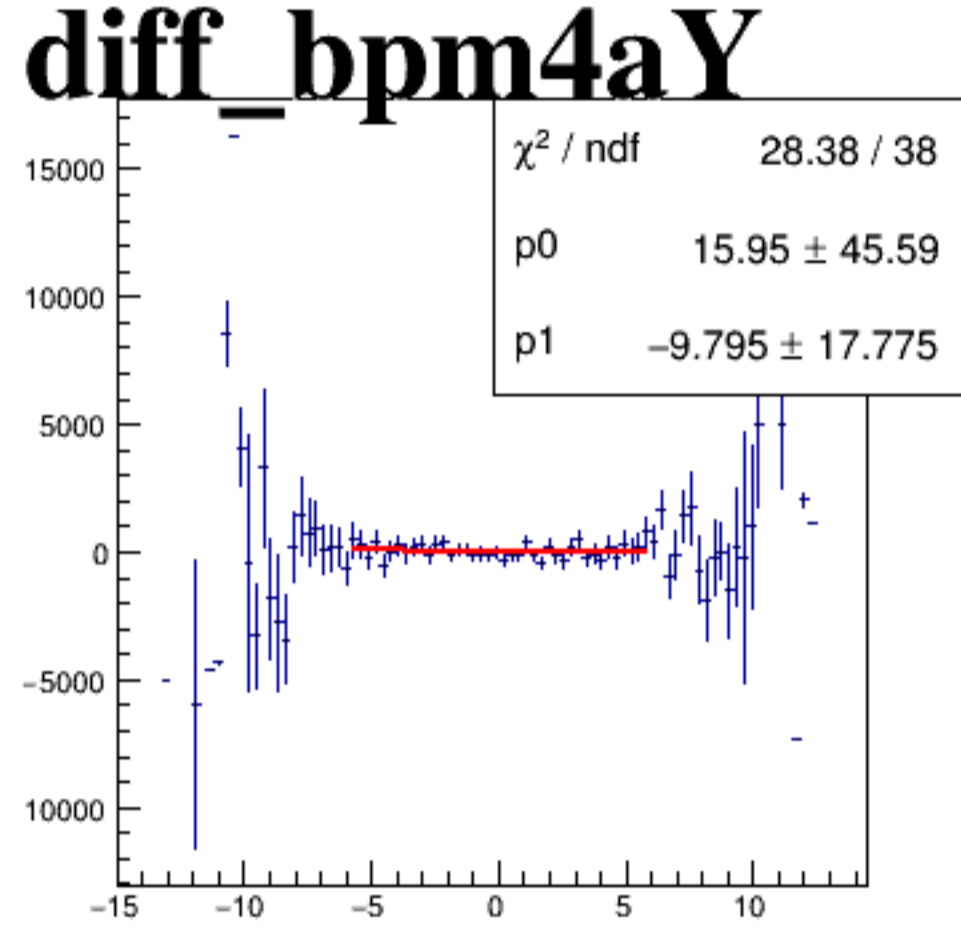
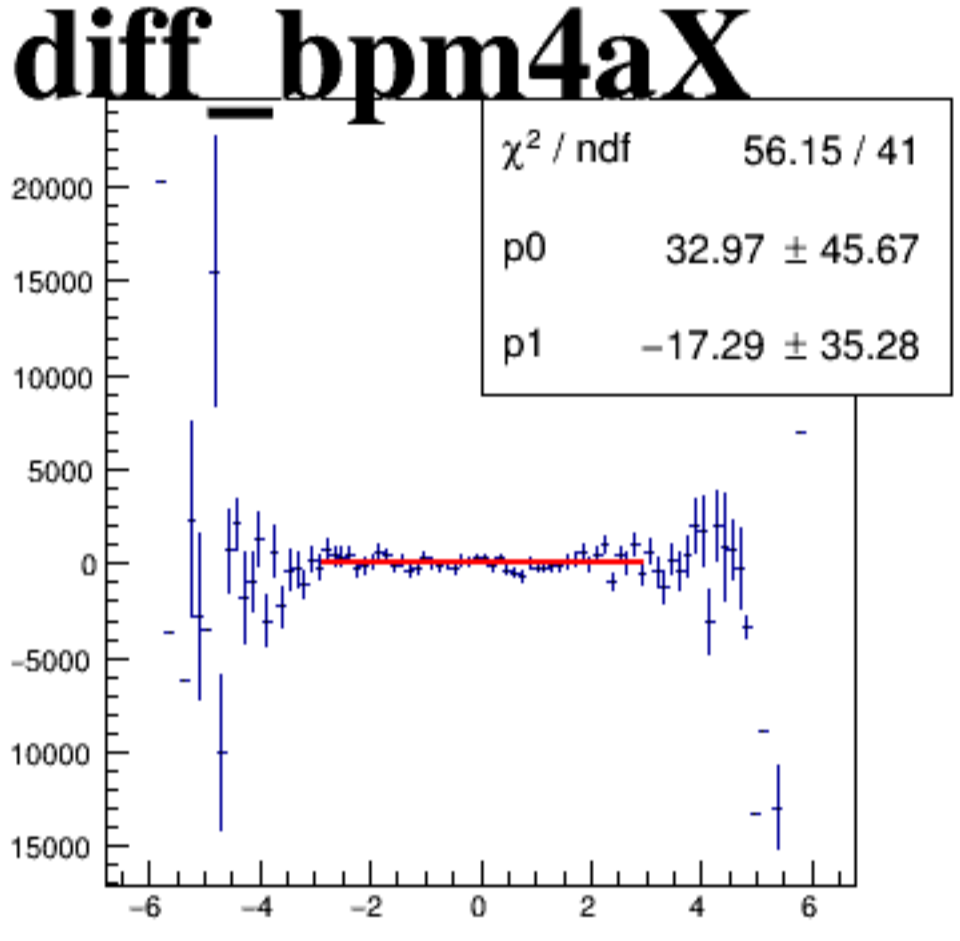
asym_atr2



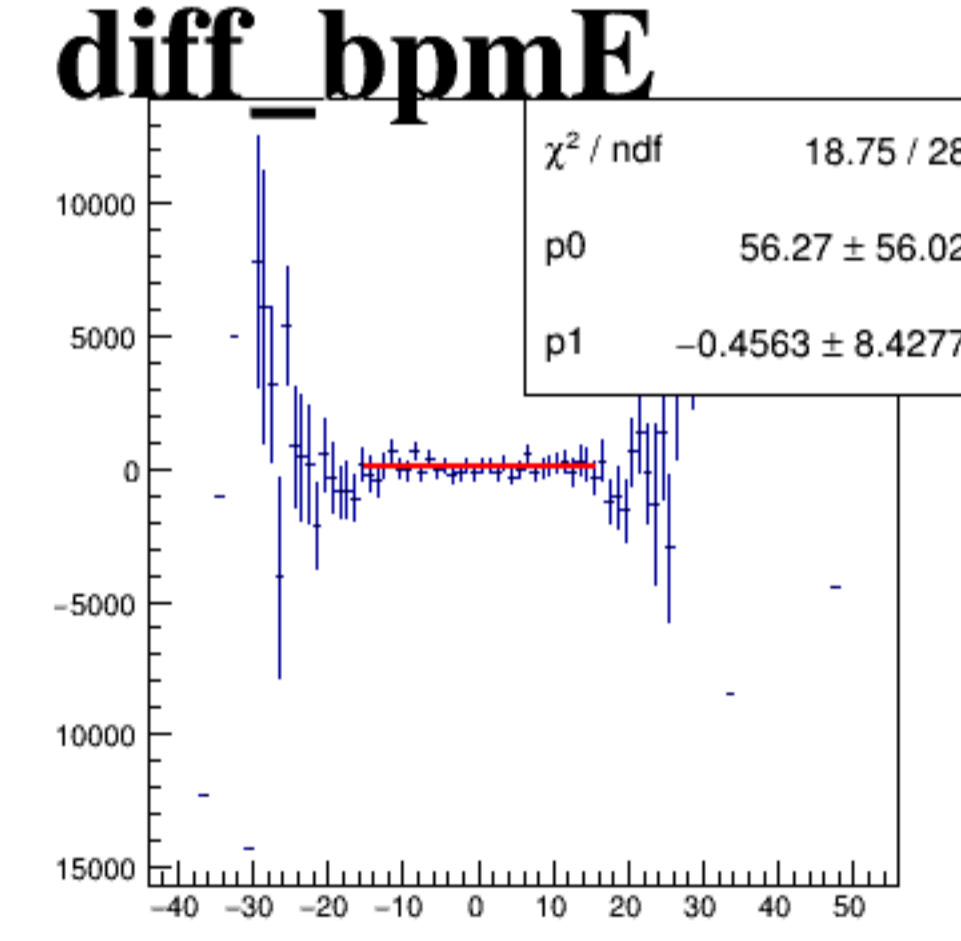
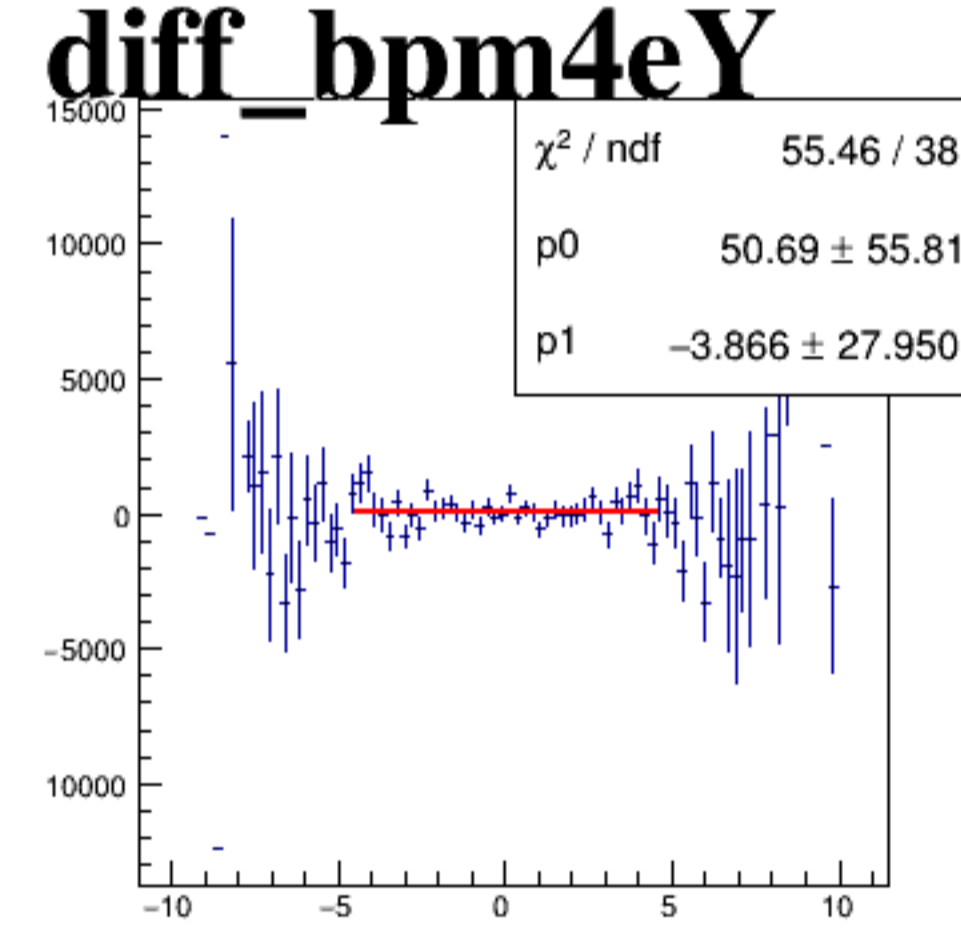
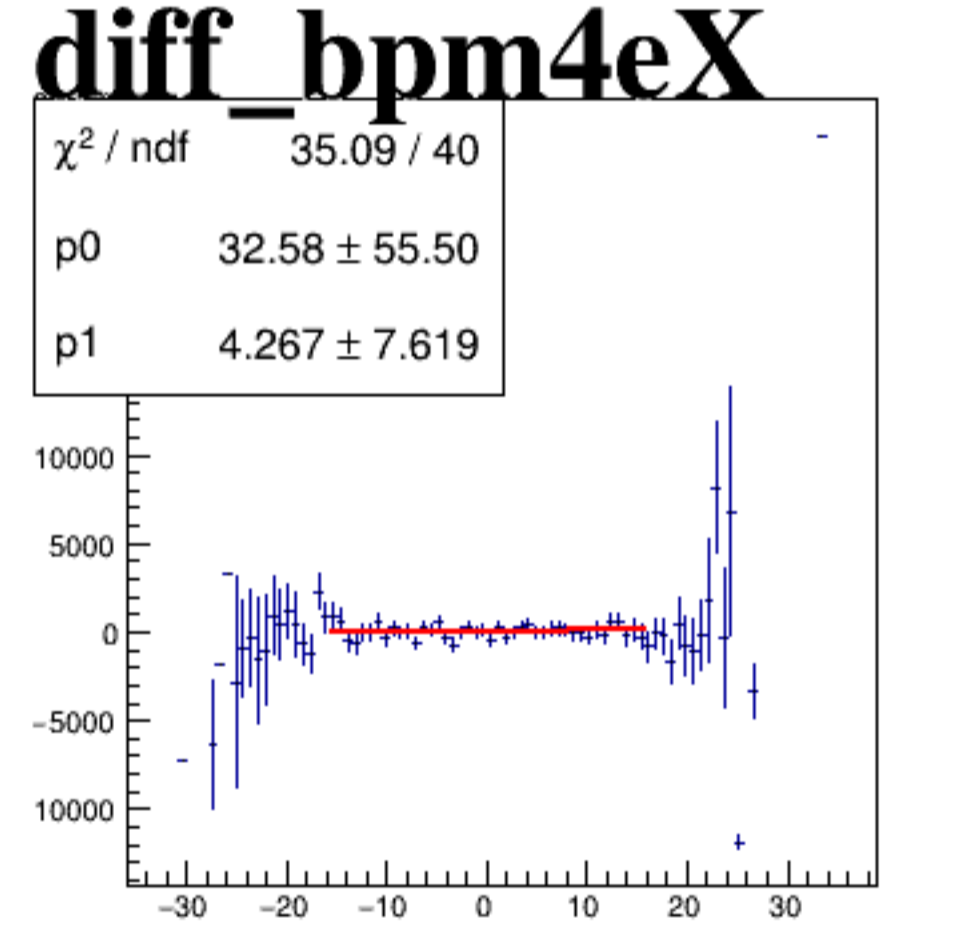
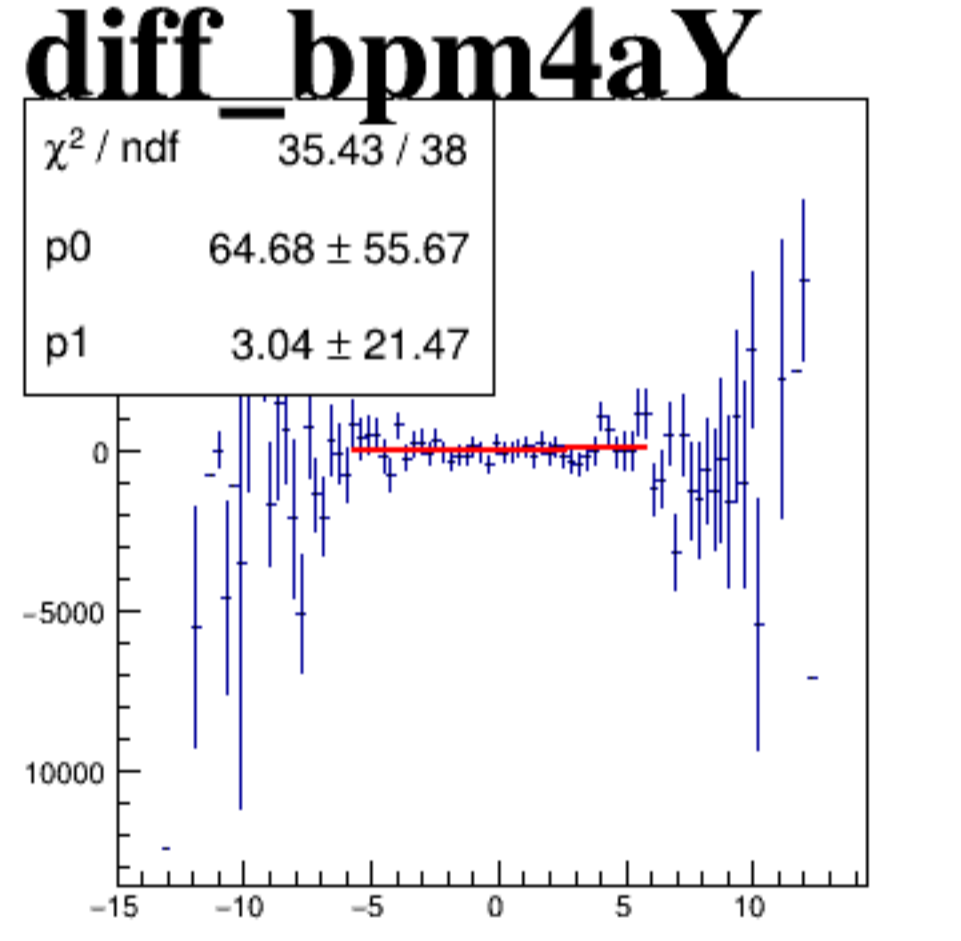
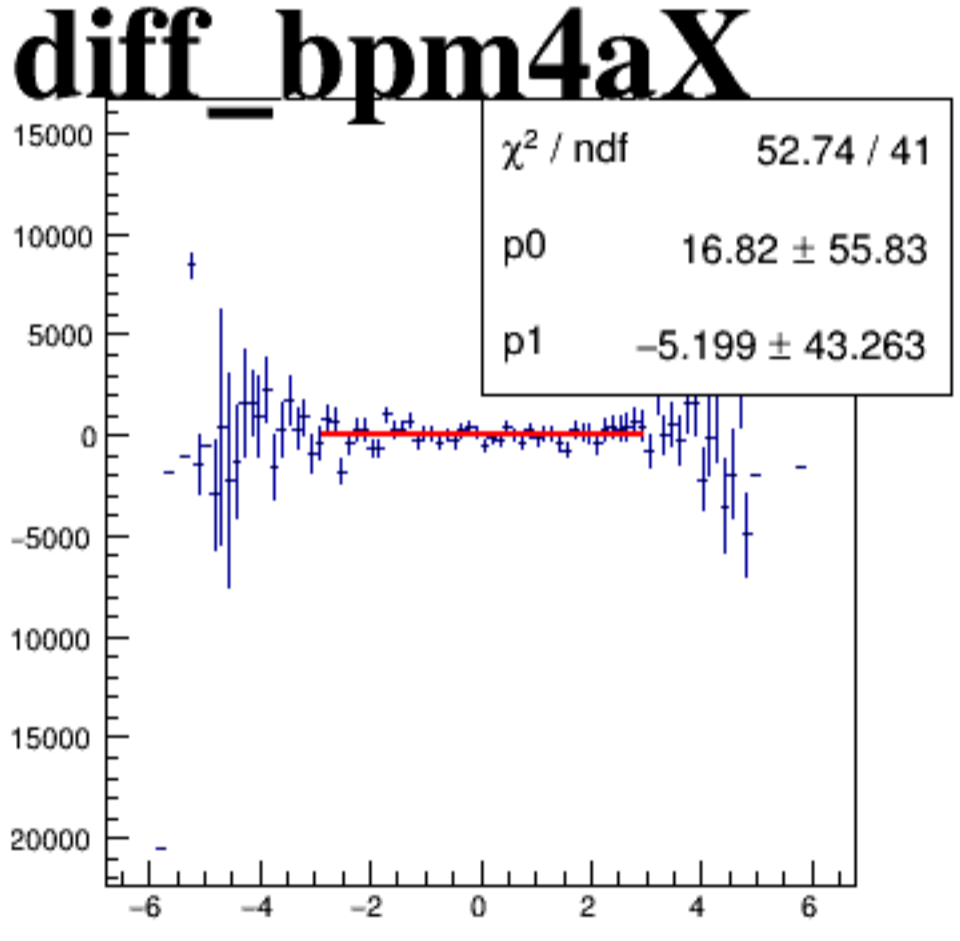
reg_asym_atl1



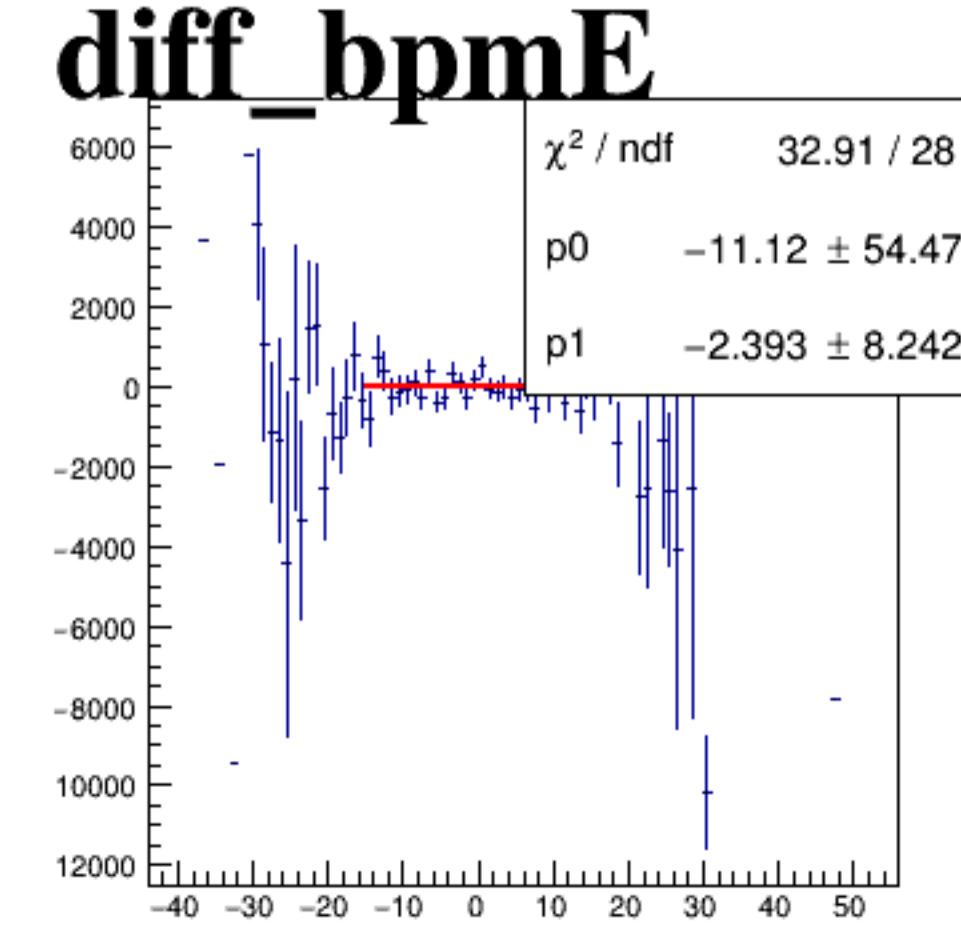
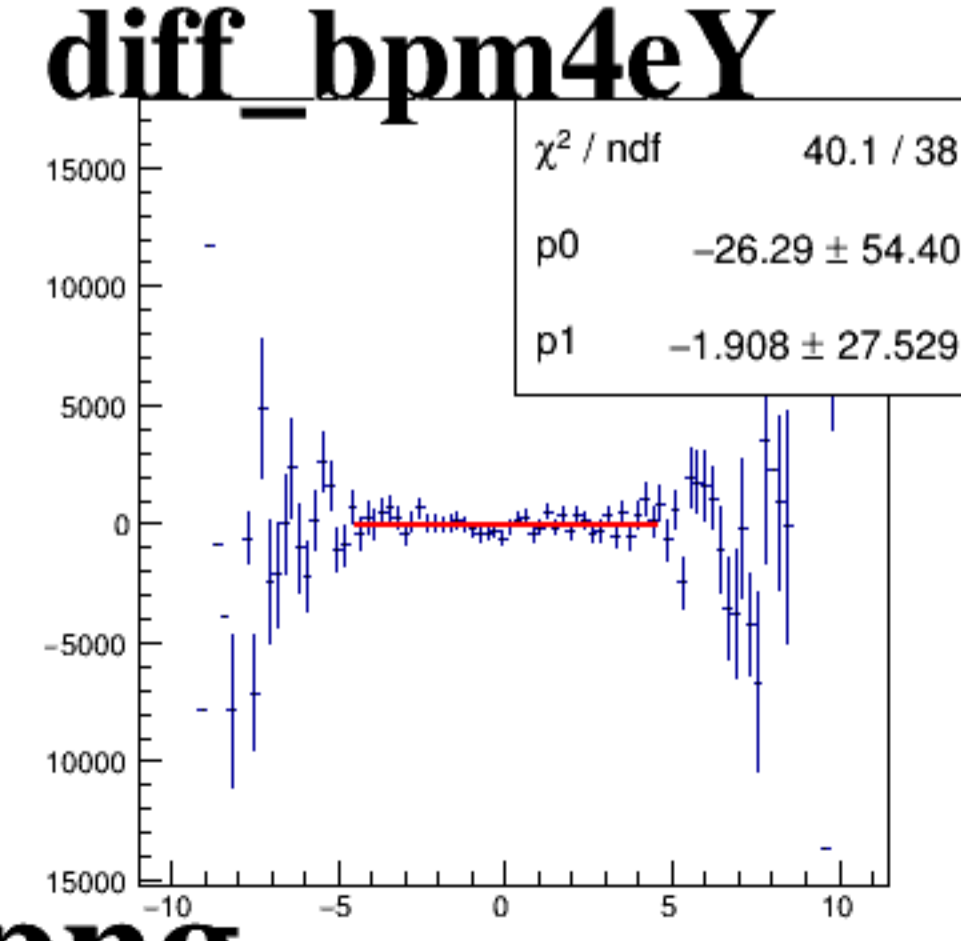
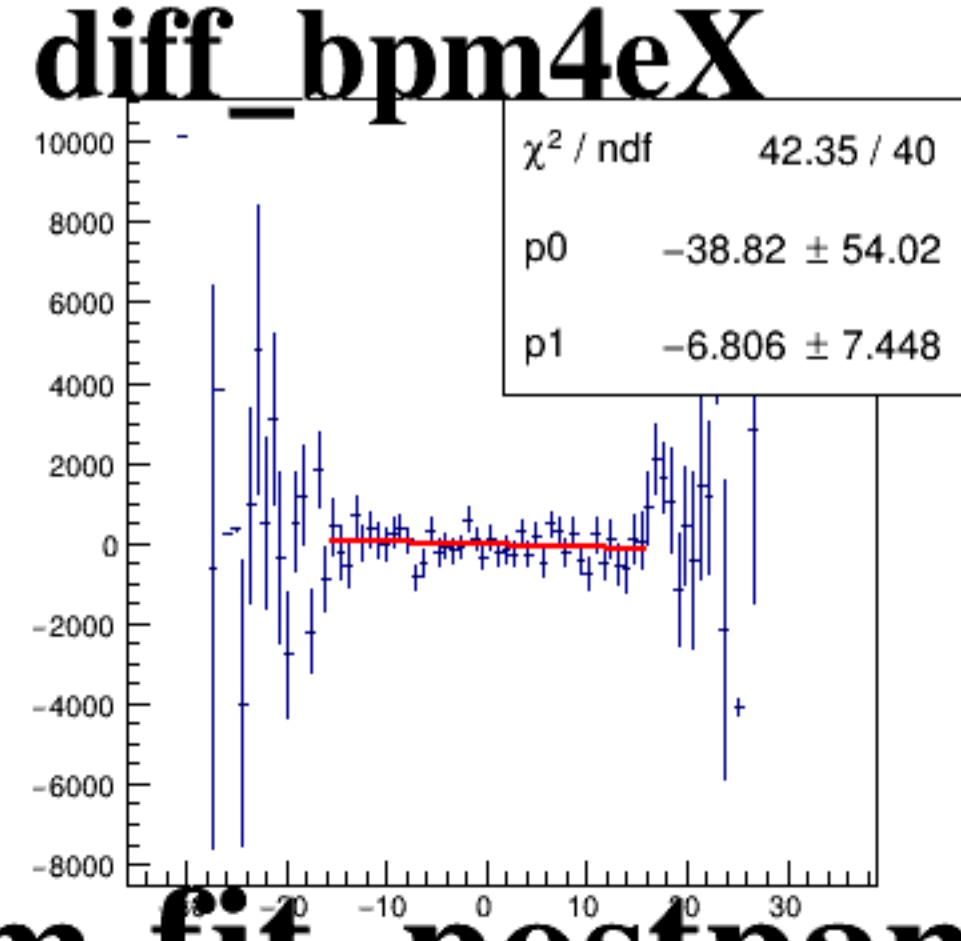
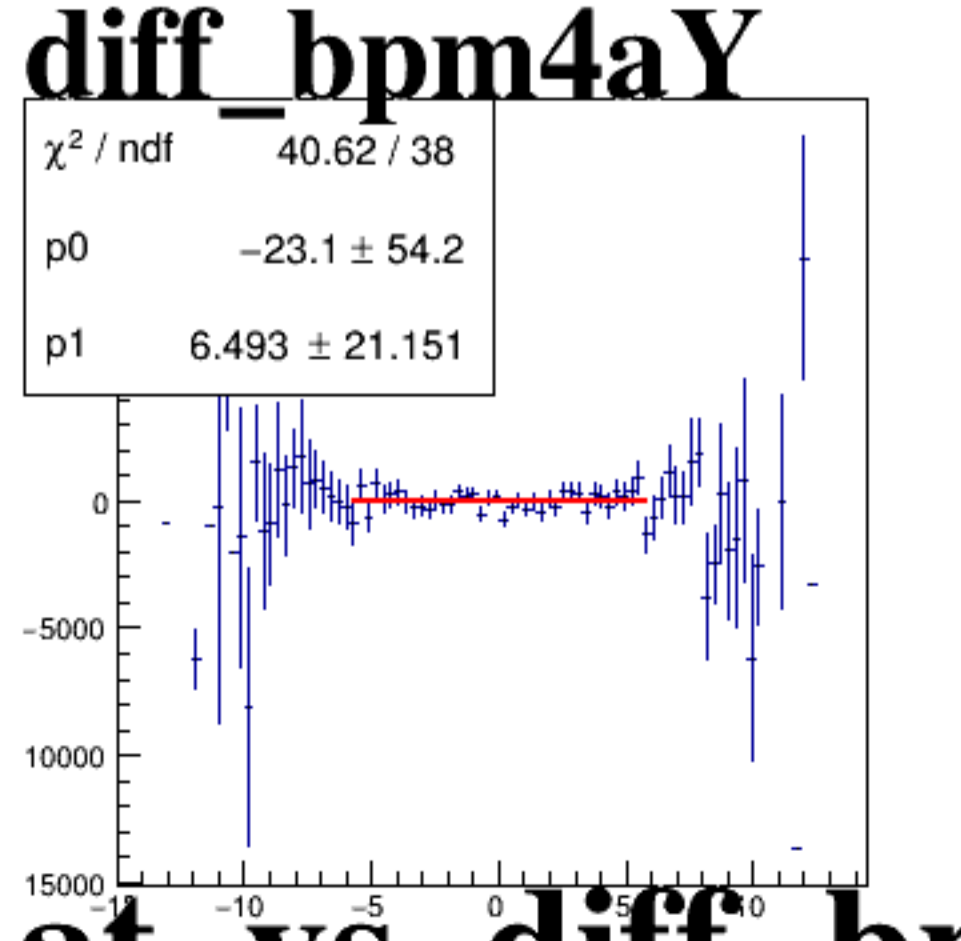
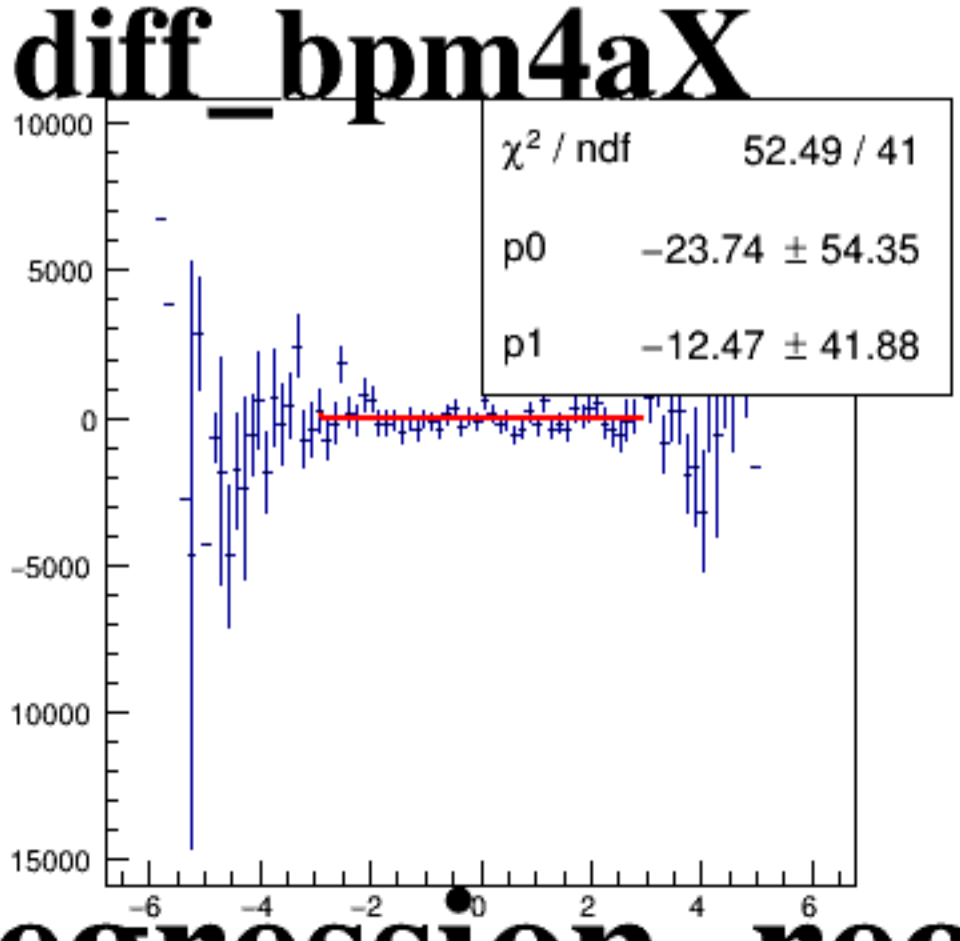
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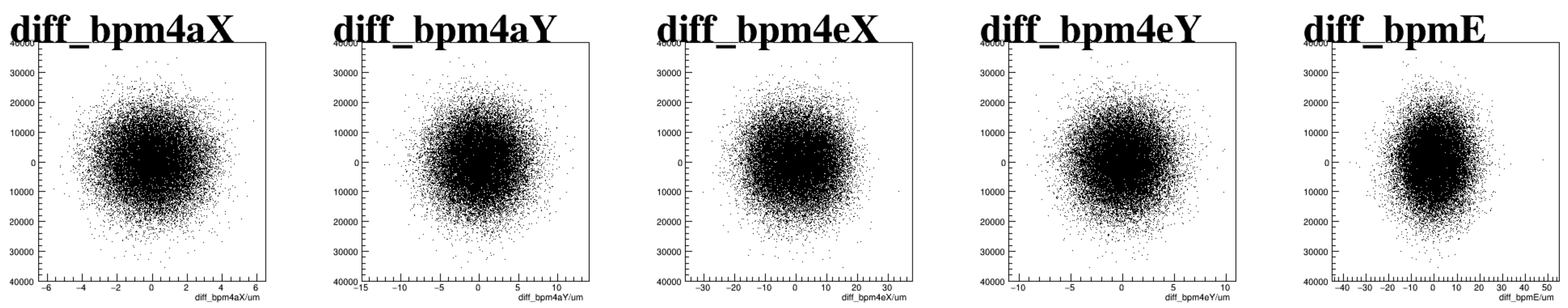
reg_asym_atl2



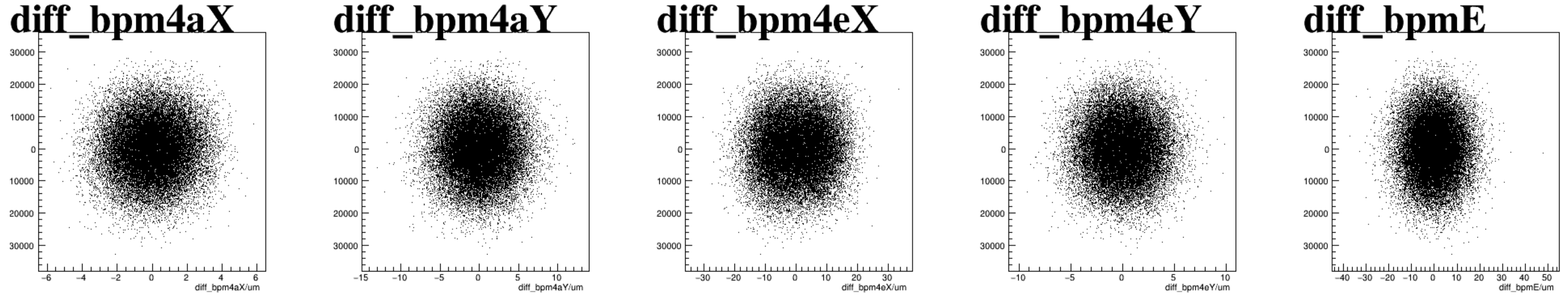
reg_asym_atr2



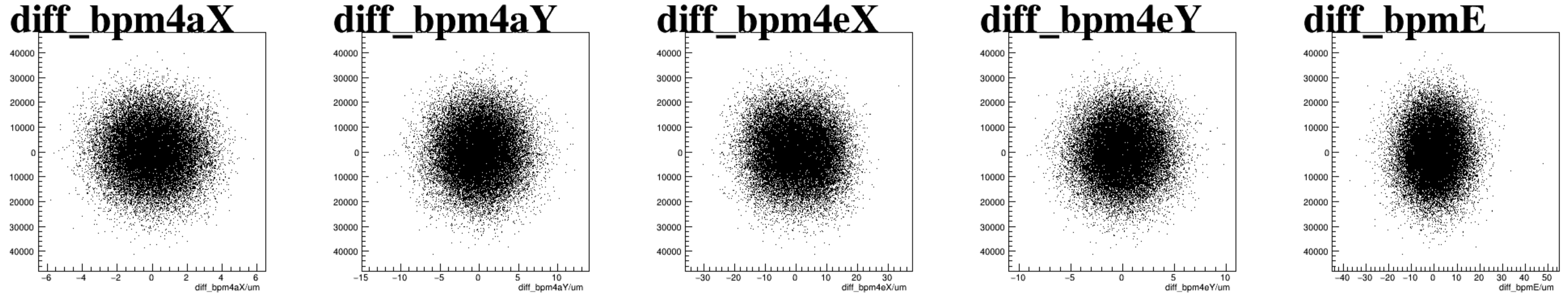
asym_atl1



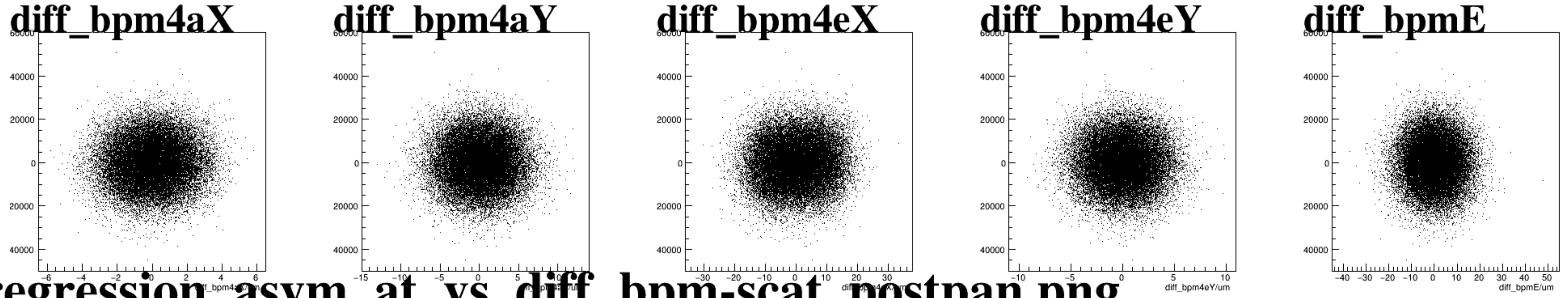
asym_atr1



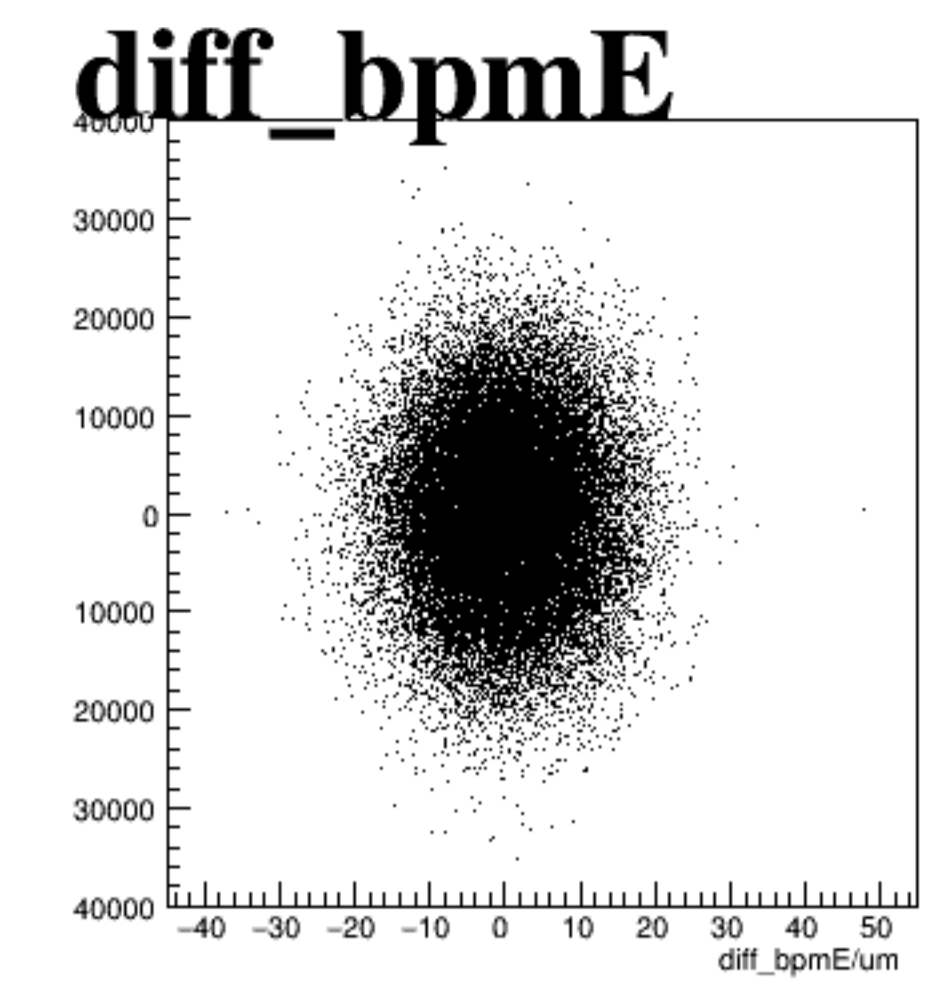
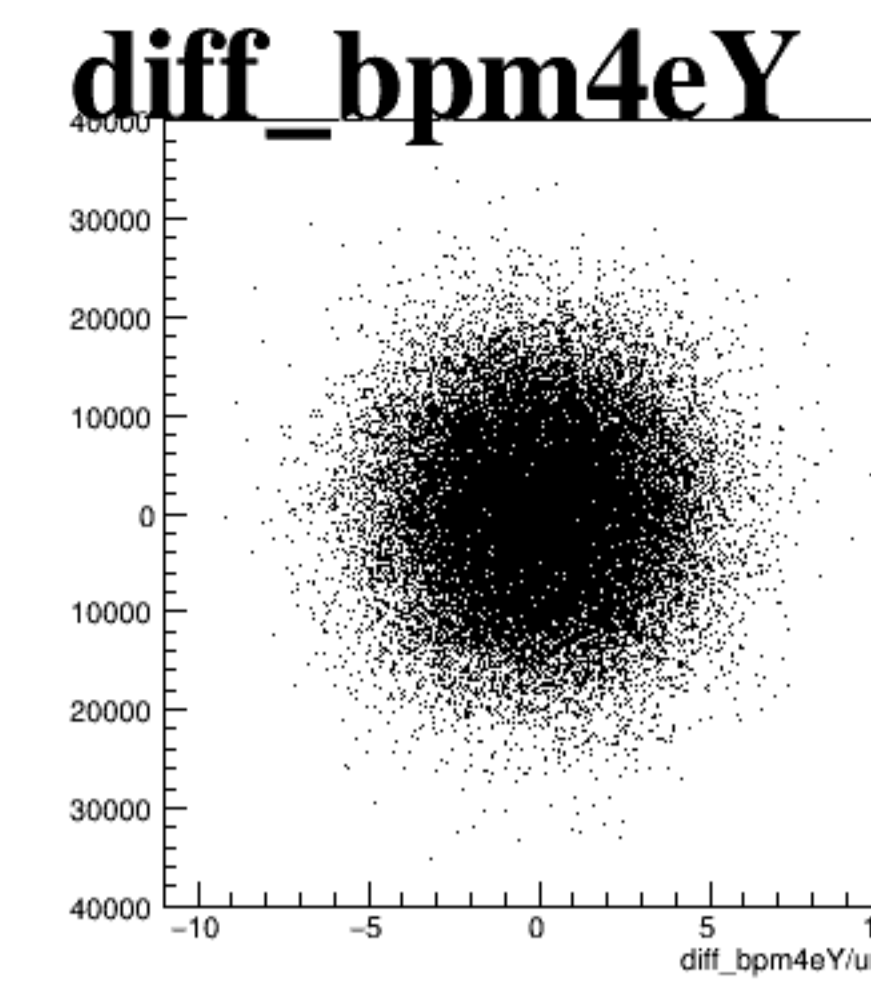
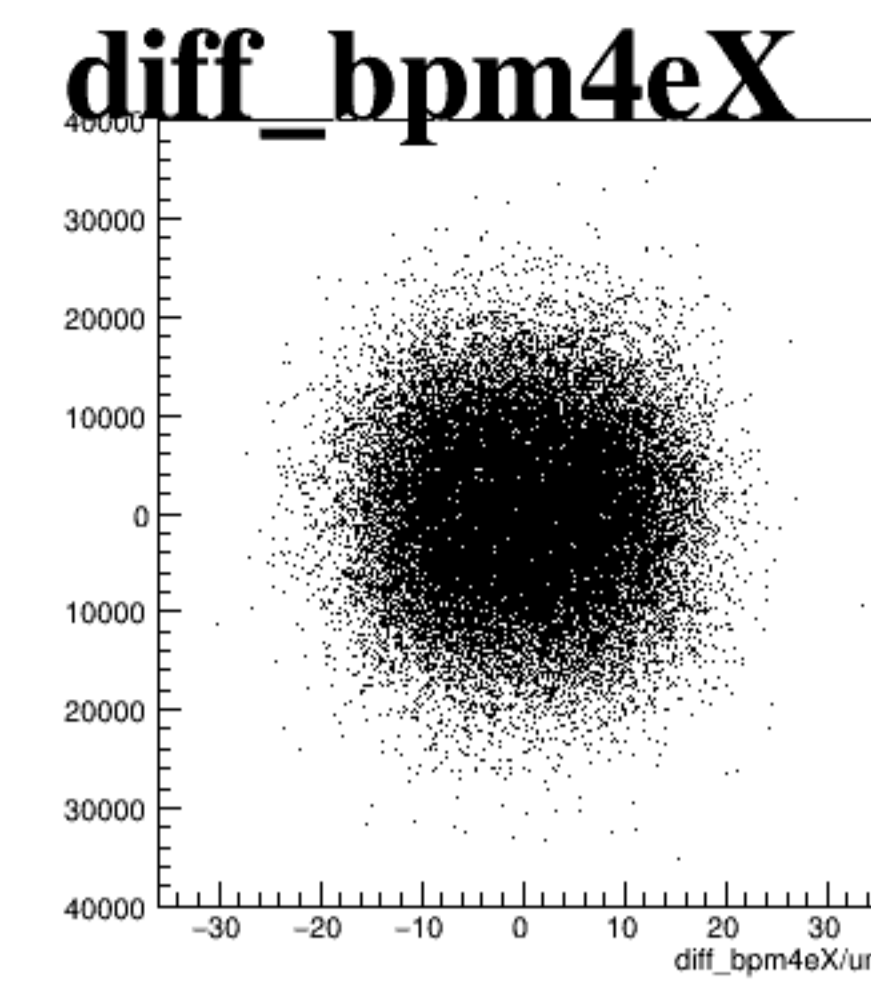
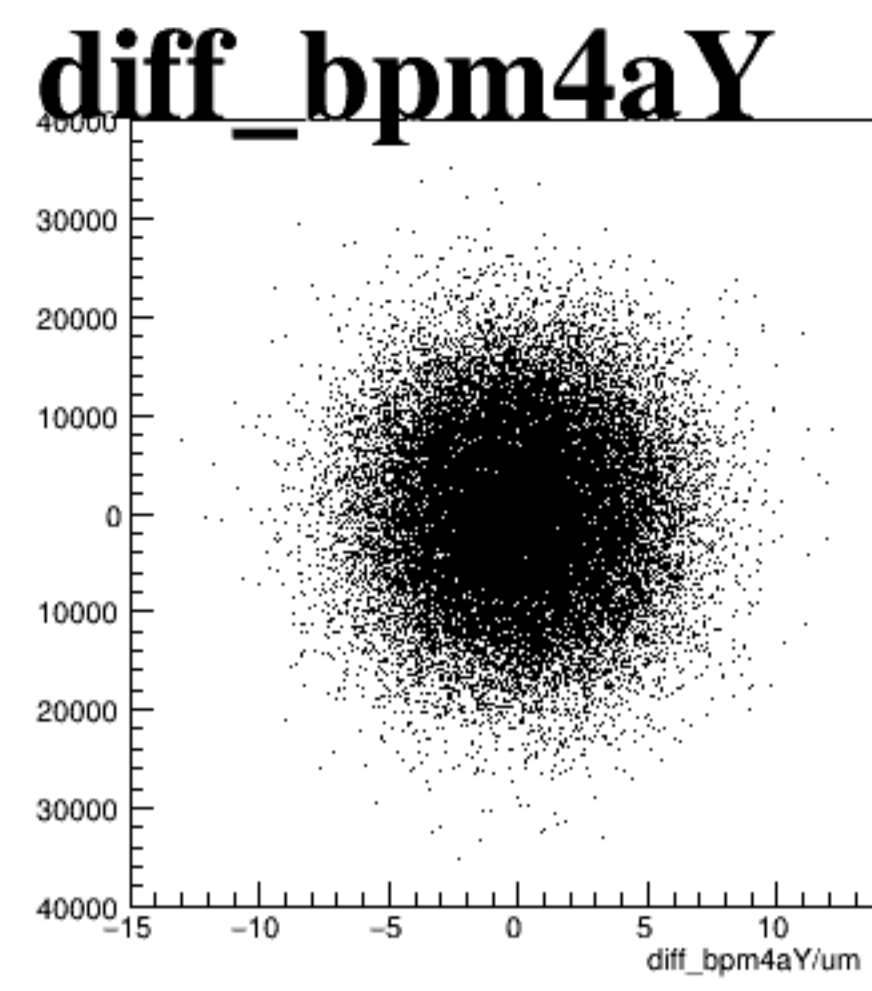
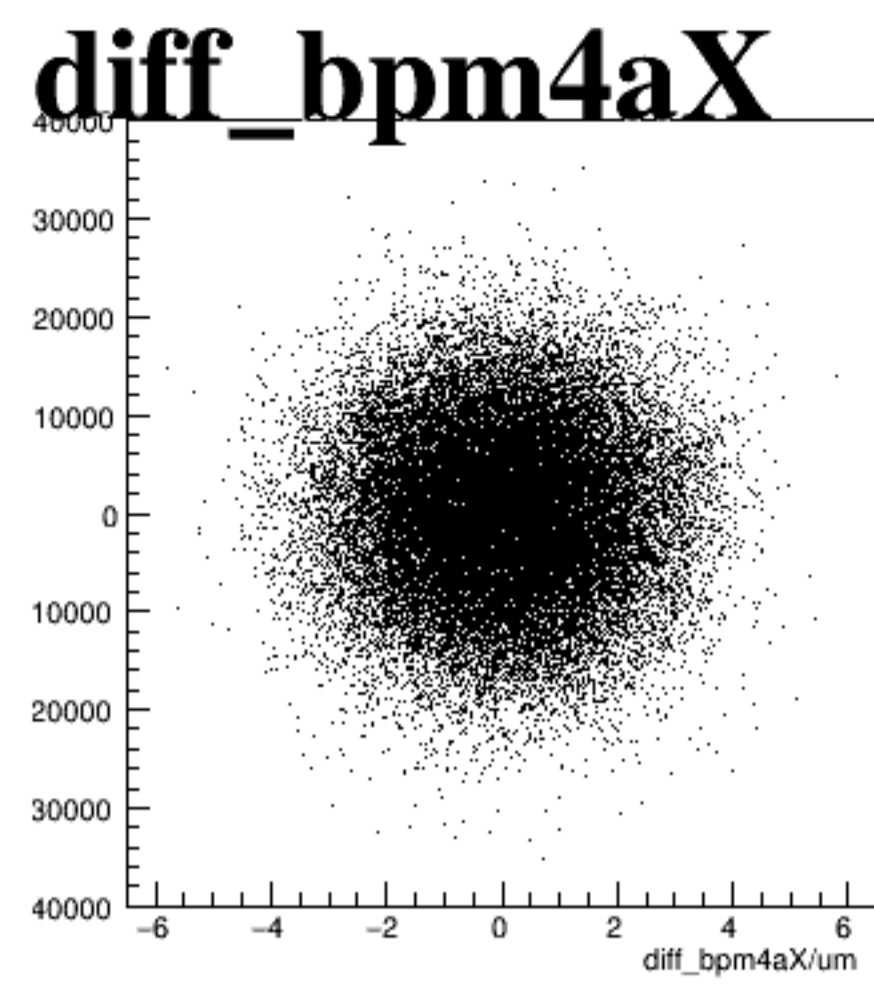
asym_atl2



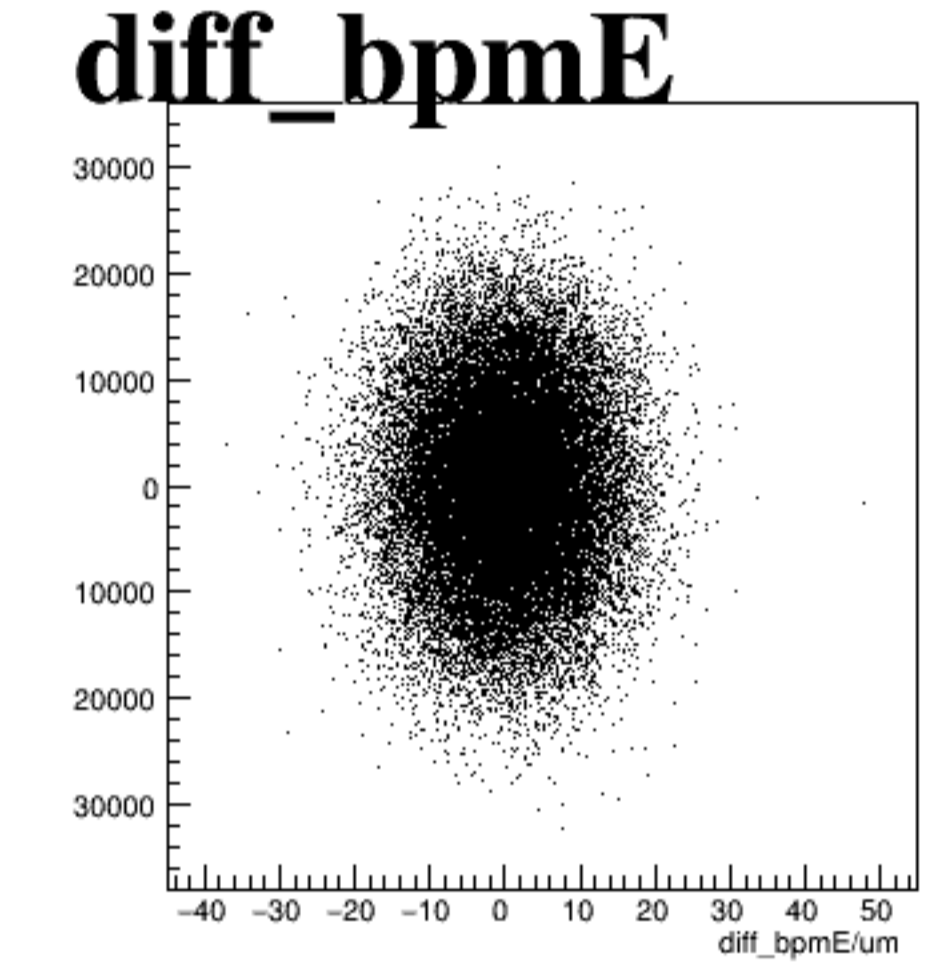
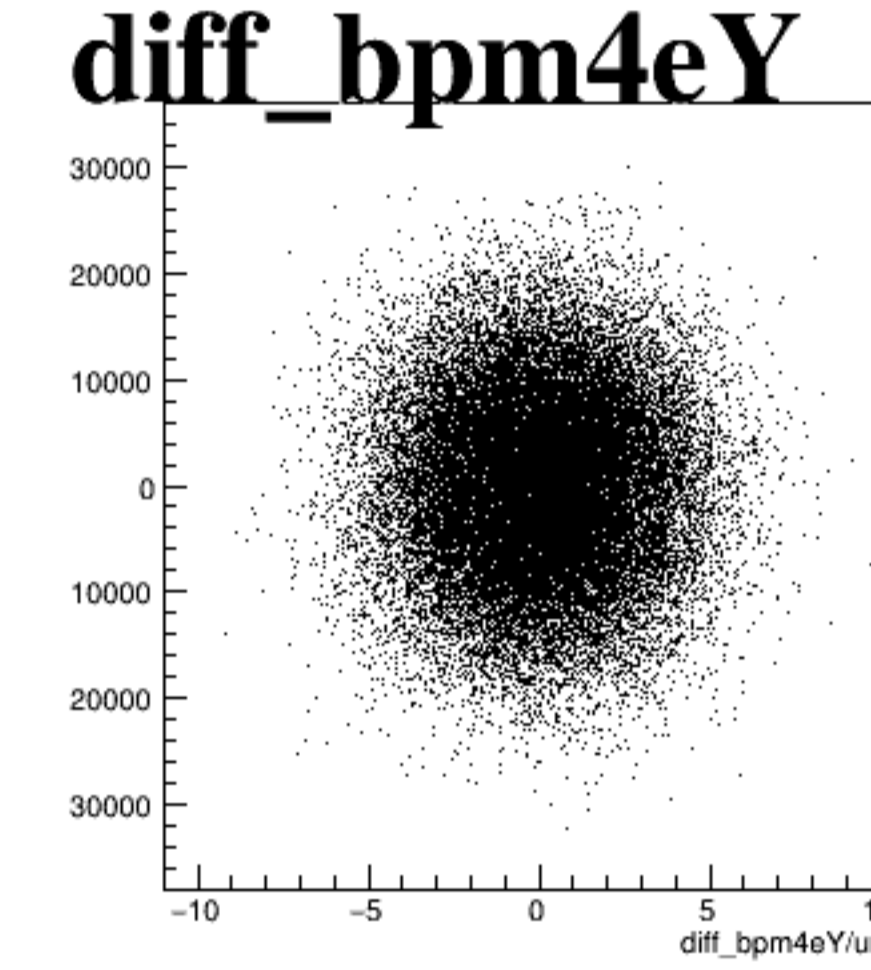
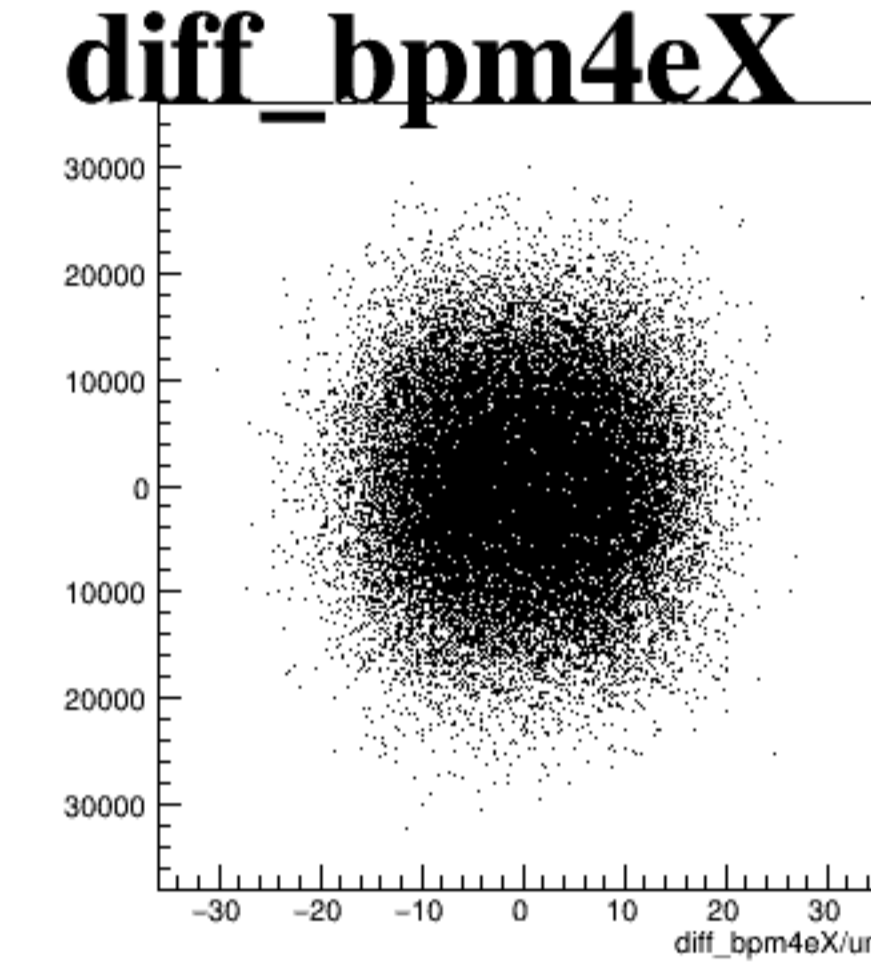
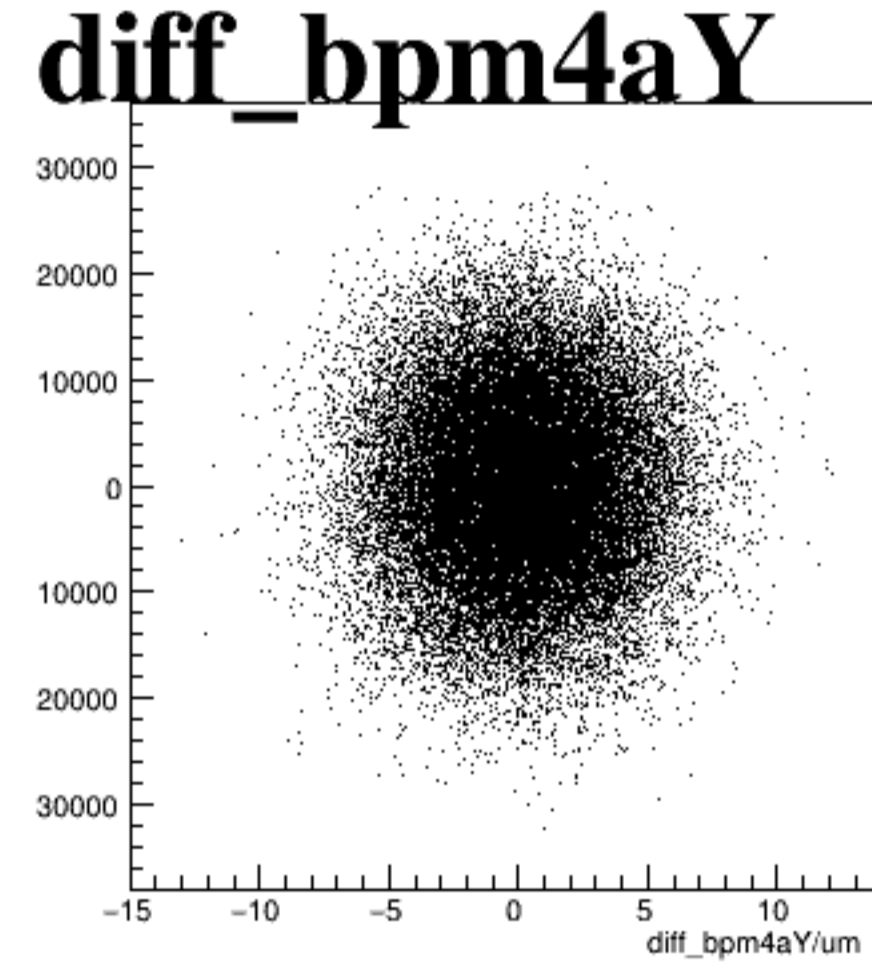
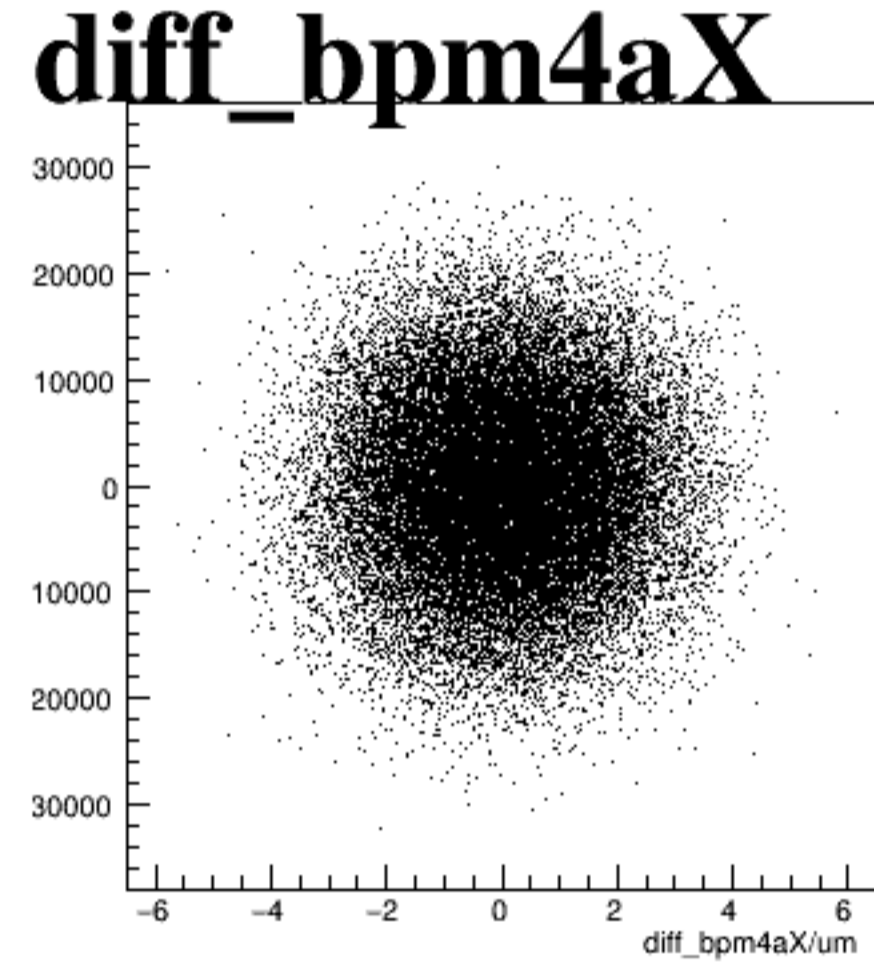
asym_atr2



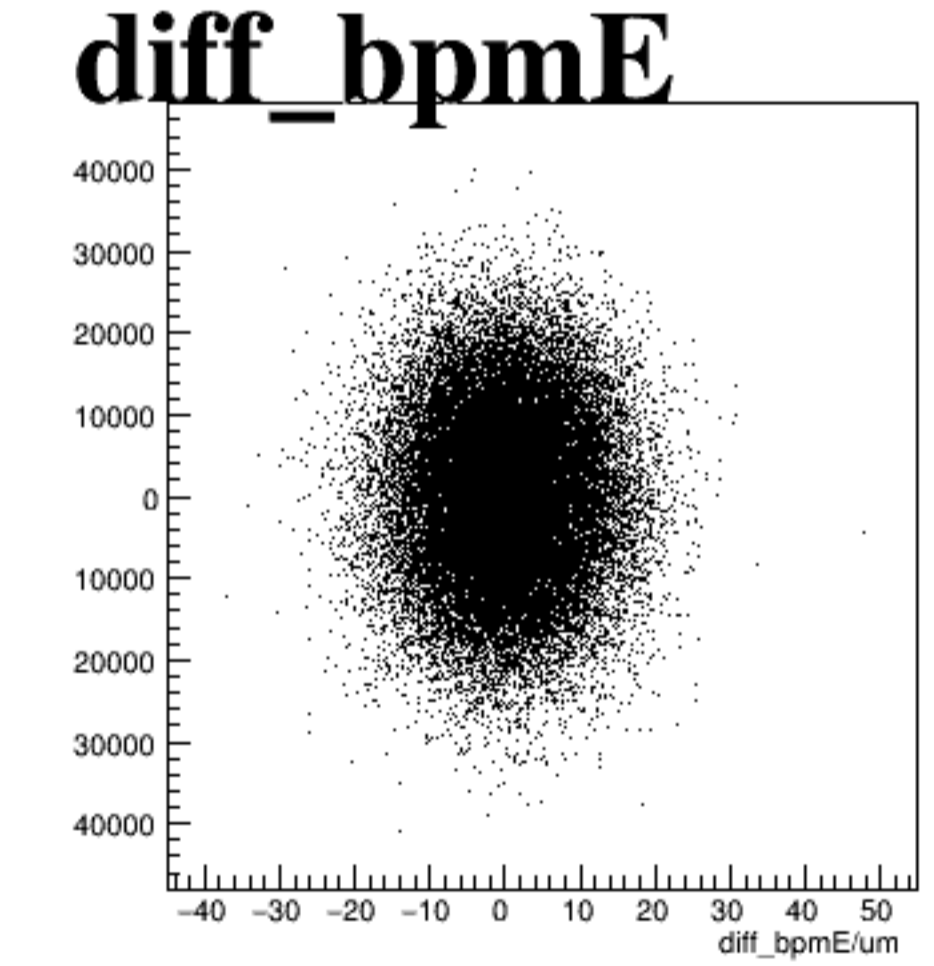
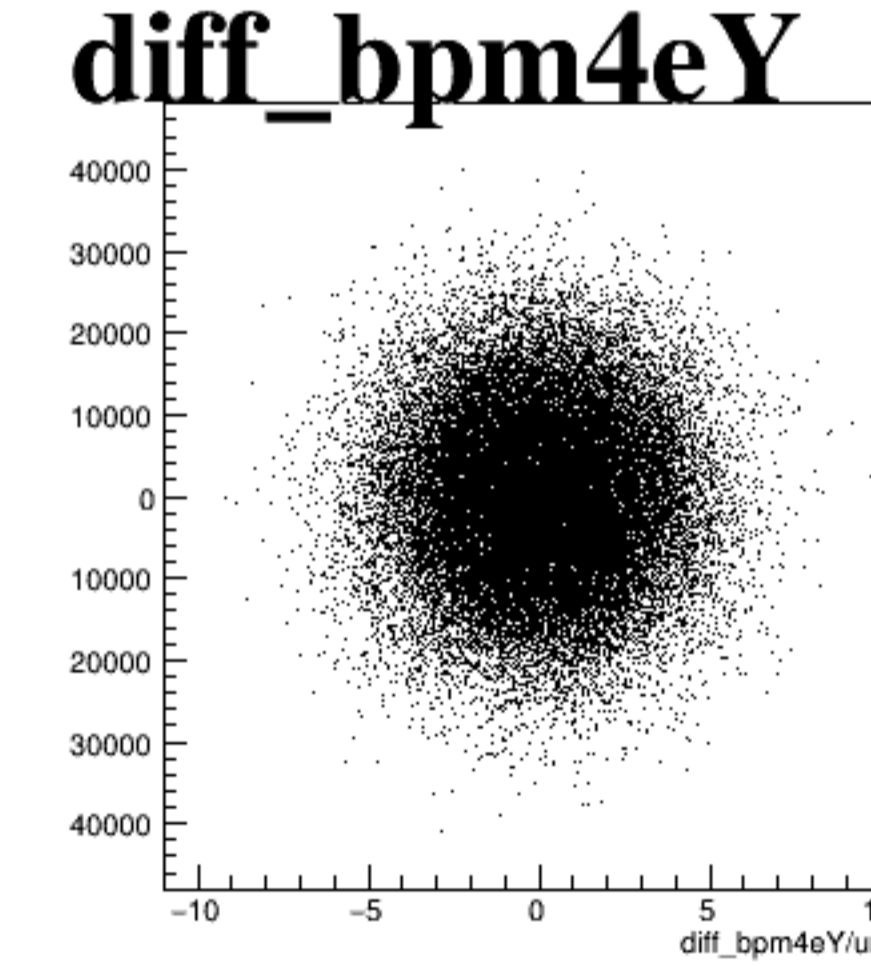
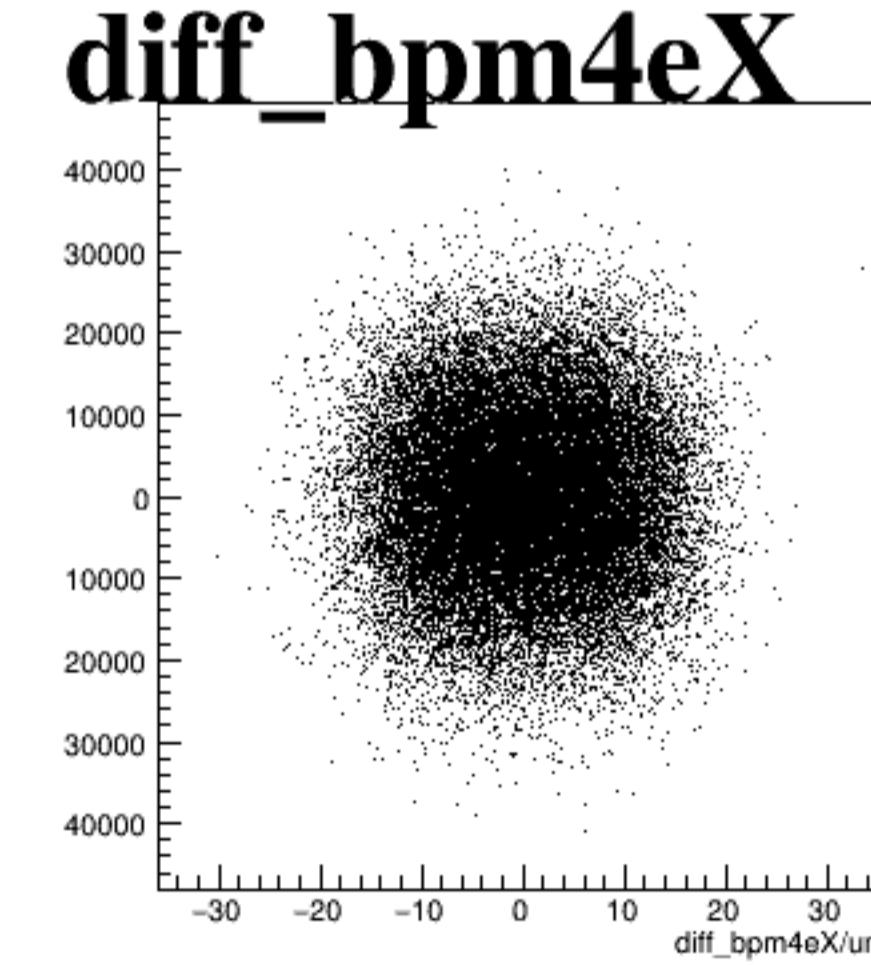
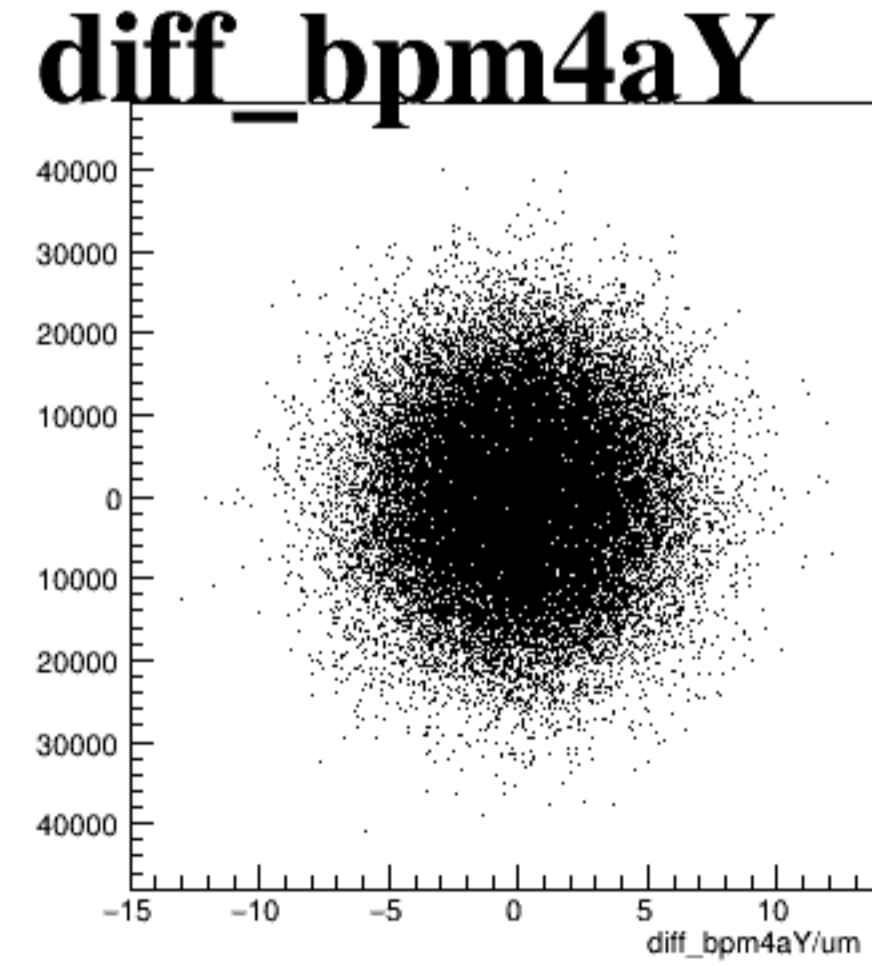
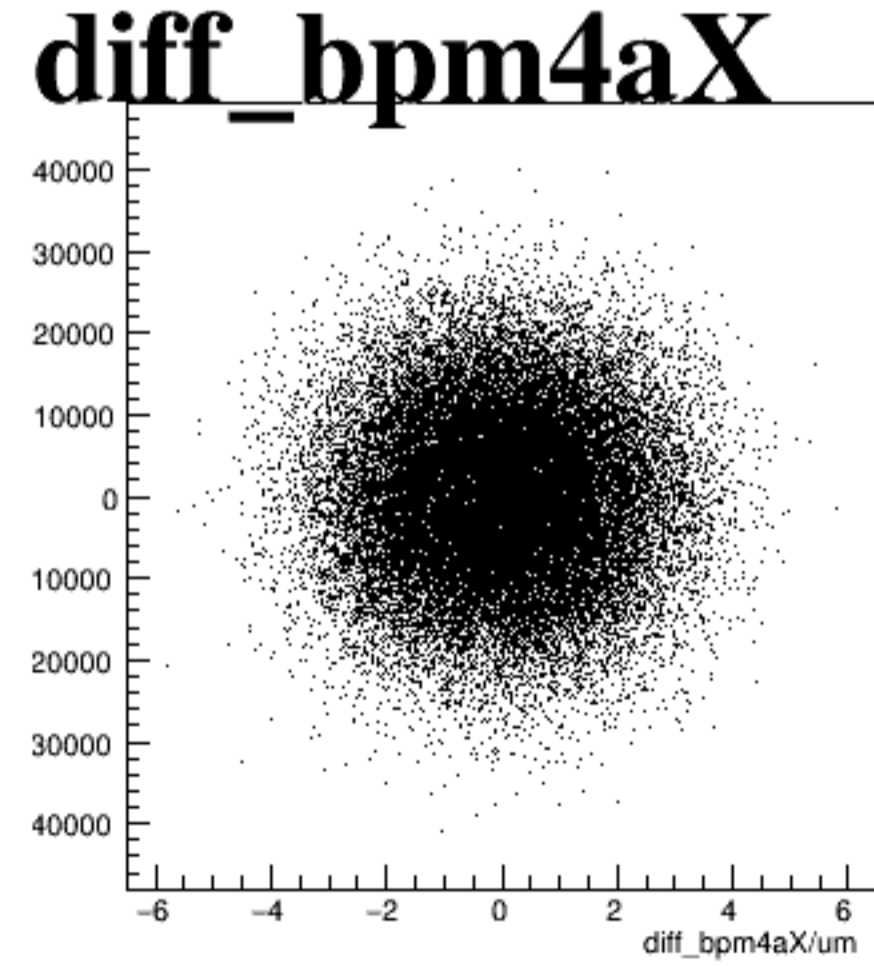
reg_asym_atl1



reg_asym_atr1



reg_asym_atl2



reg_asym_atr2

