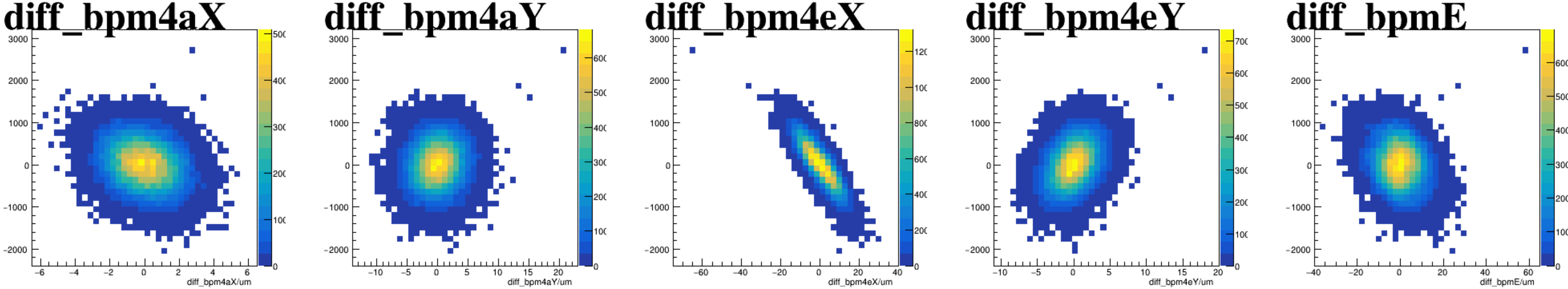
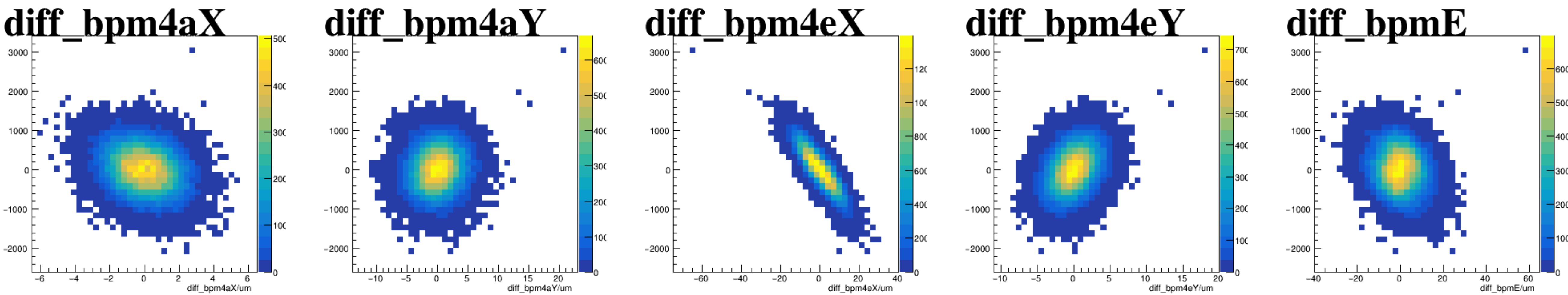


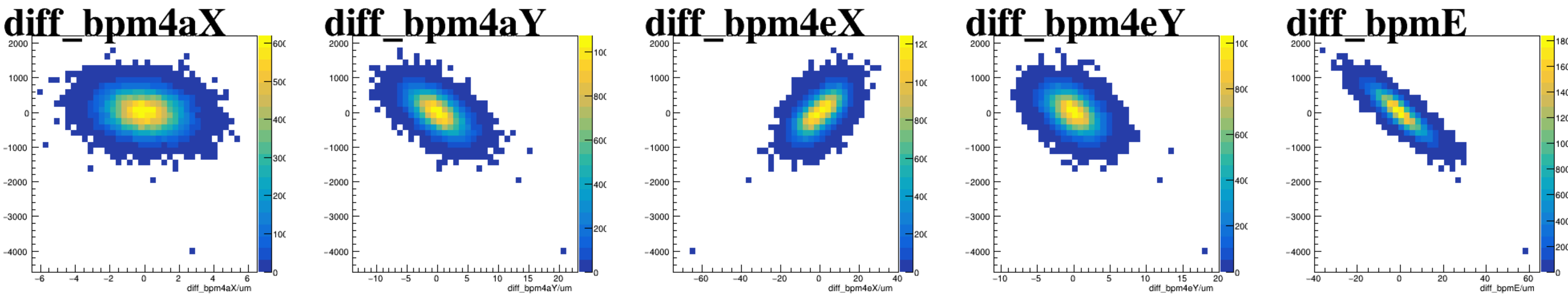
asym_usl



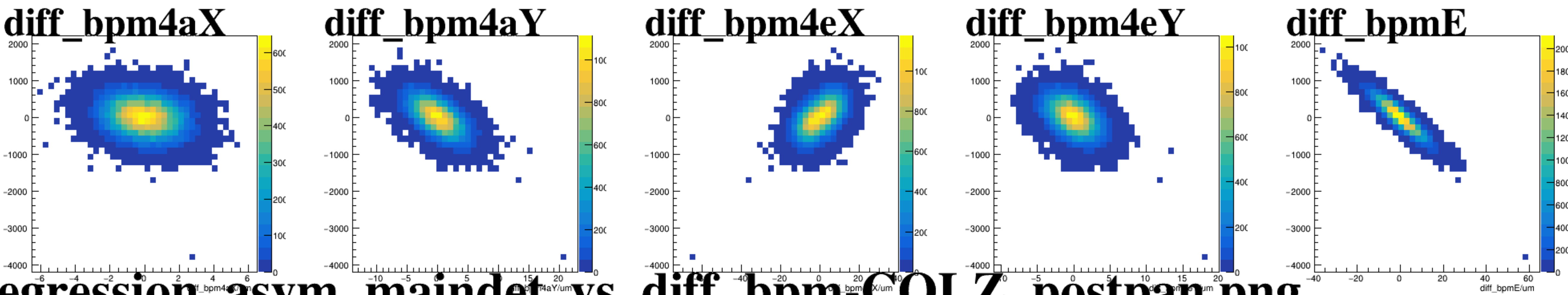
asym_dsl



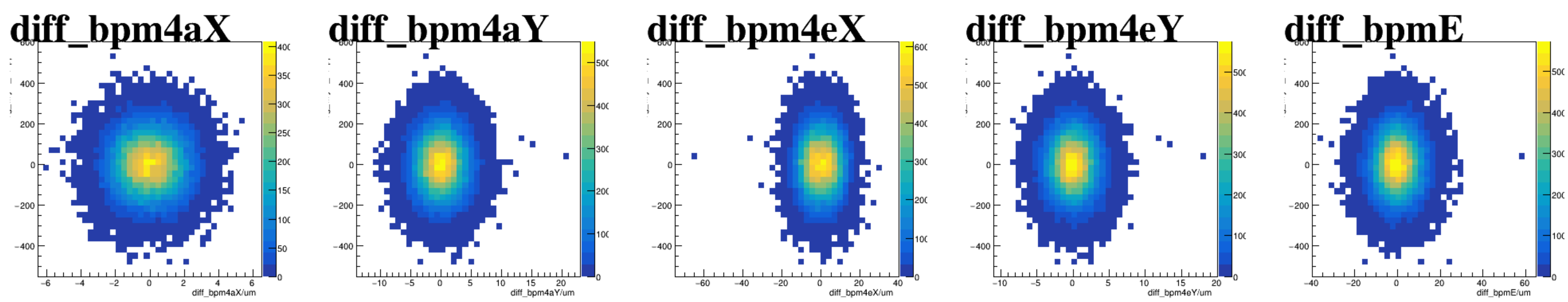
asym_usr



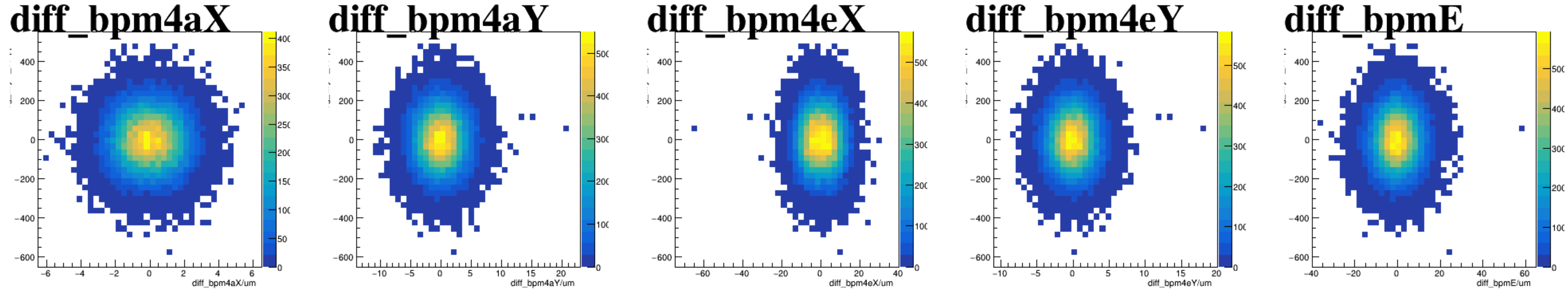
asym_dsr



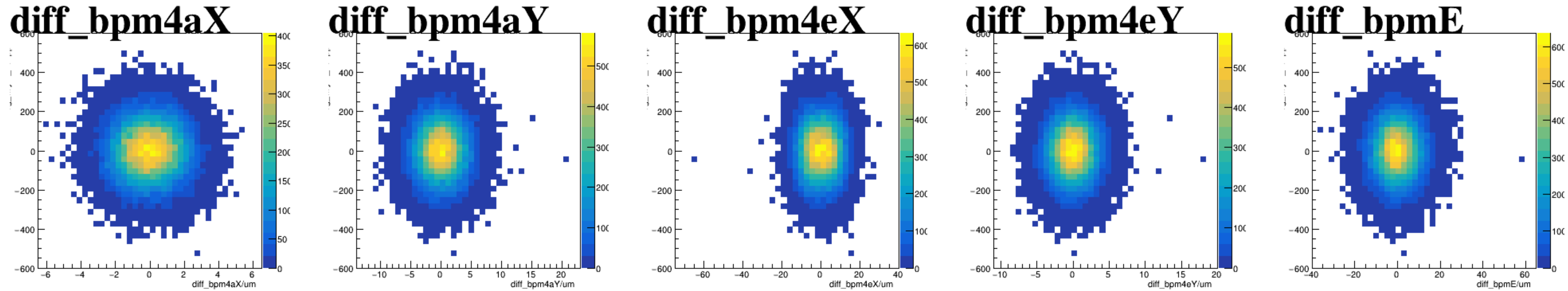
reg_asym_usl



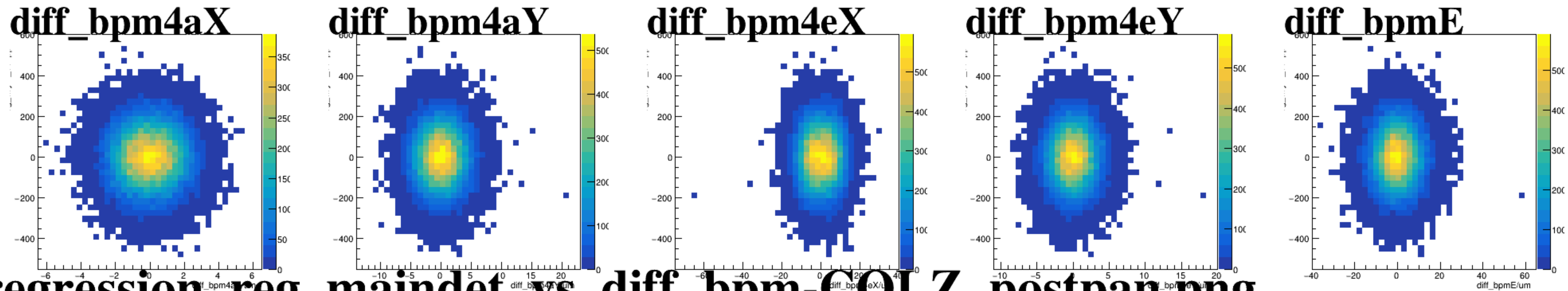
reg_asym_dsl



reg_asym_usr

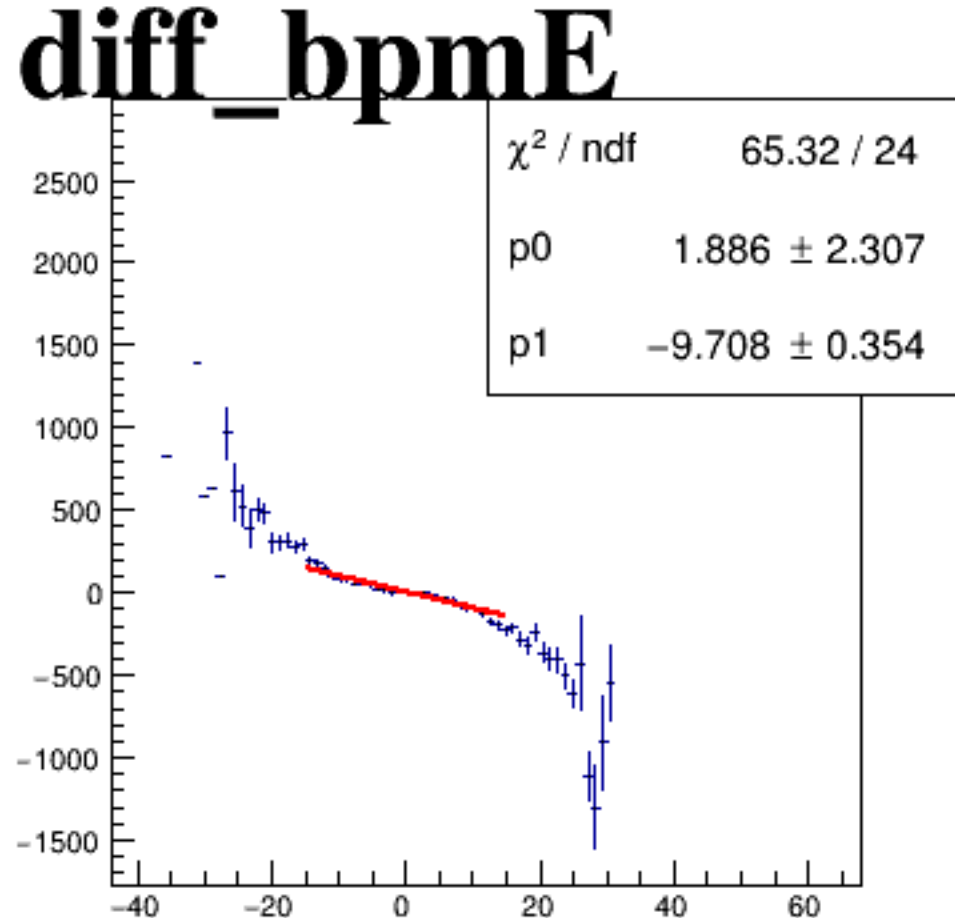
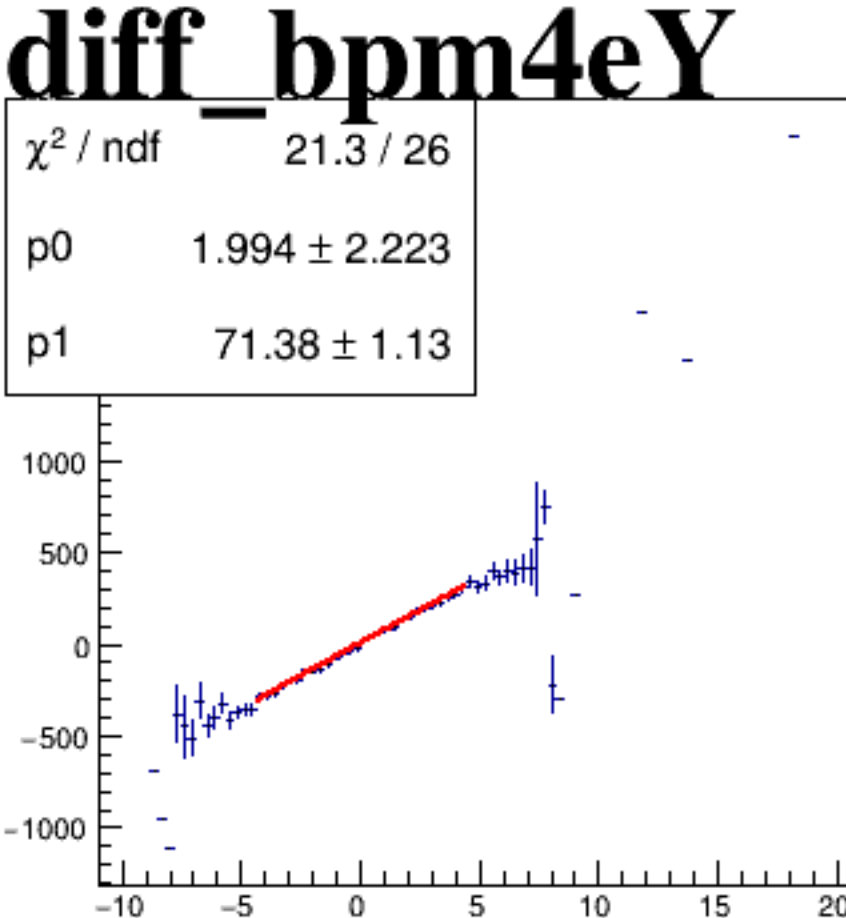
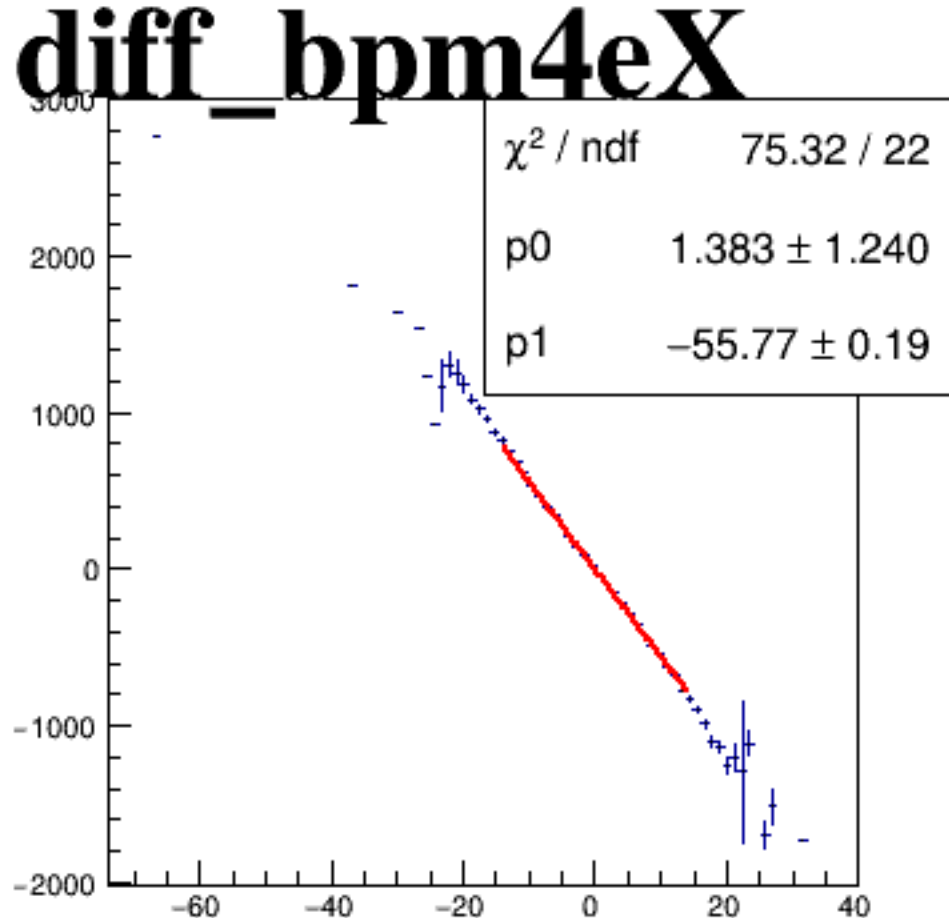
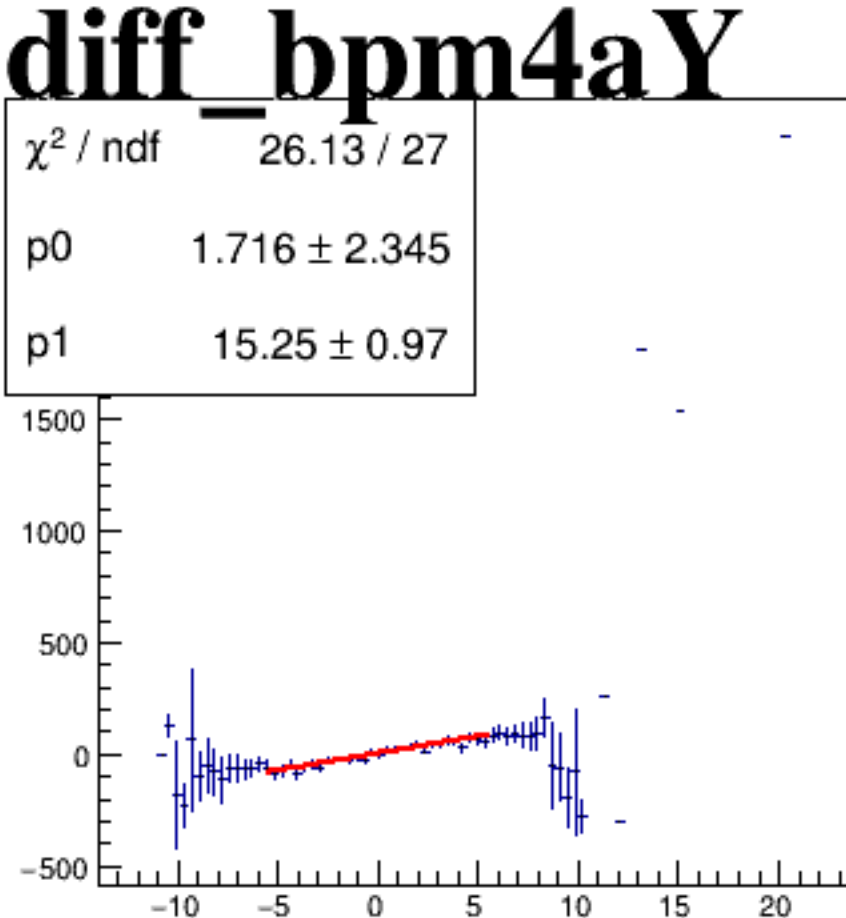
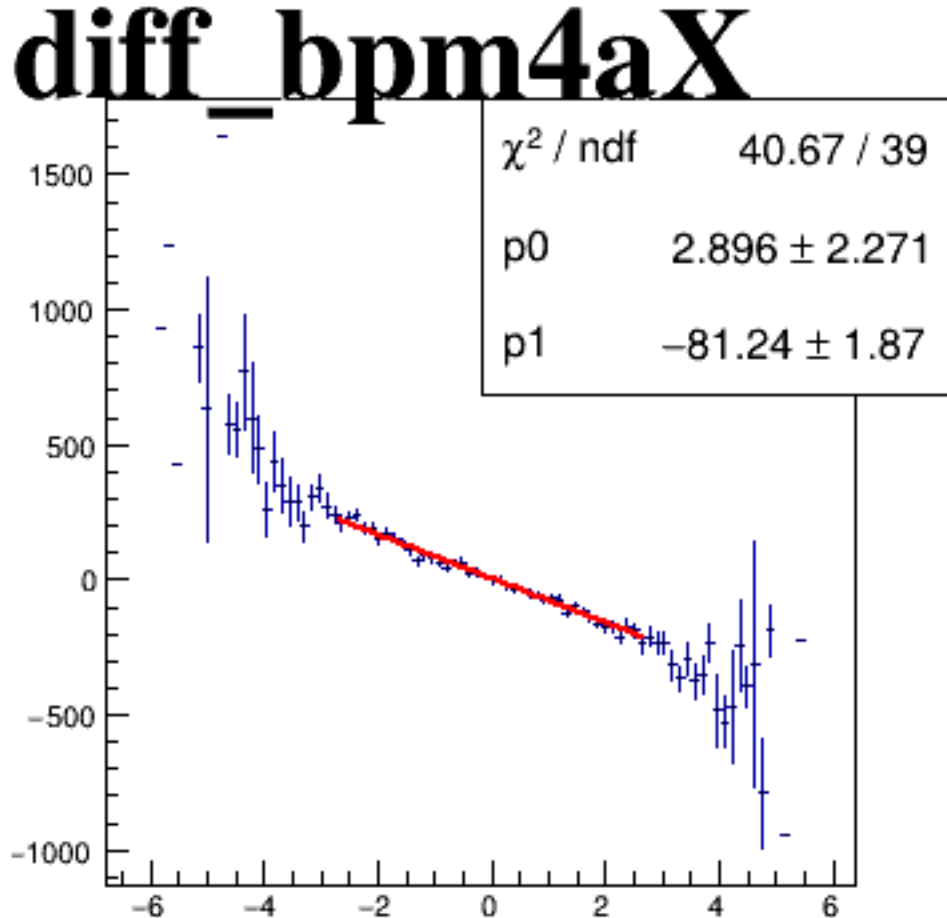


reg_asym_dsr

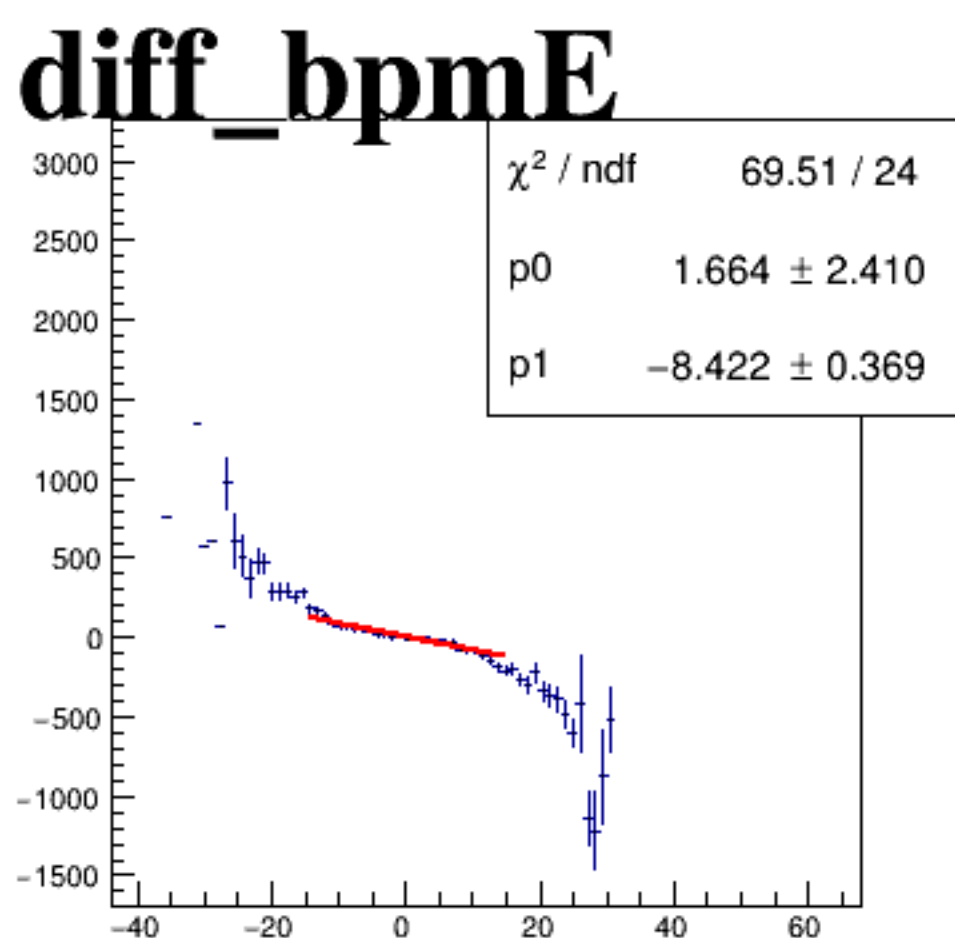
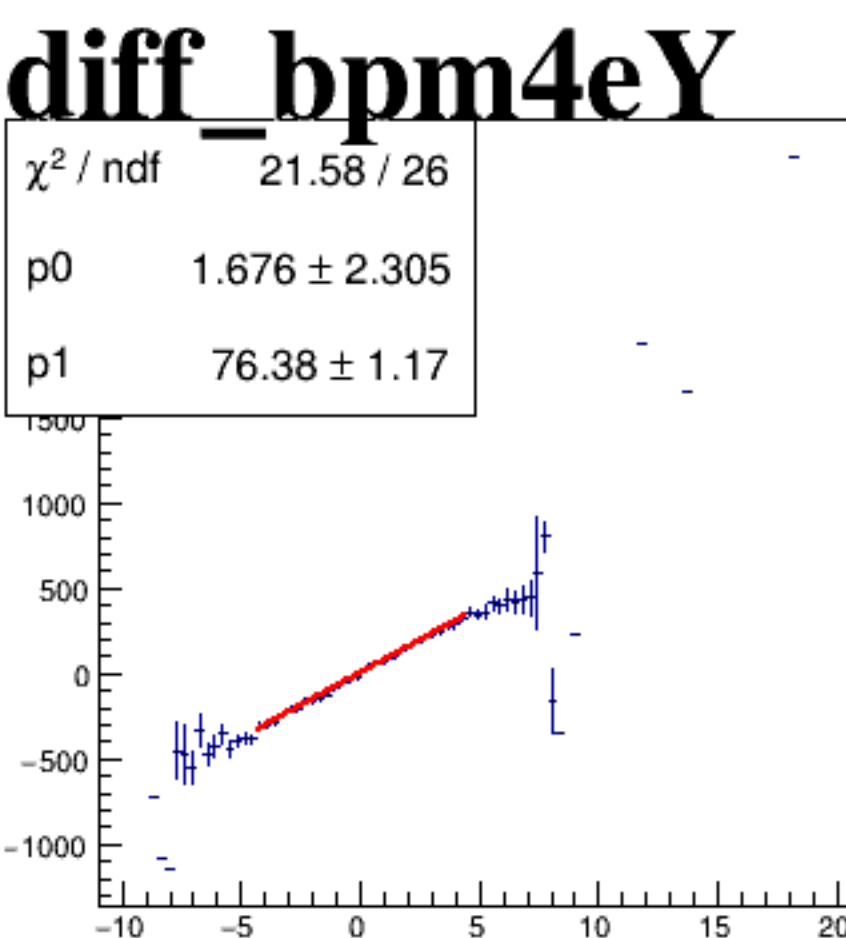
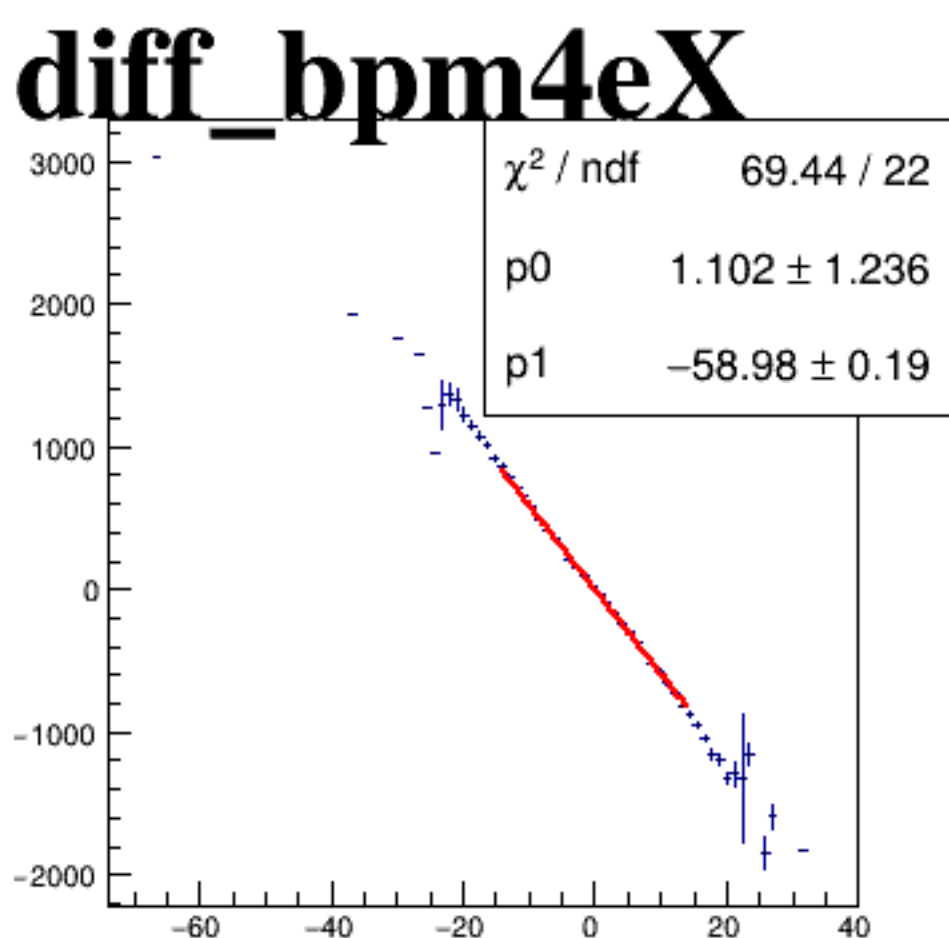
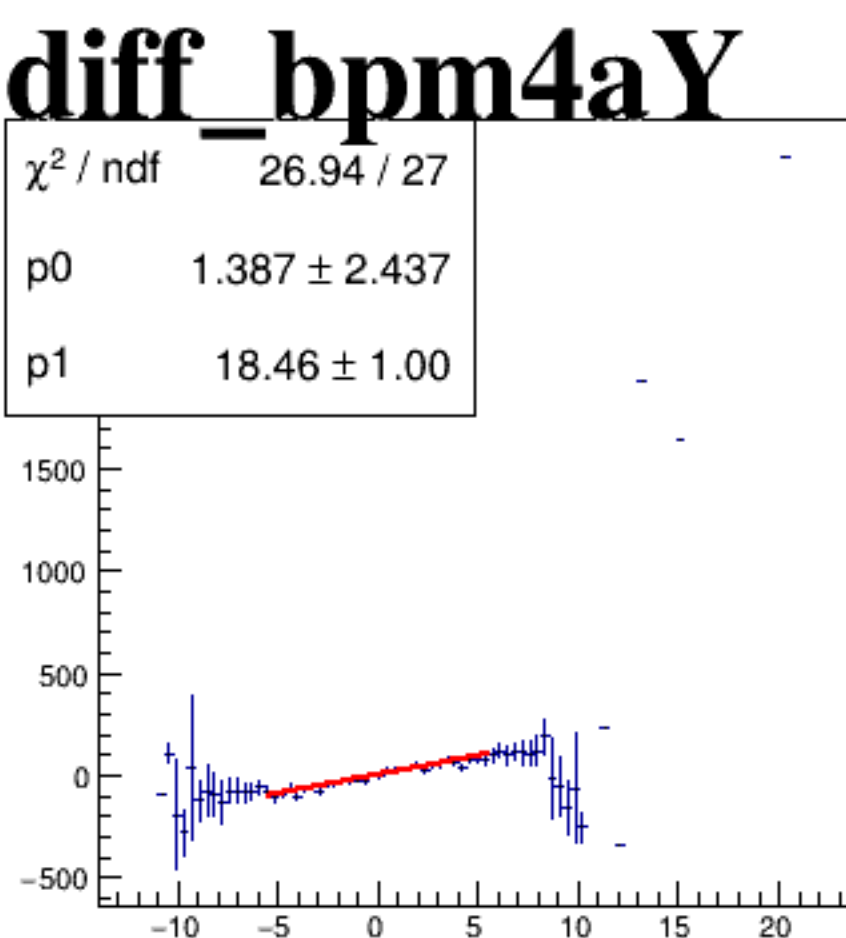
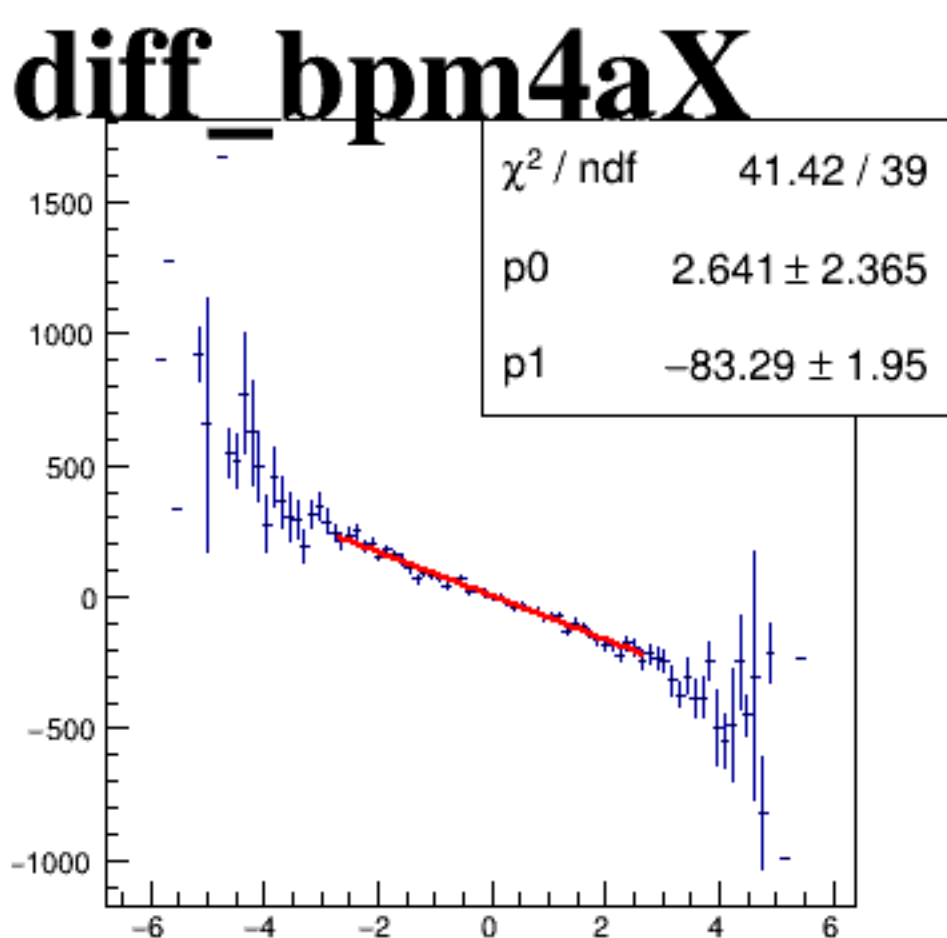


run4905_000_regression_reg_maindet_vs_diff_bpm-COLZ_postpan.png

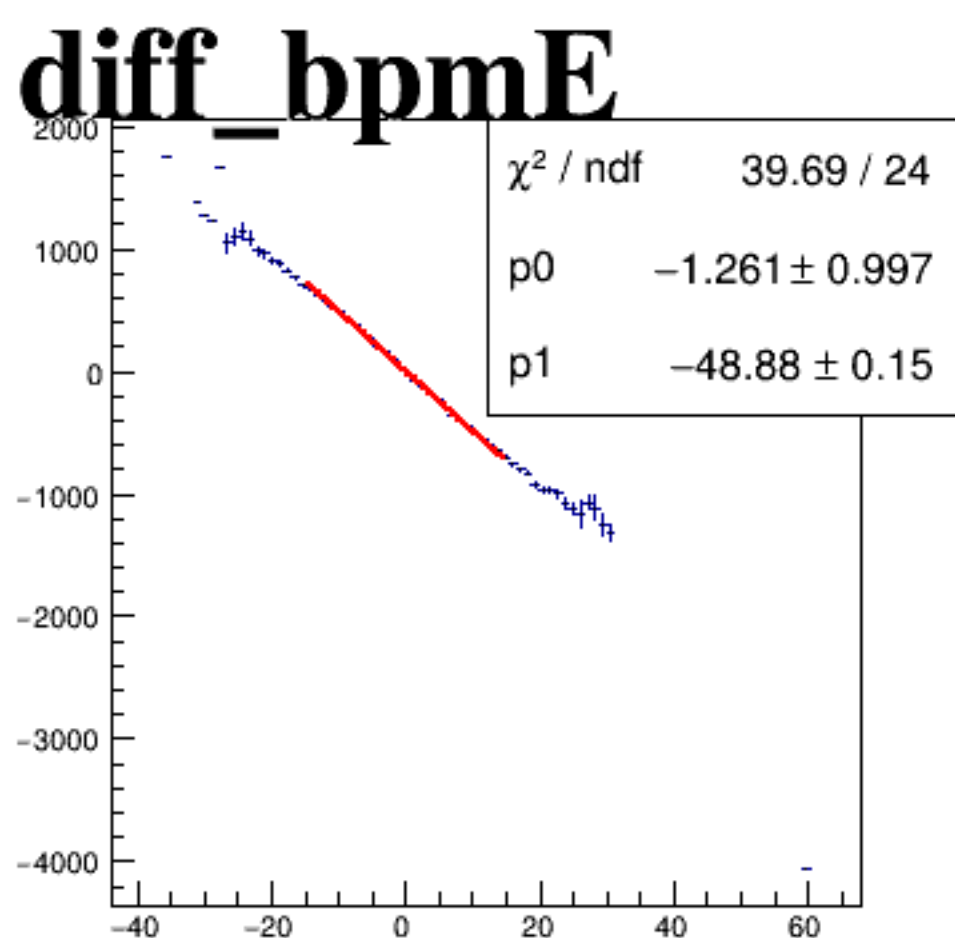
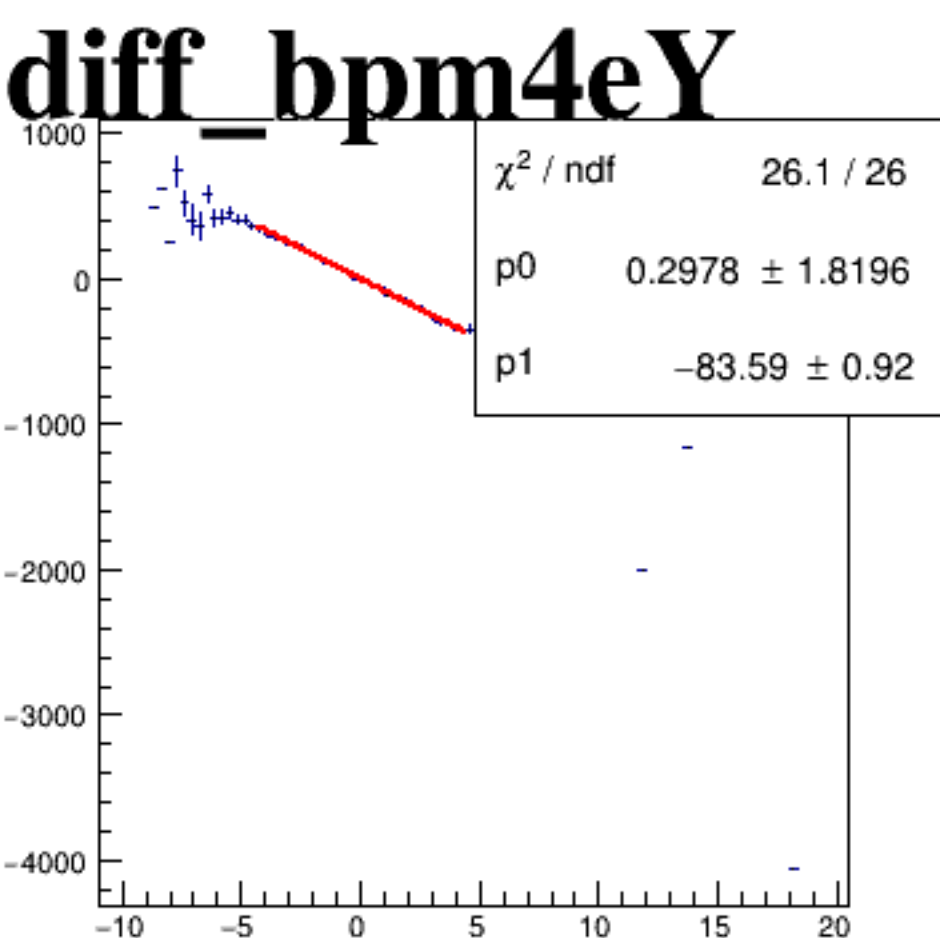
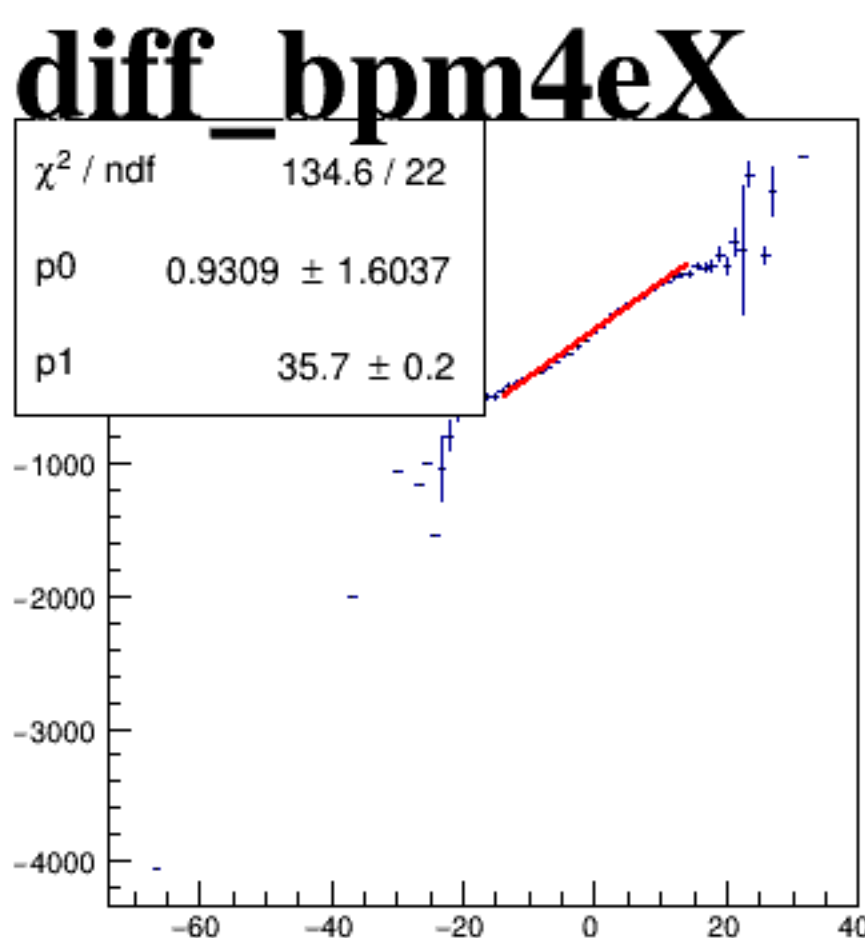
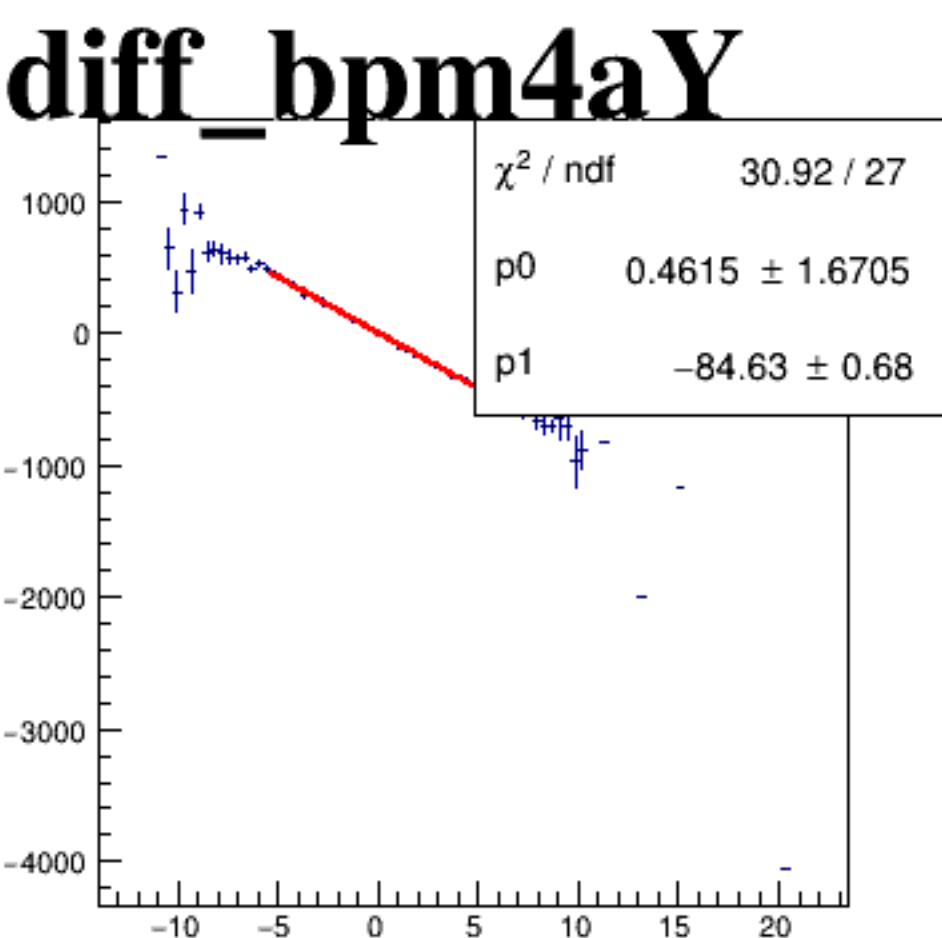
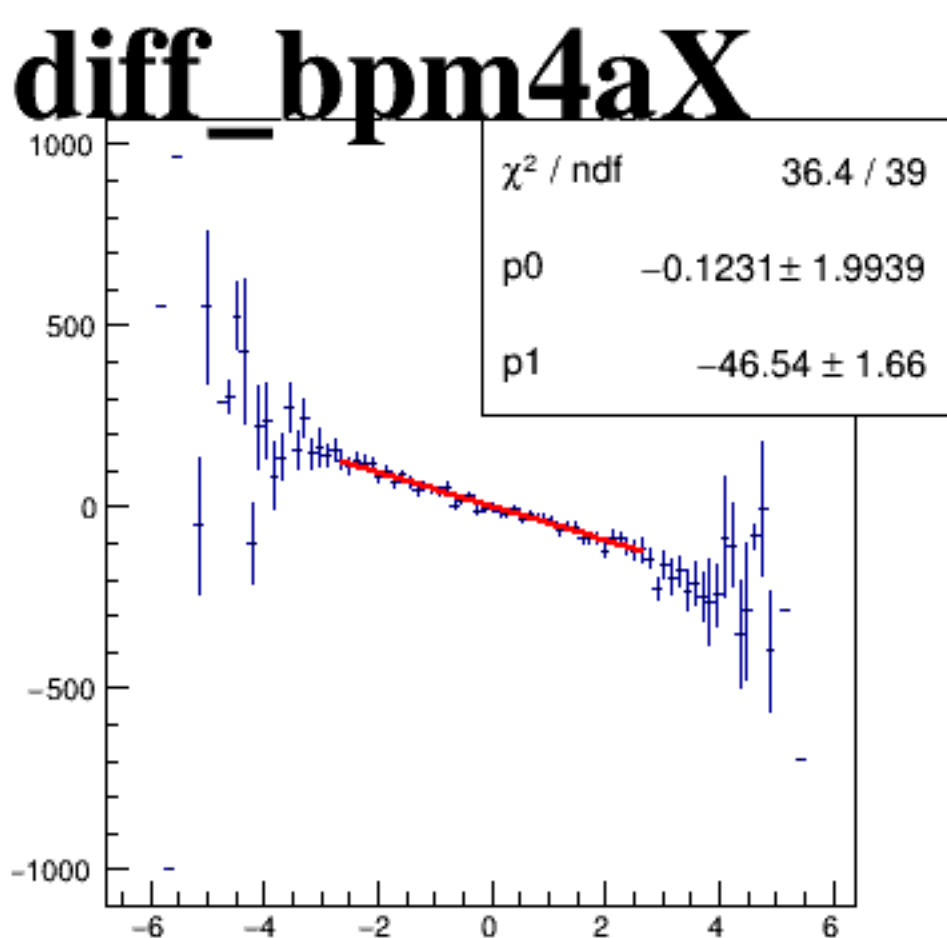
asym_usl



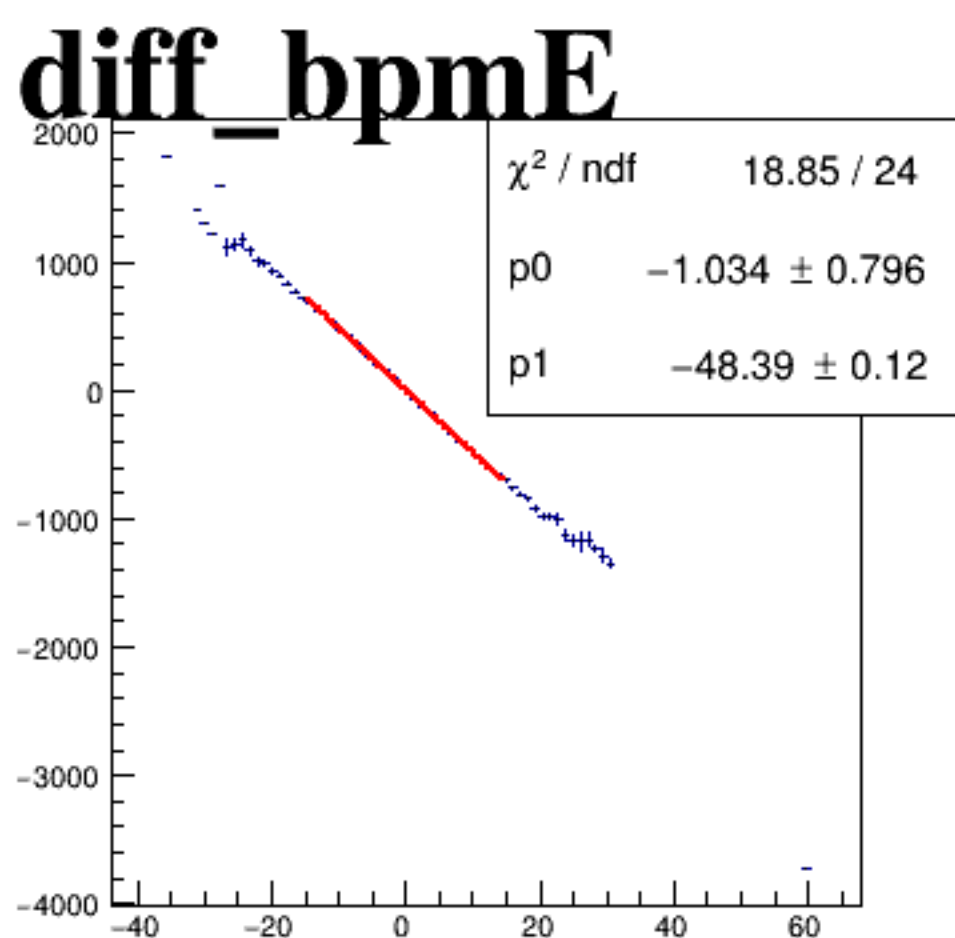
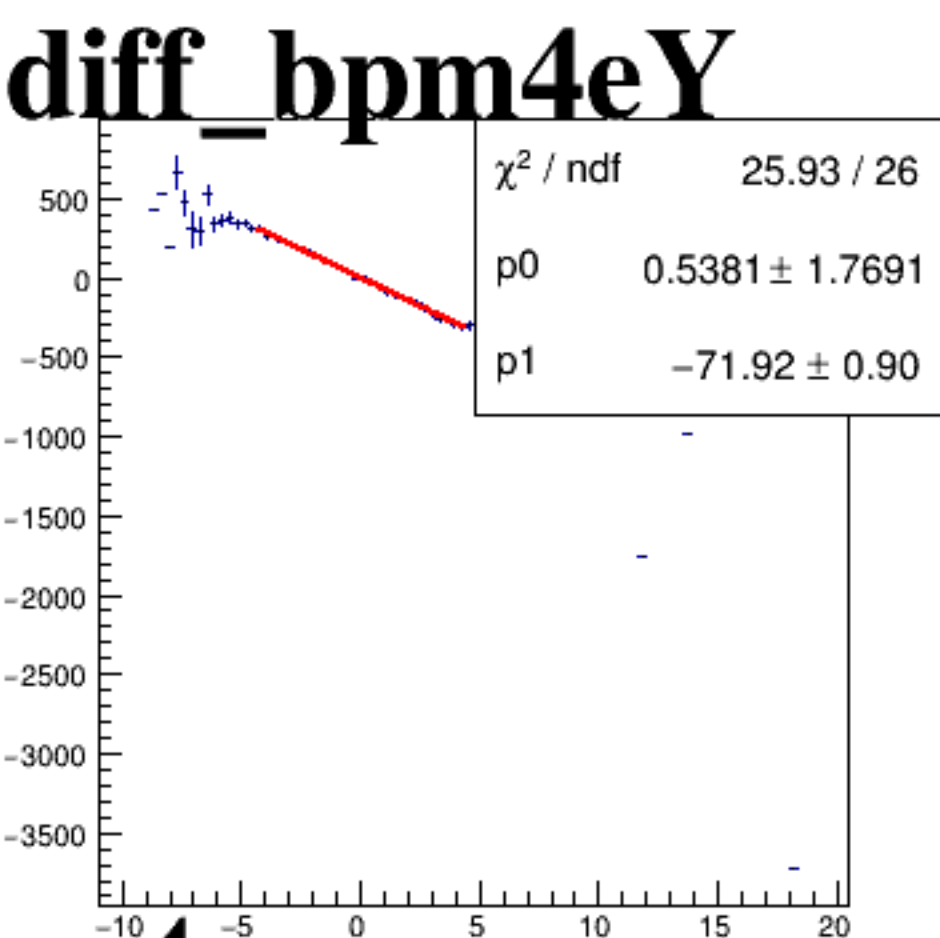
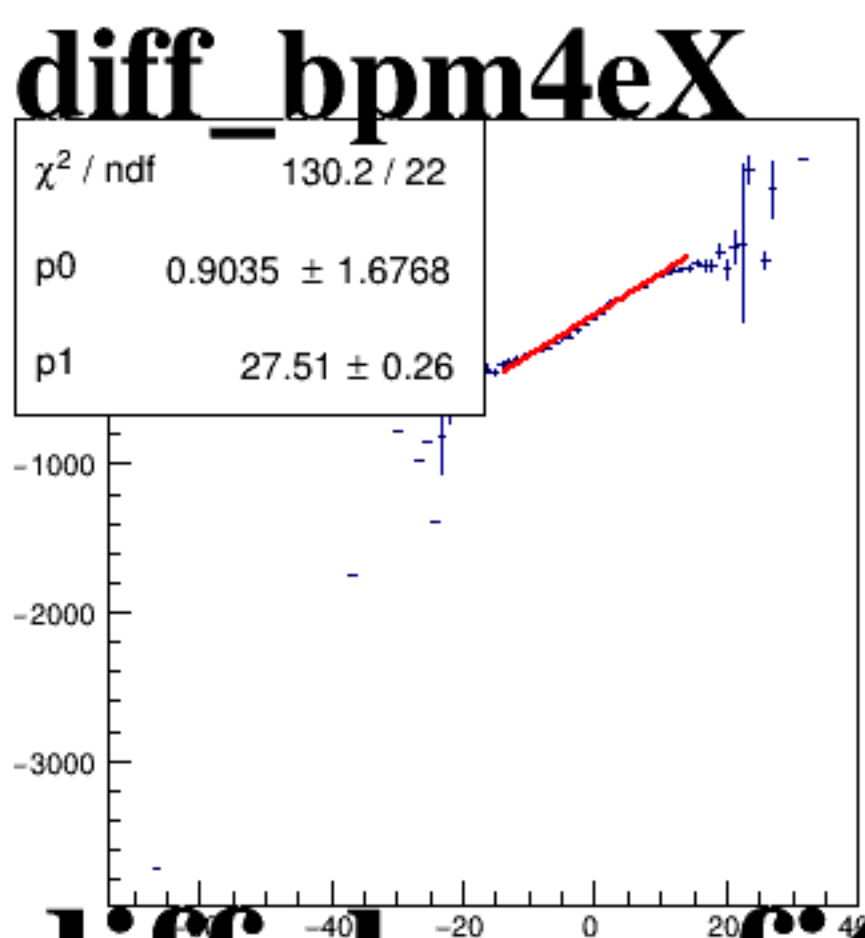
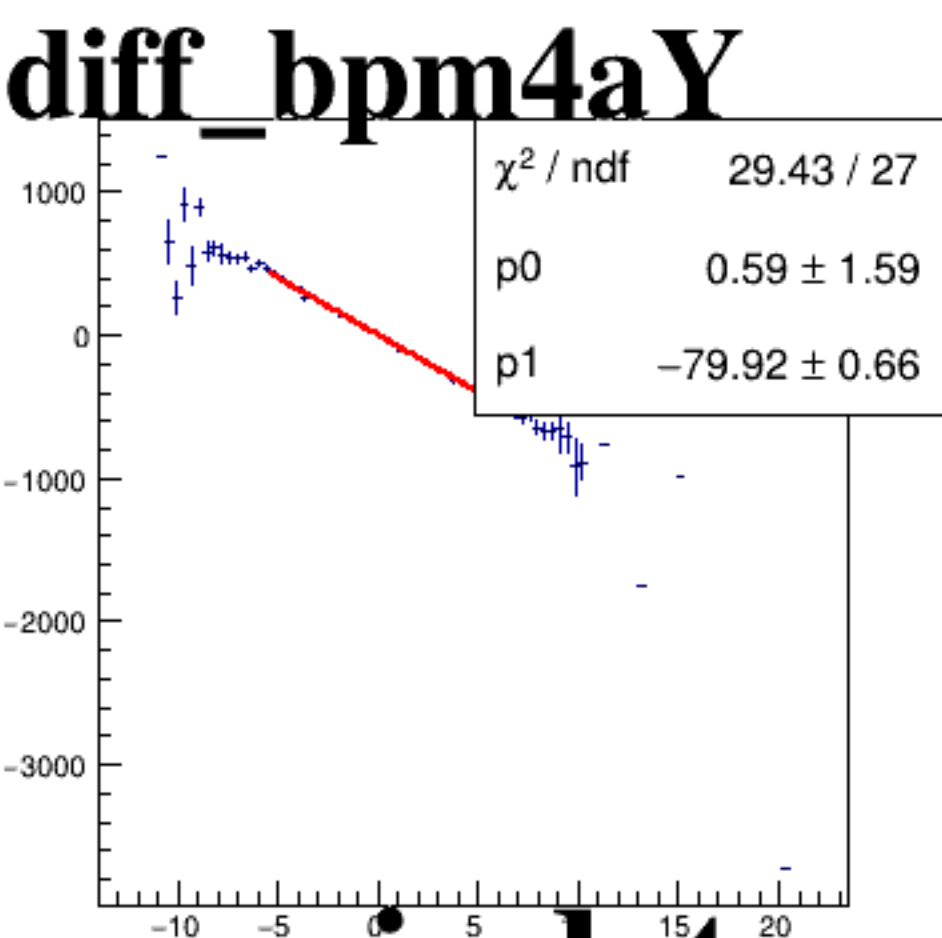
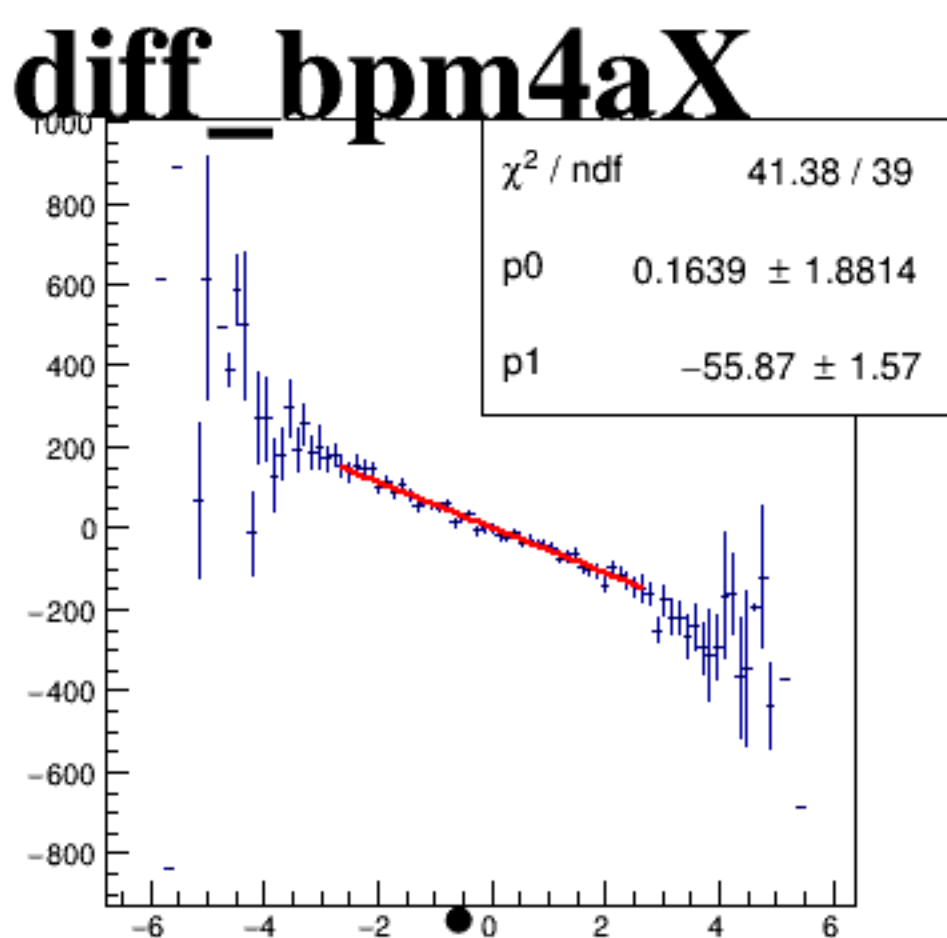
asym_dsl



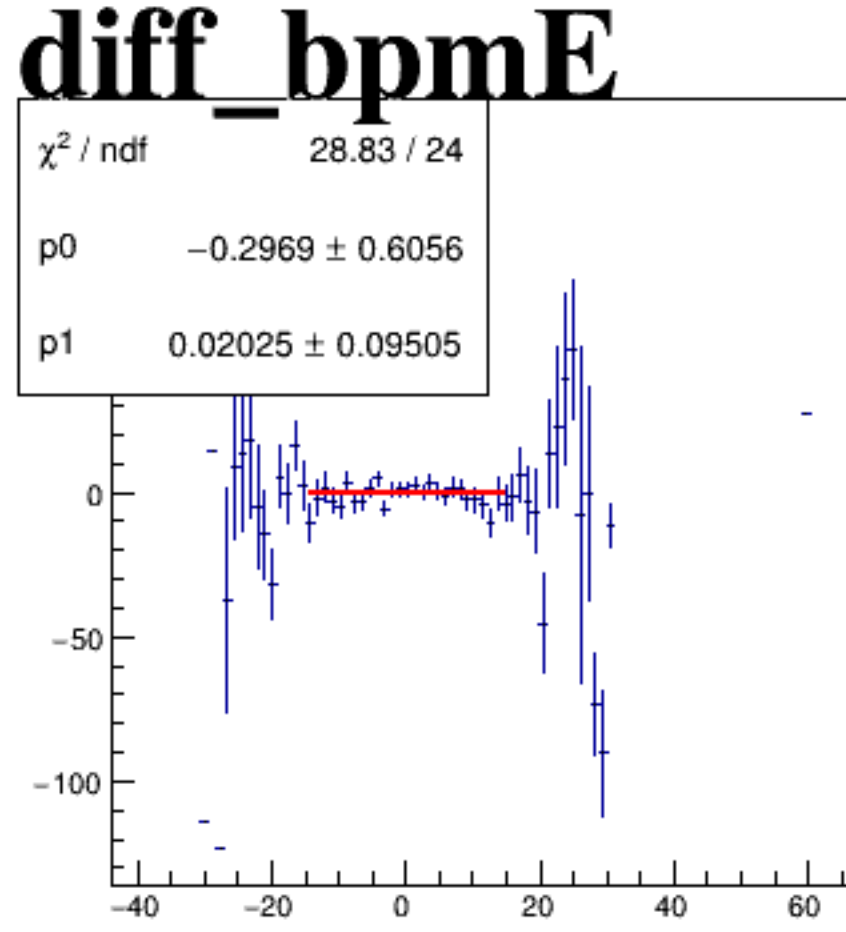
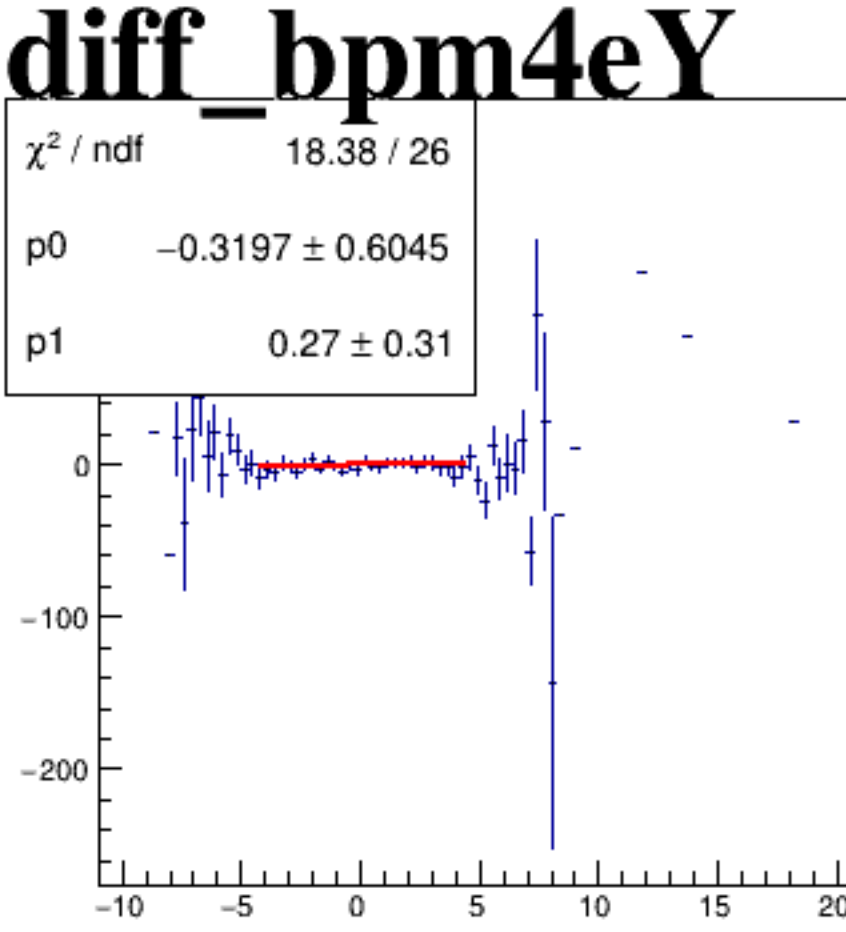
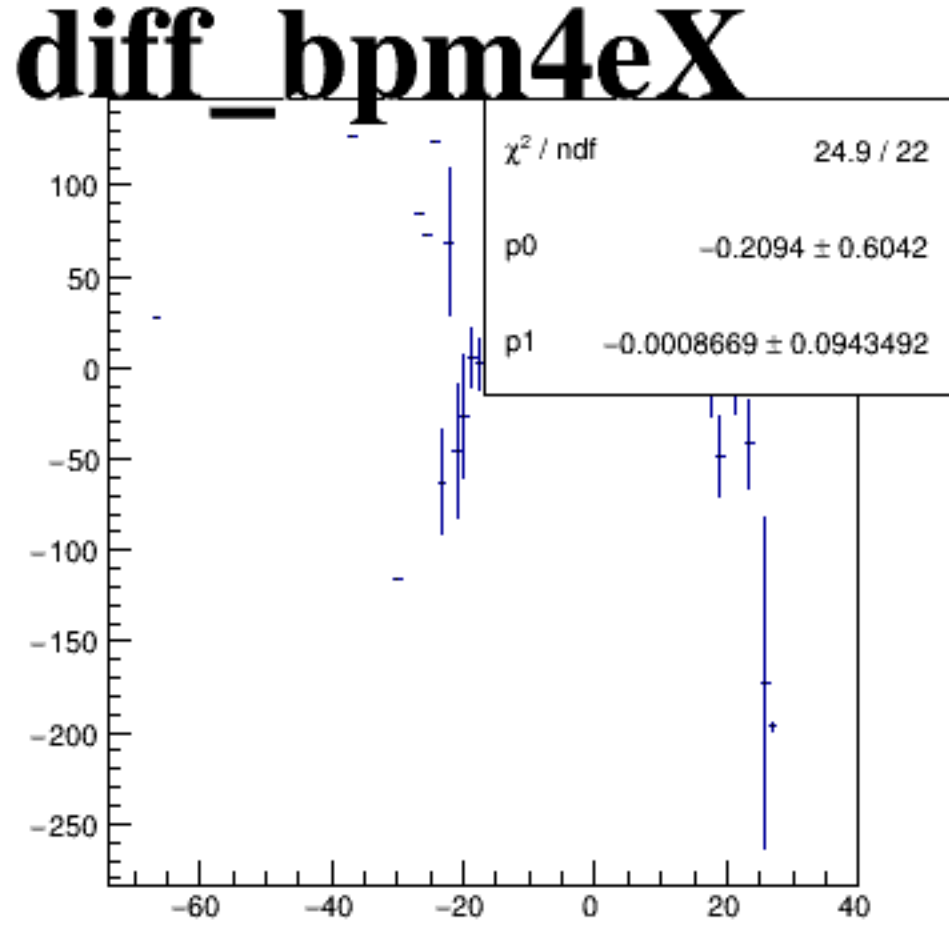
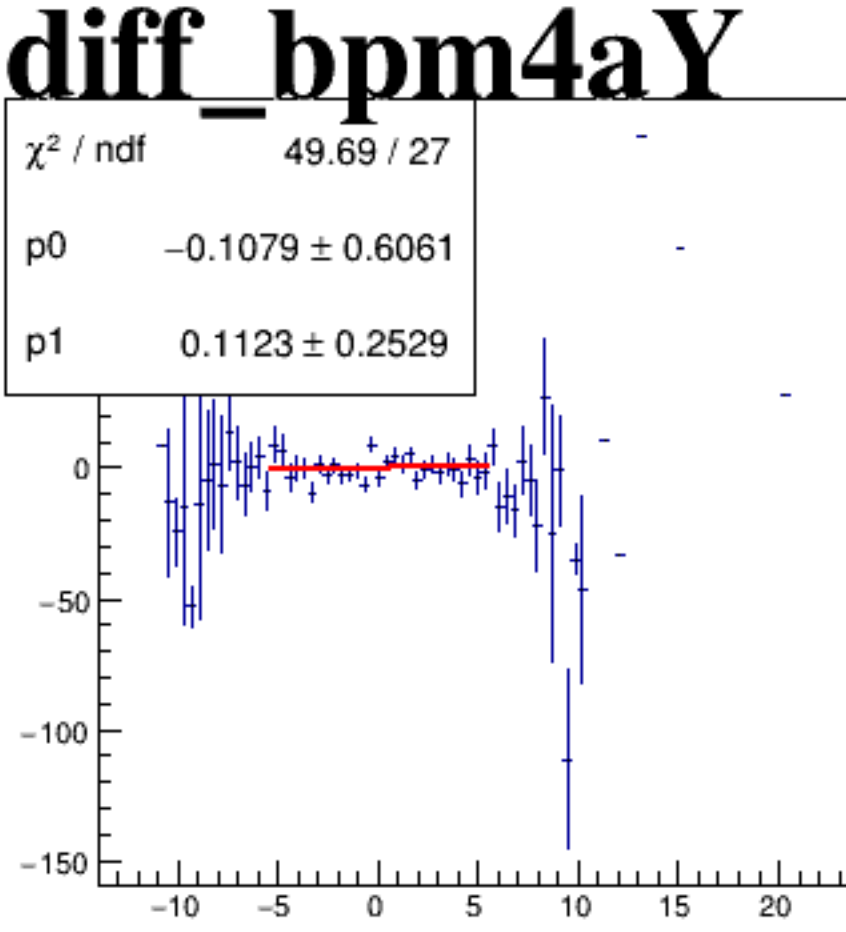
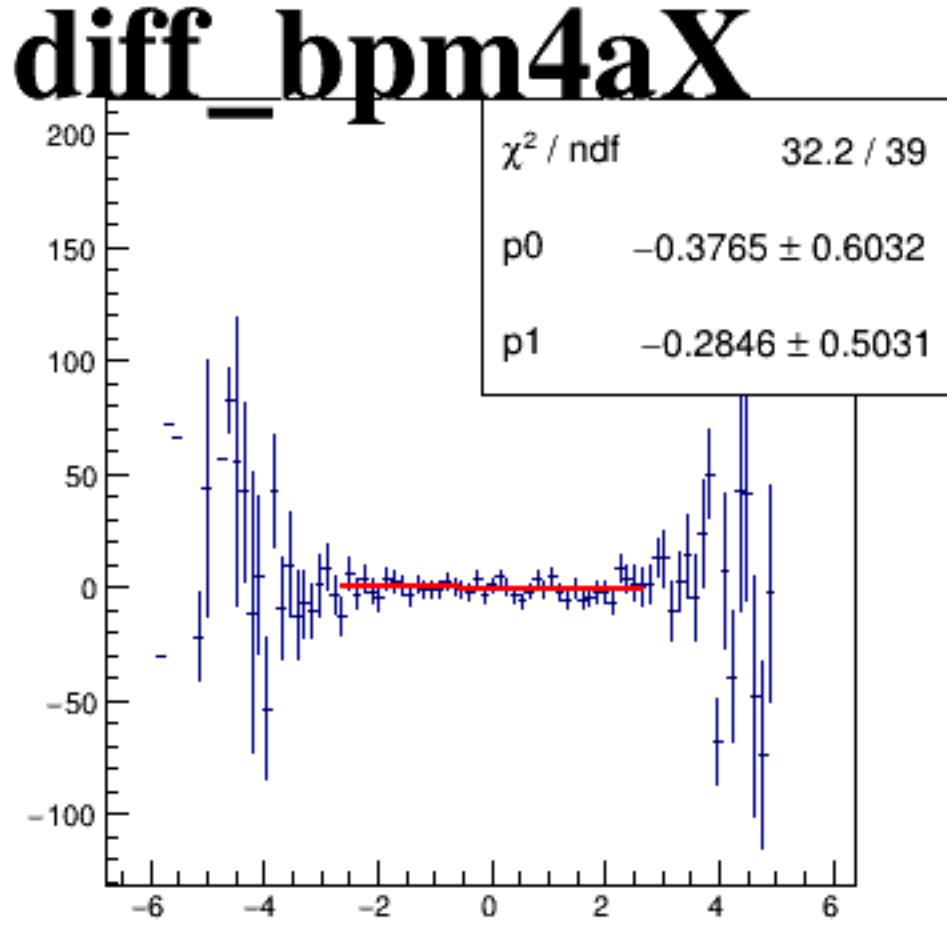
asym_usr



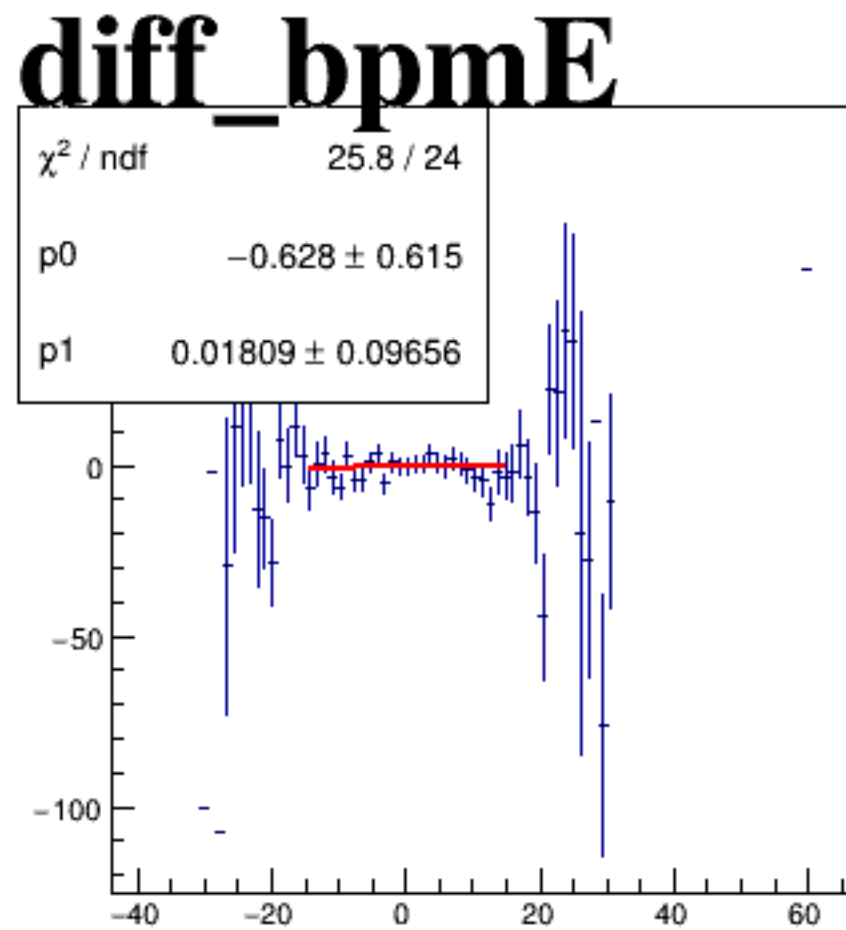
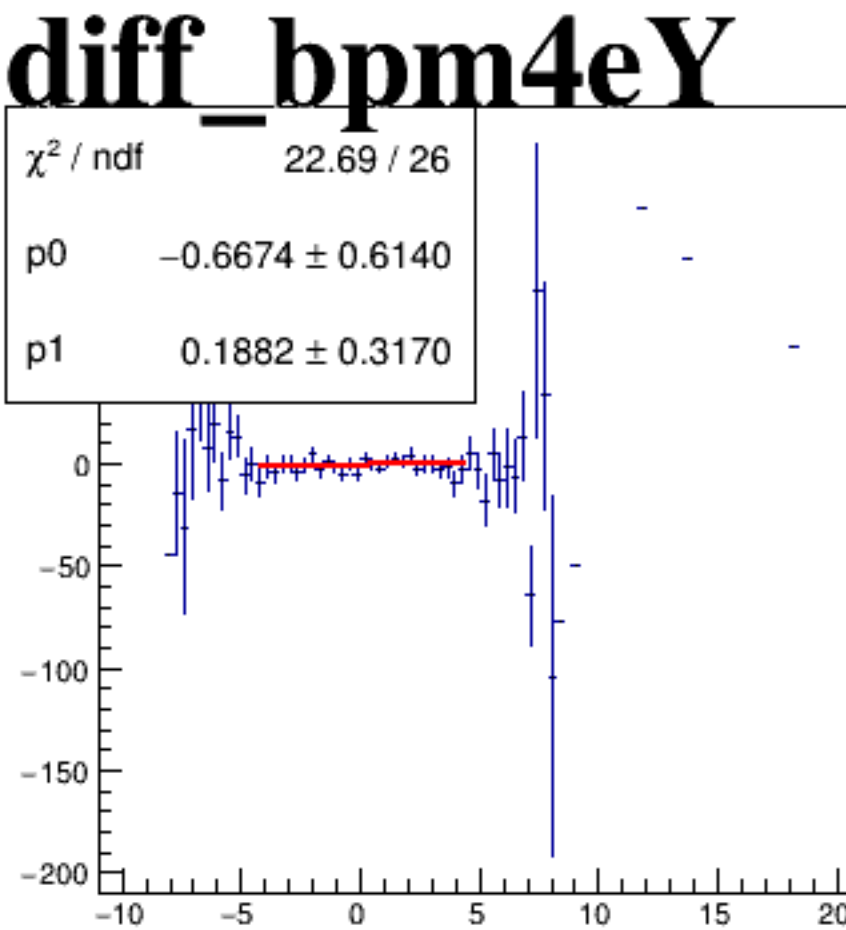
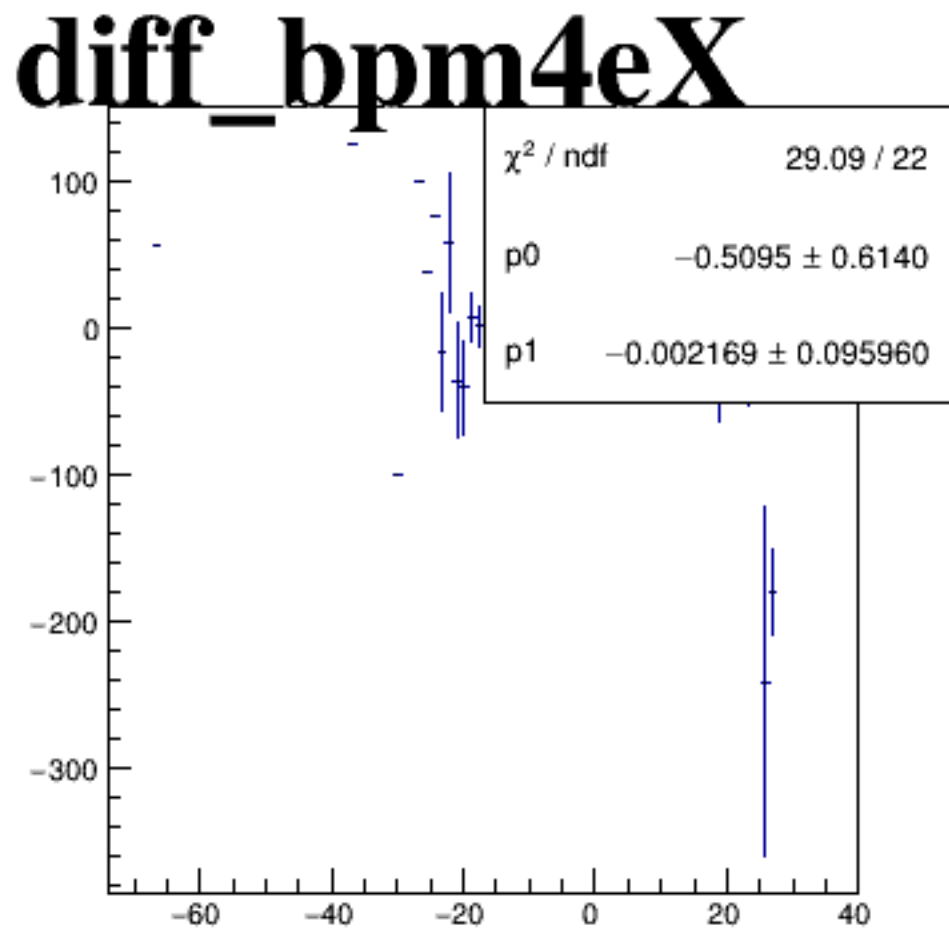
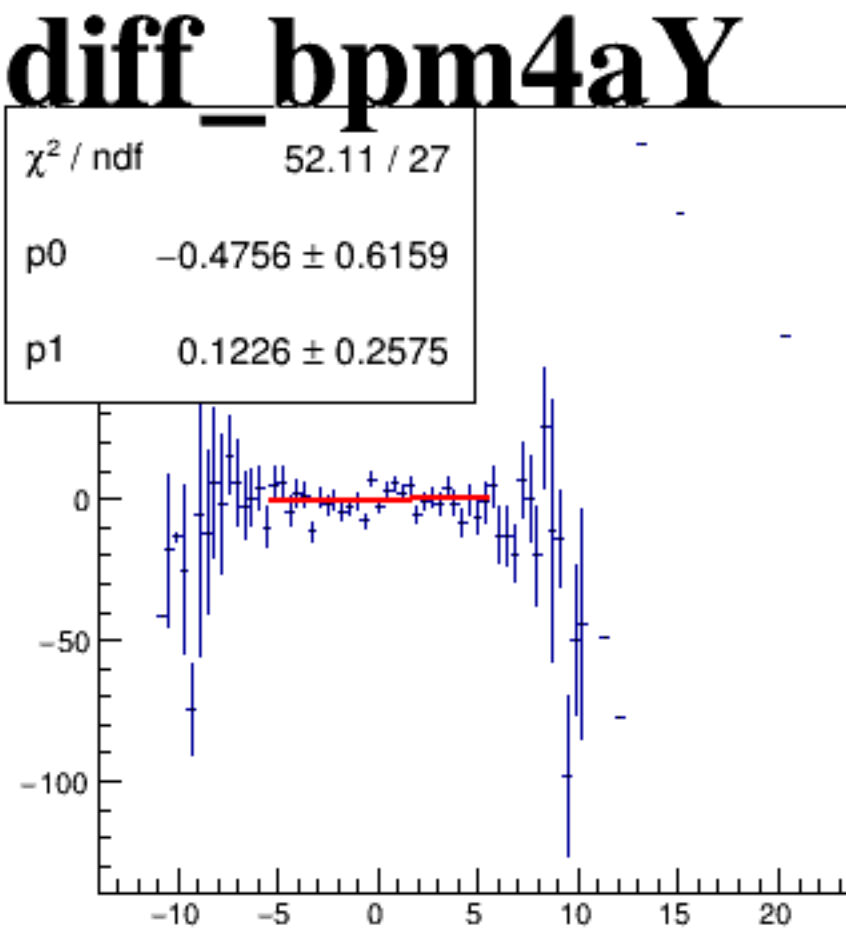
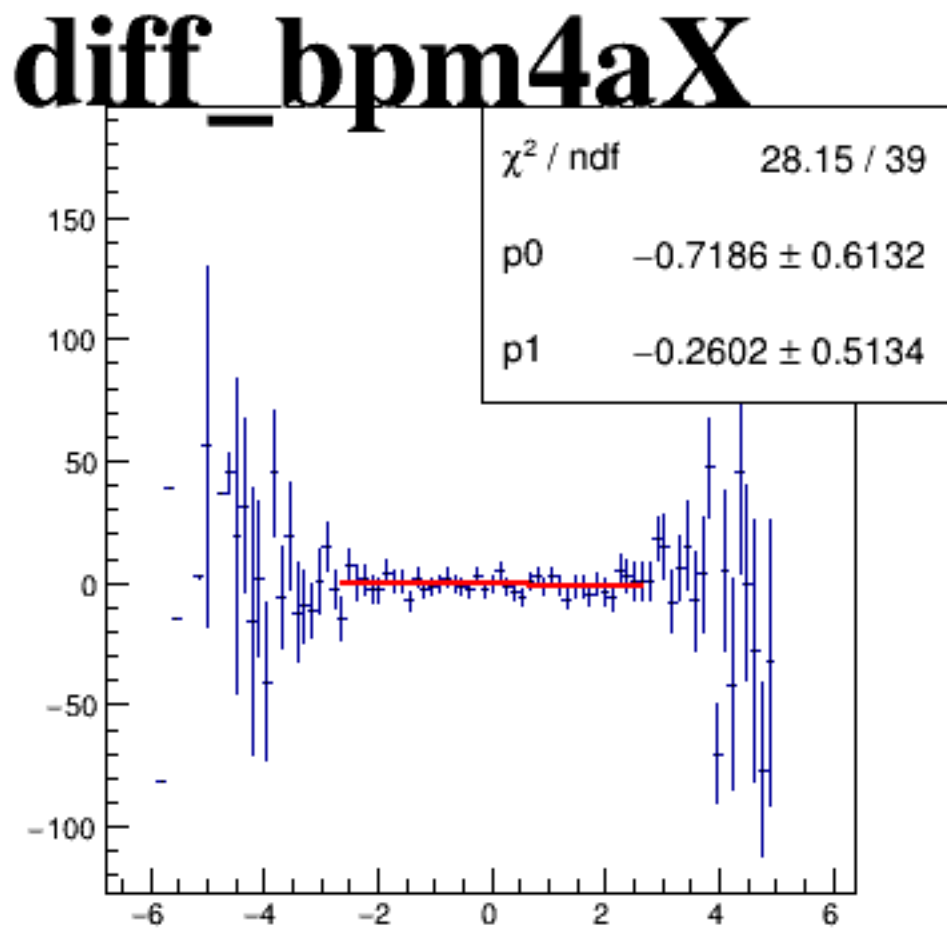
asym_dsr



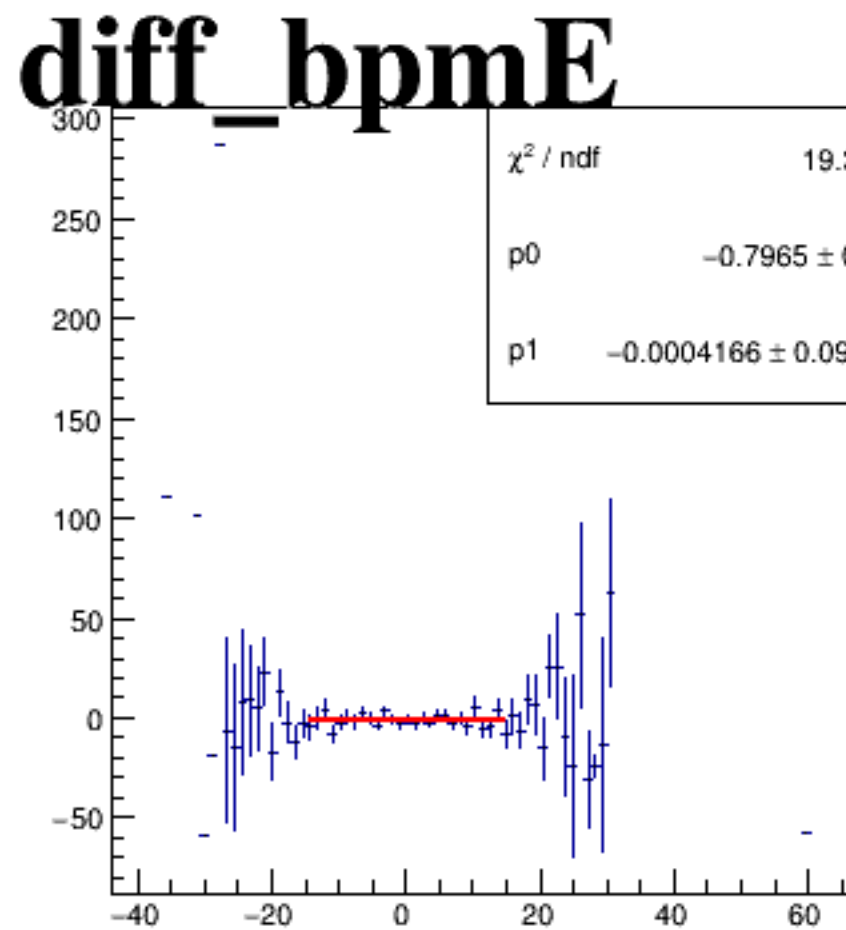
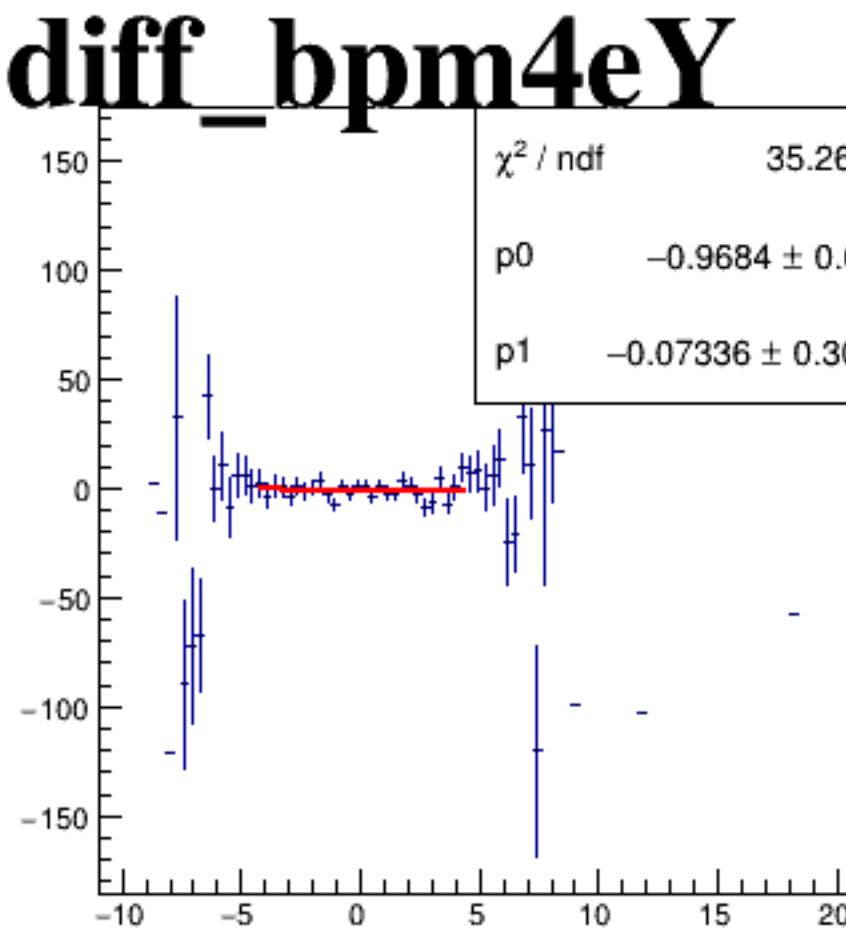
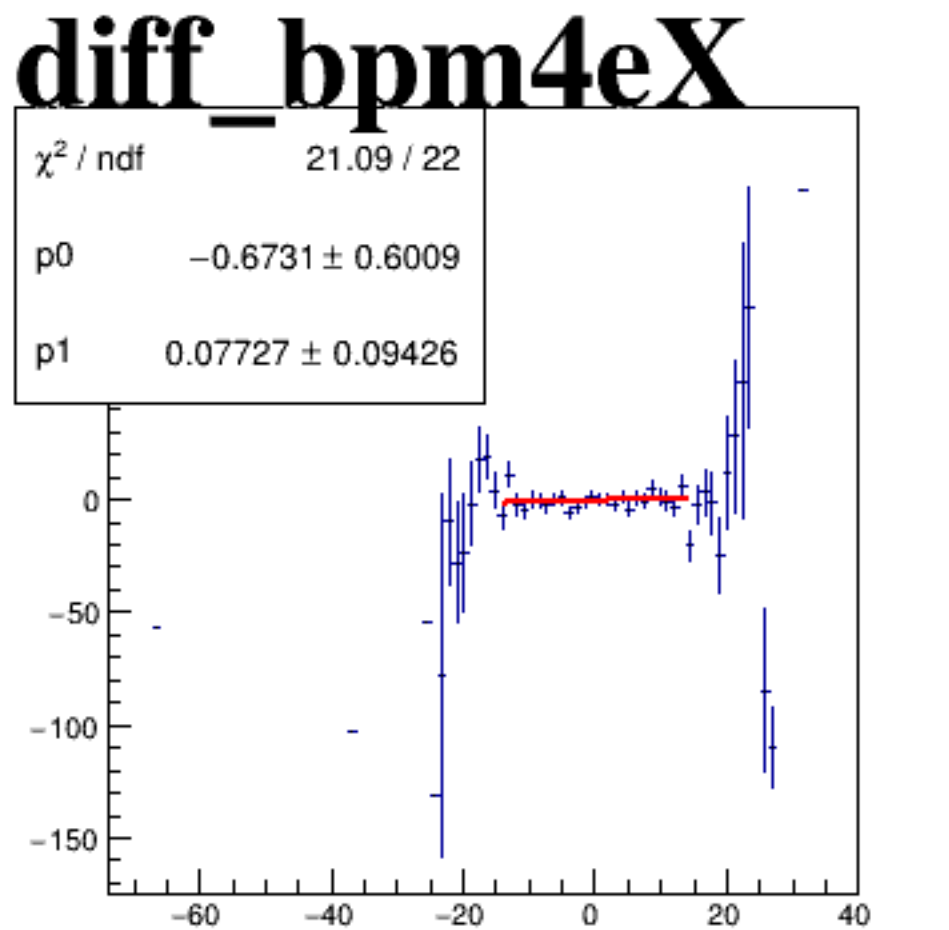
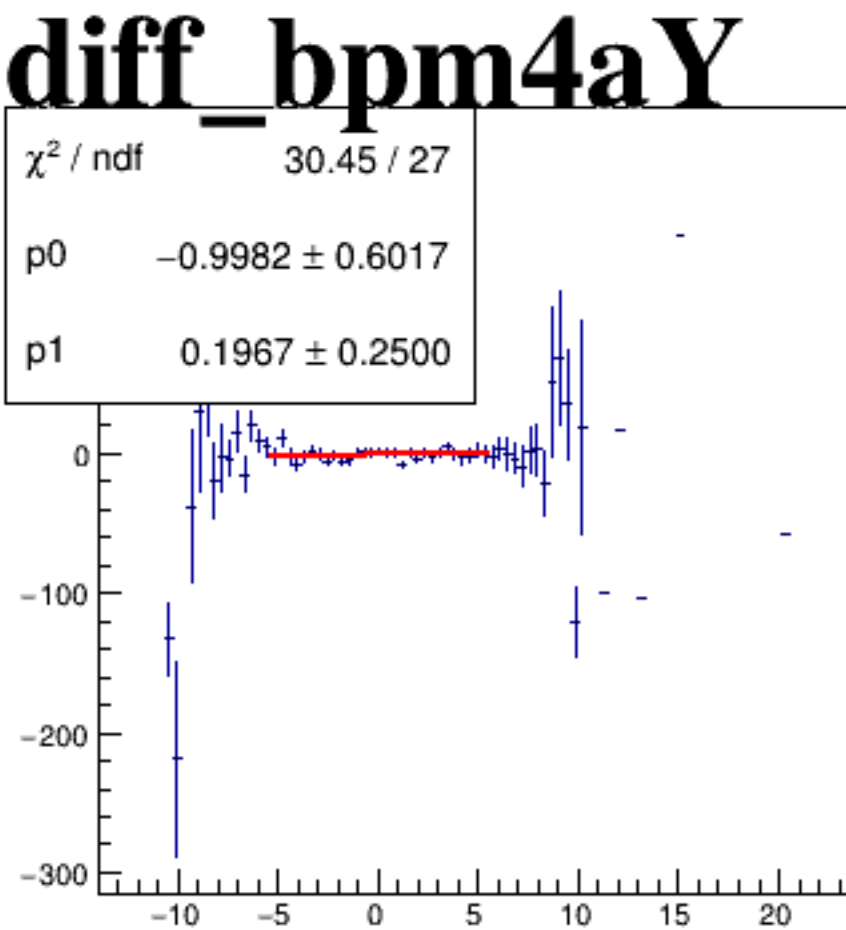
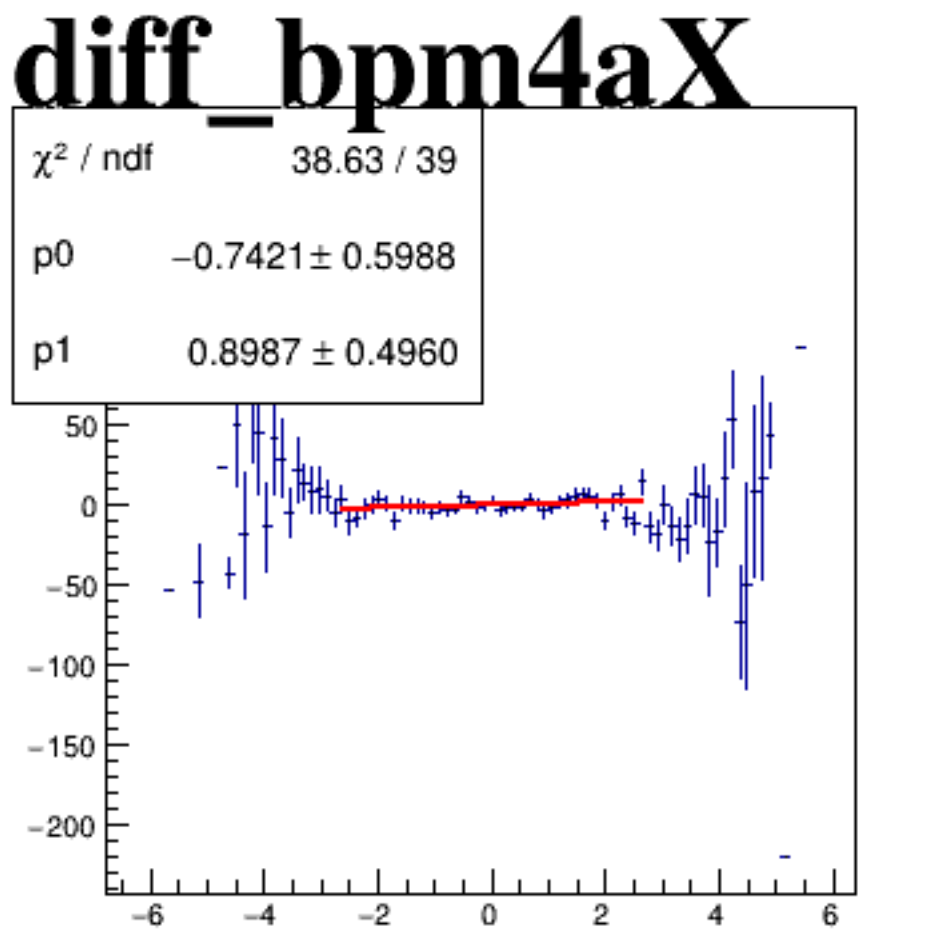
reg_asym_usl



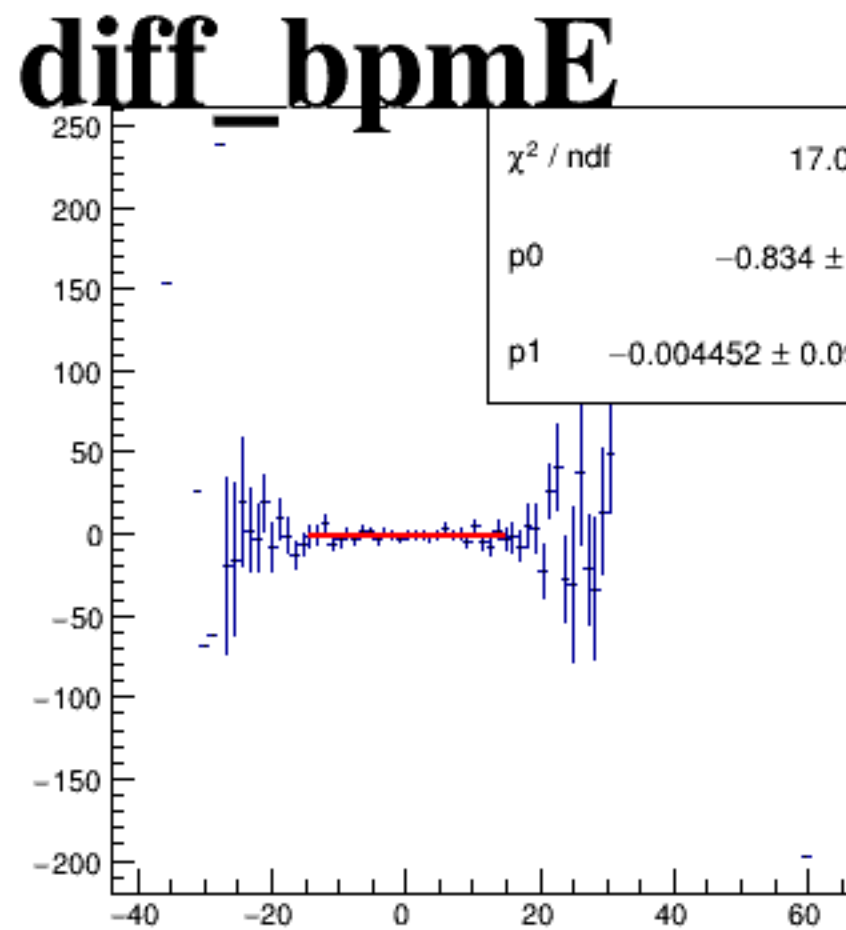
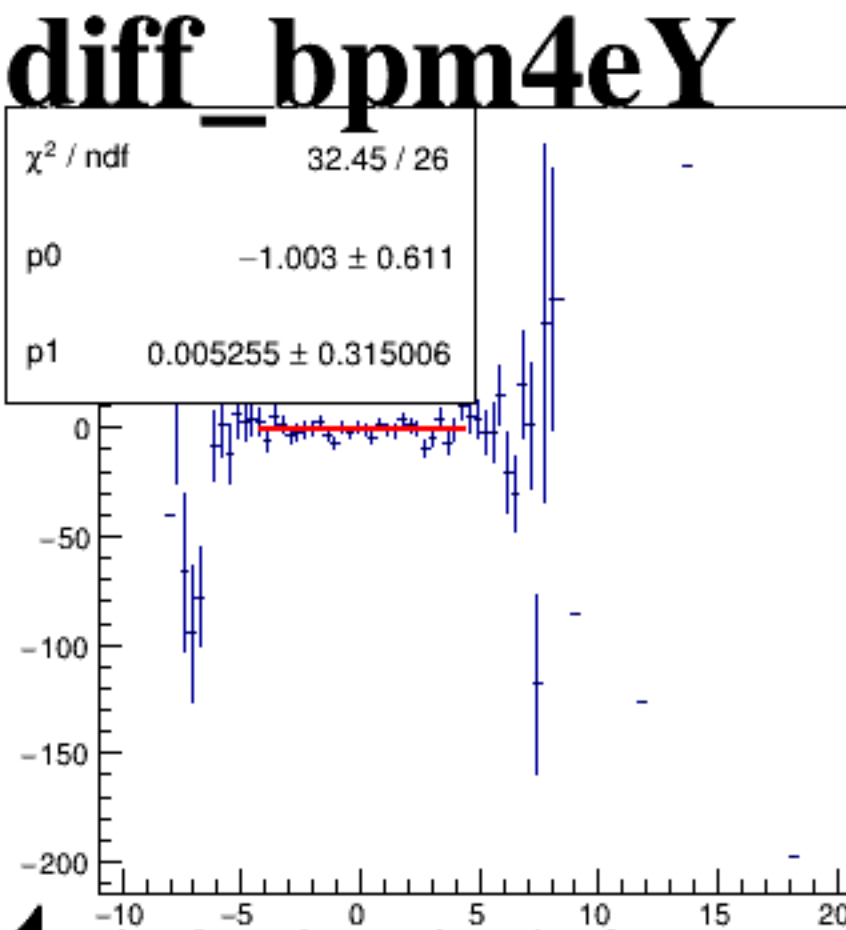
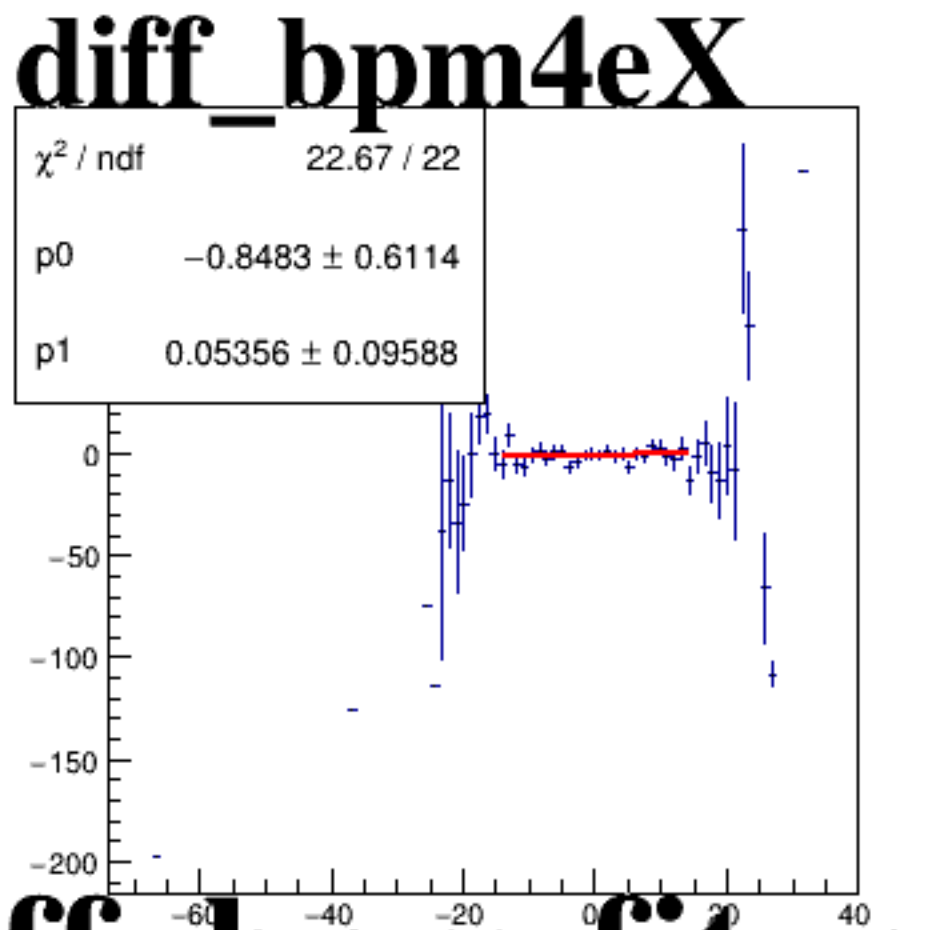
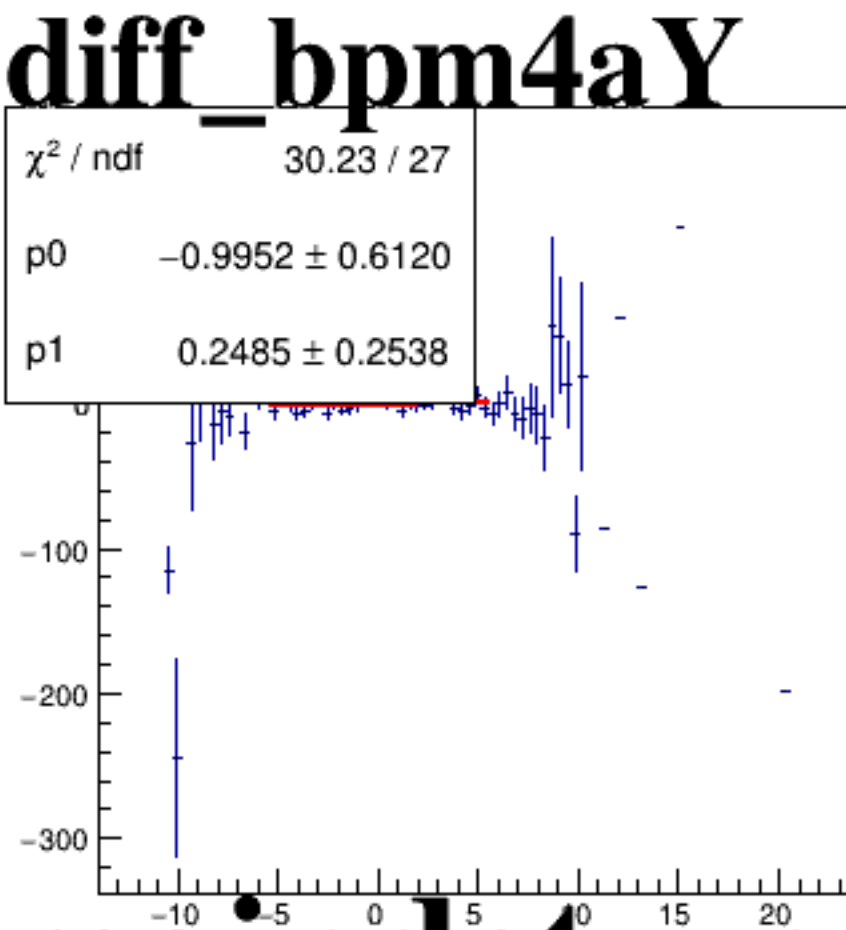
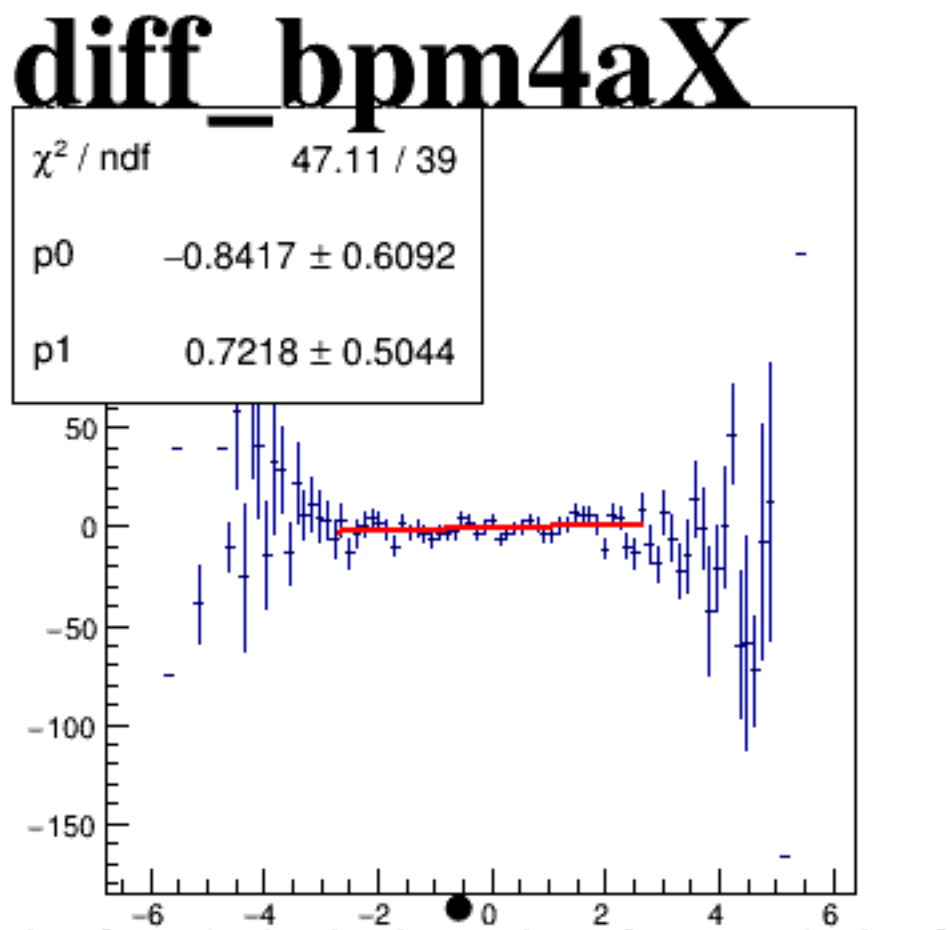
reg_asym_dsl



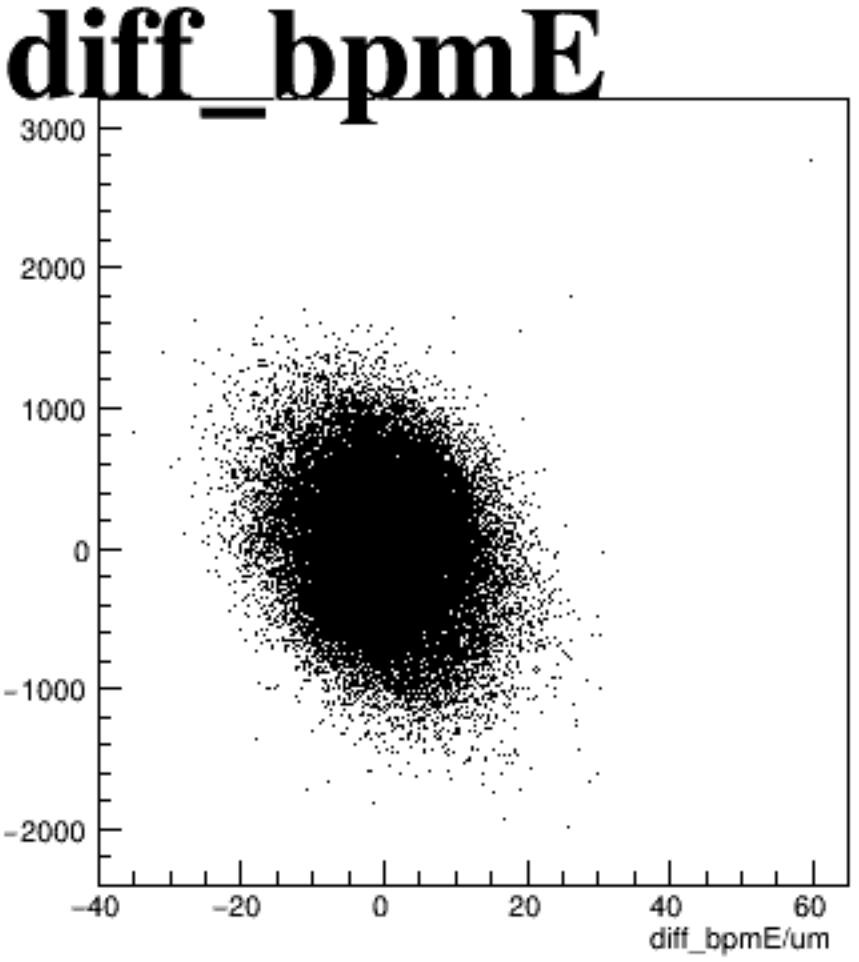
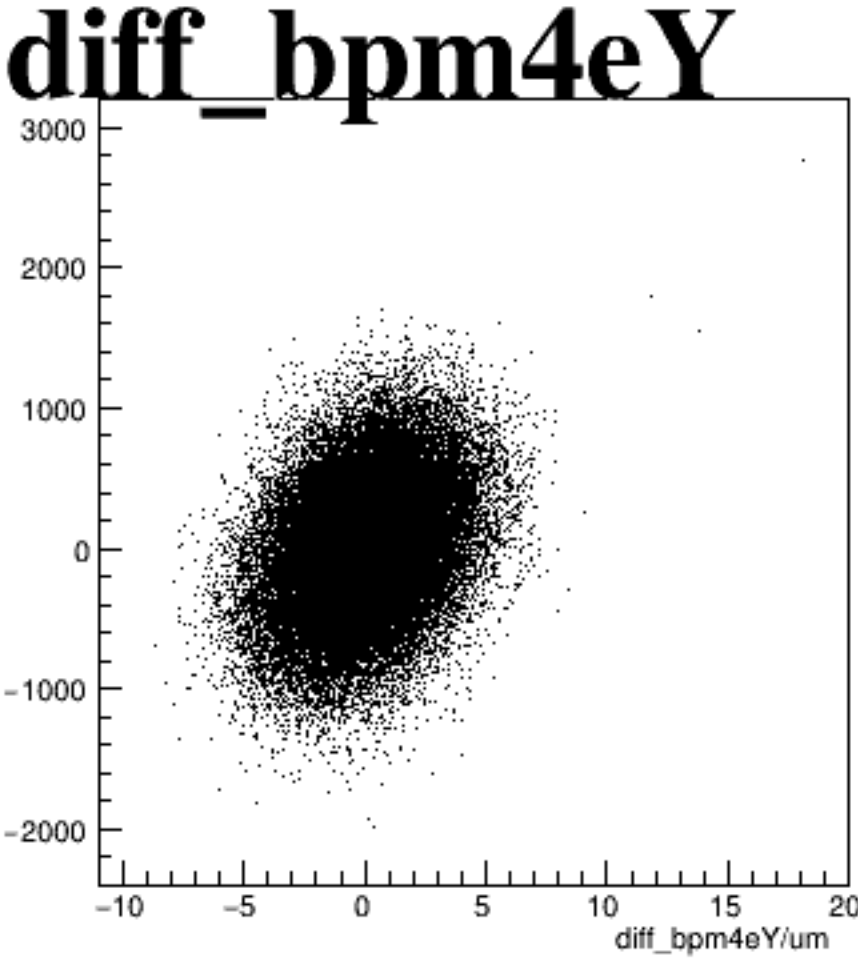
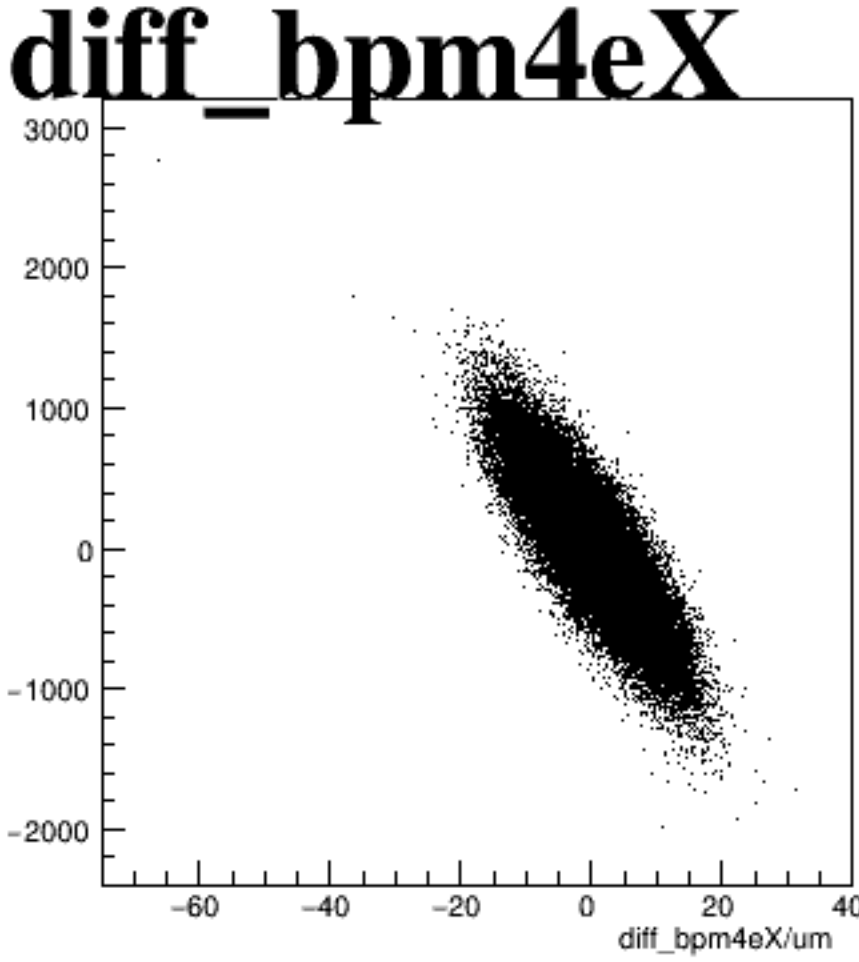
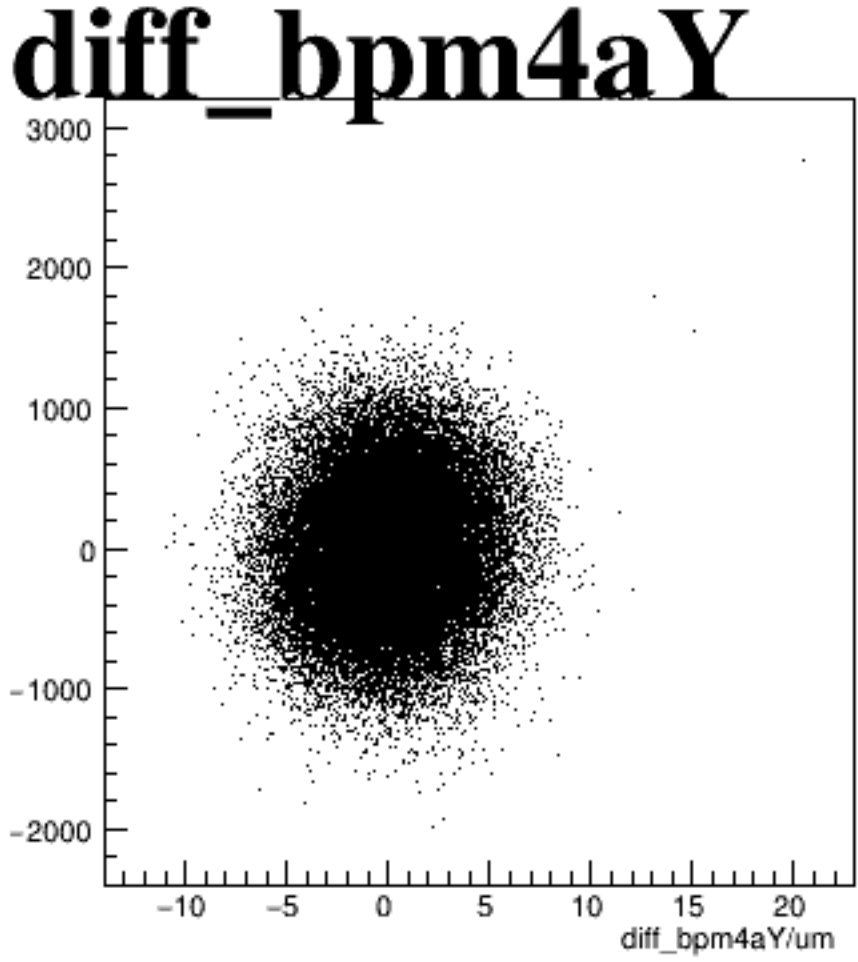
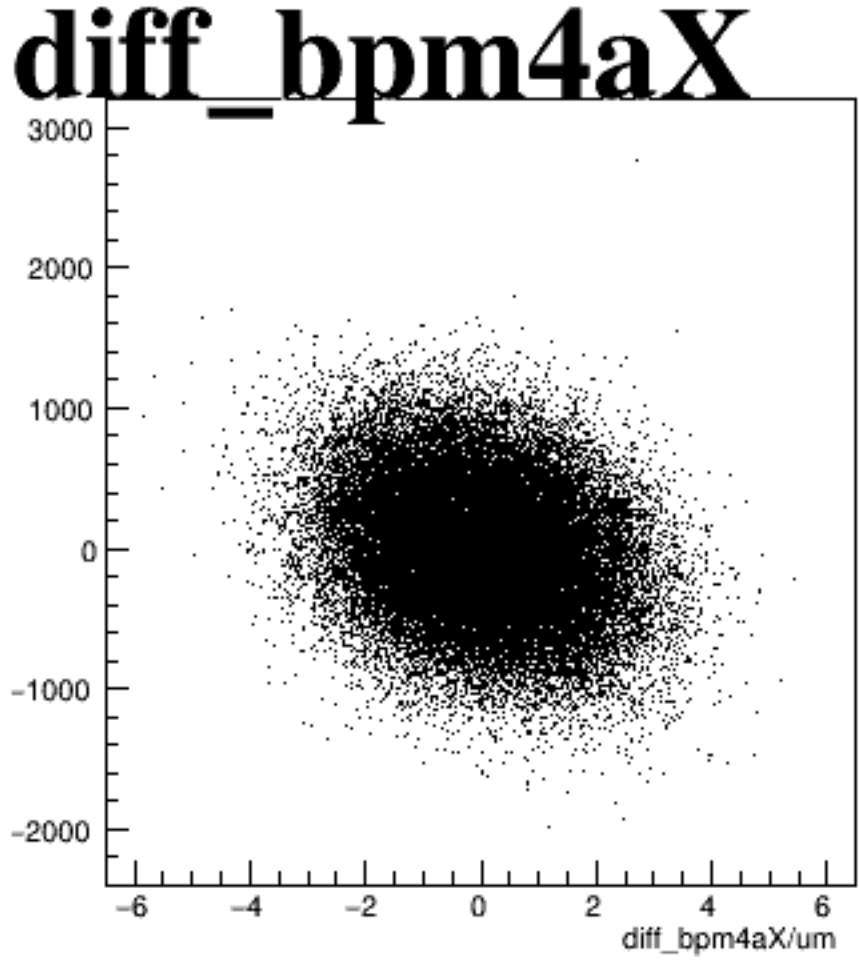
reg_asym_usr



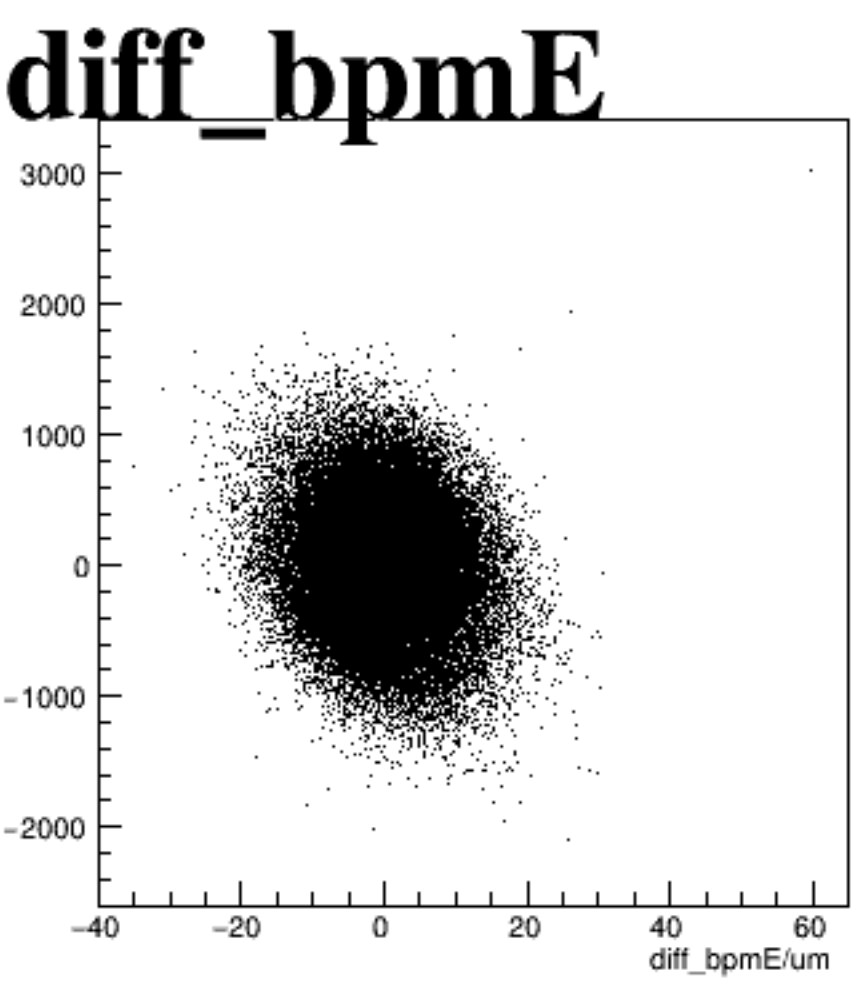
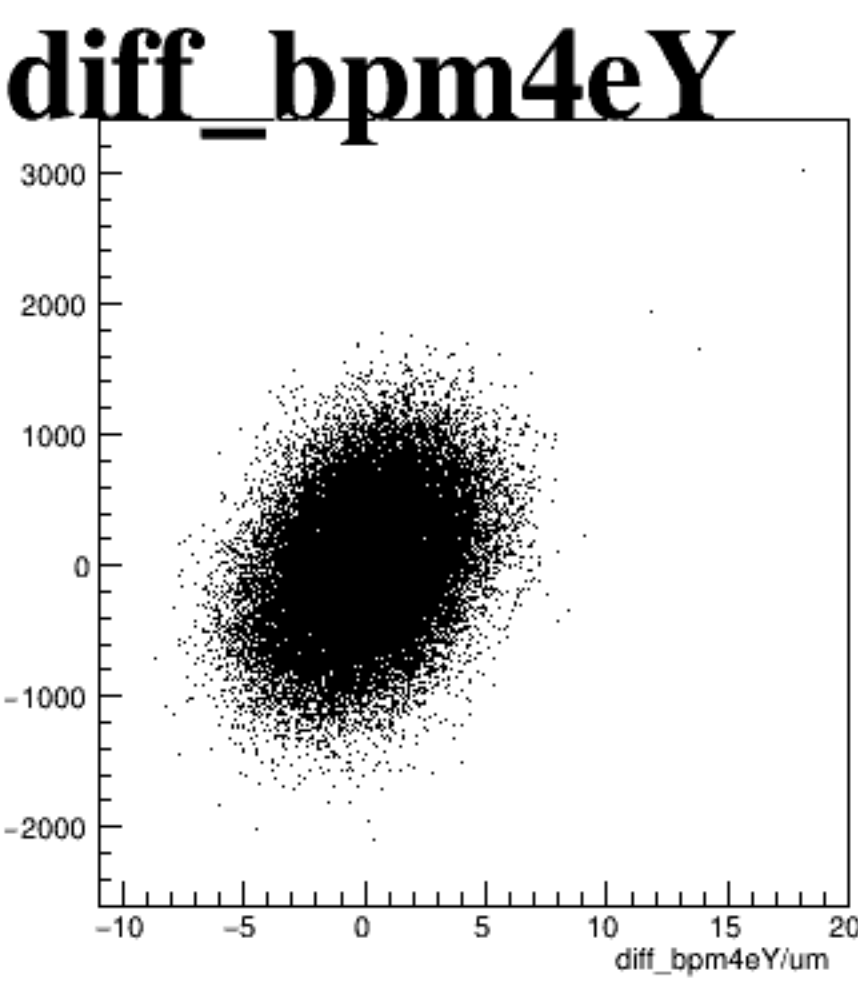
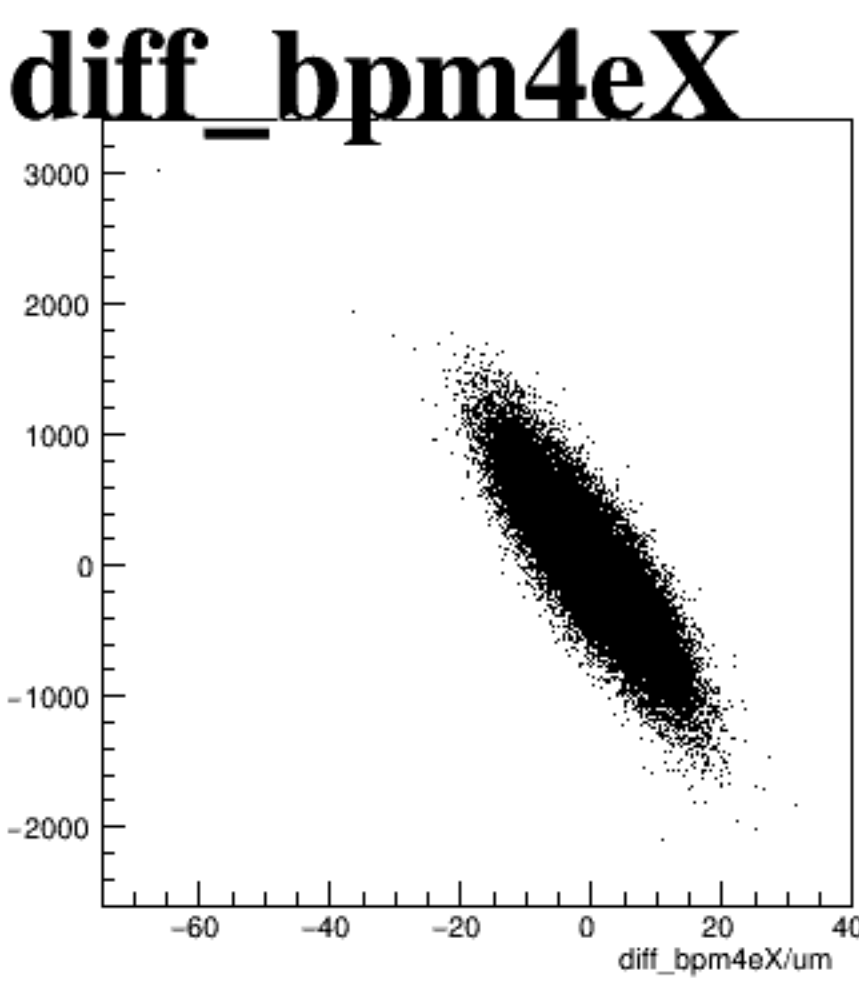
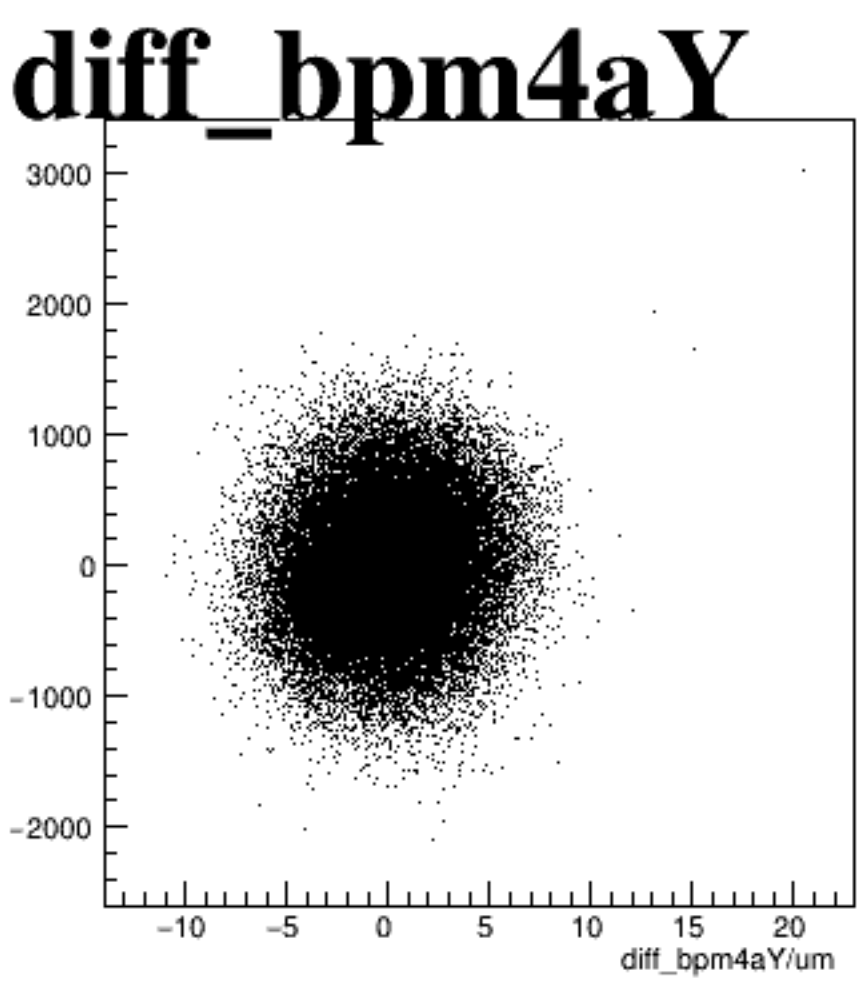
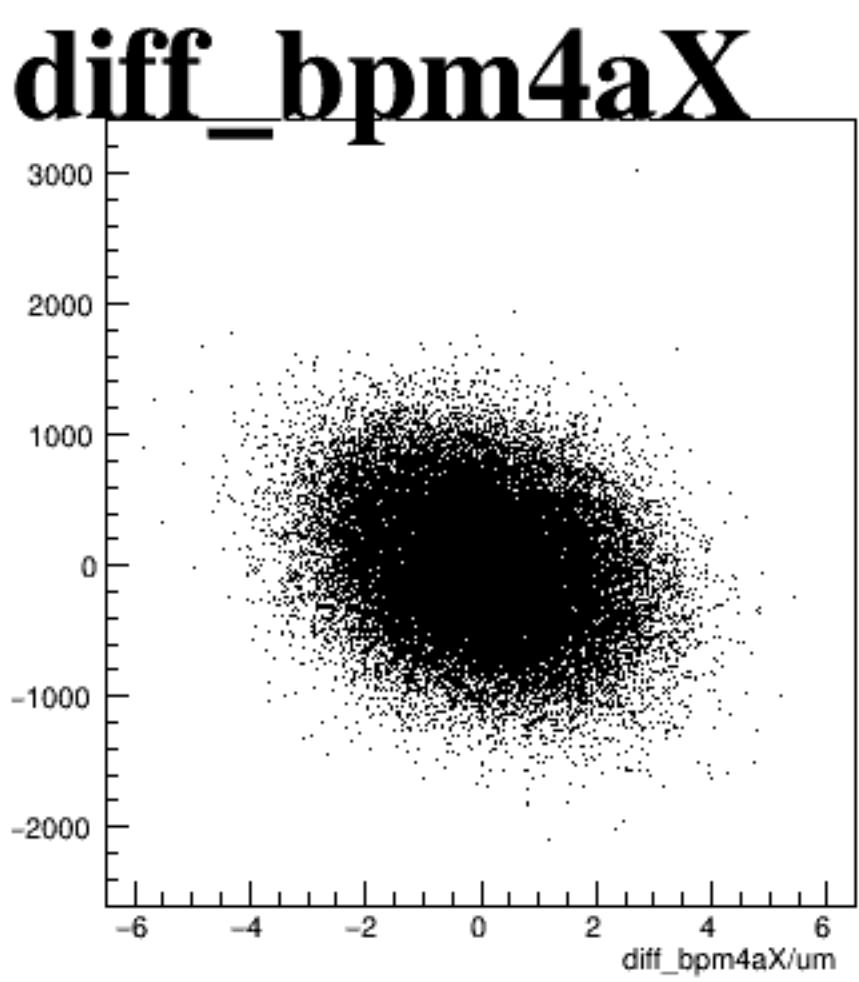
reg_asym_dsr



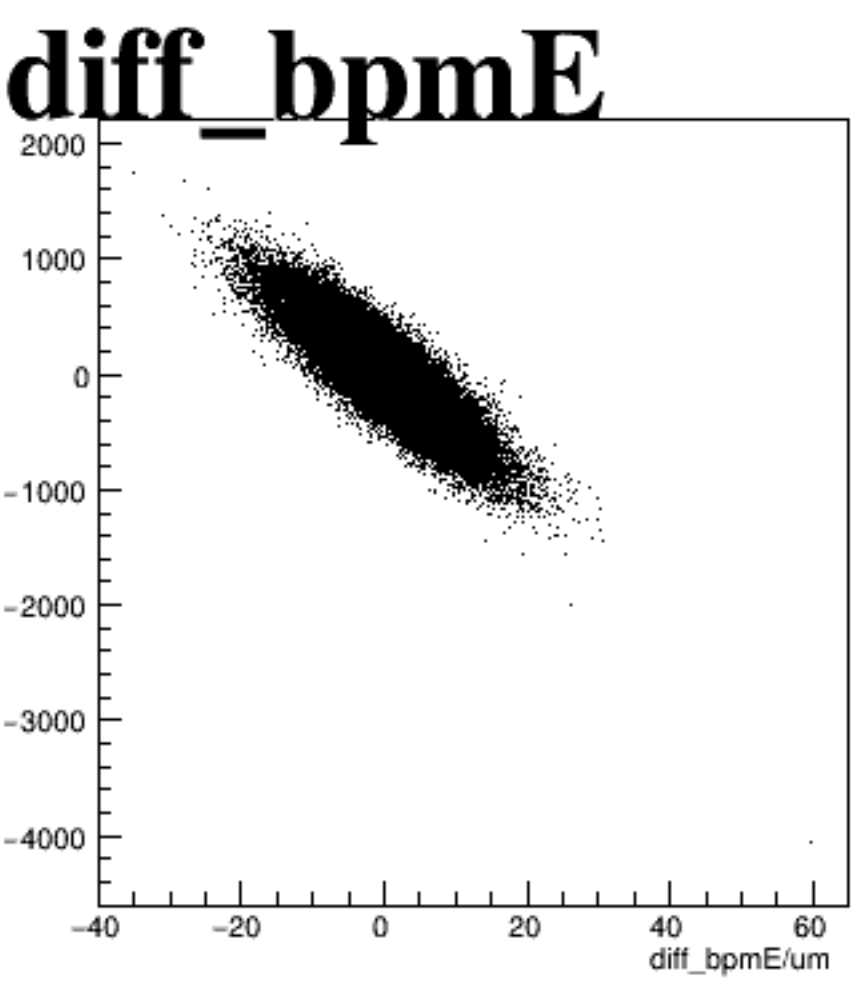
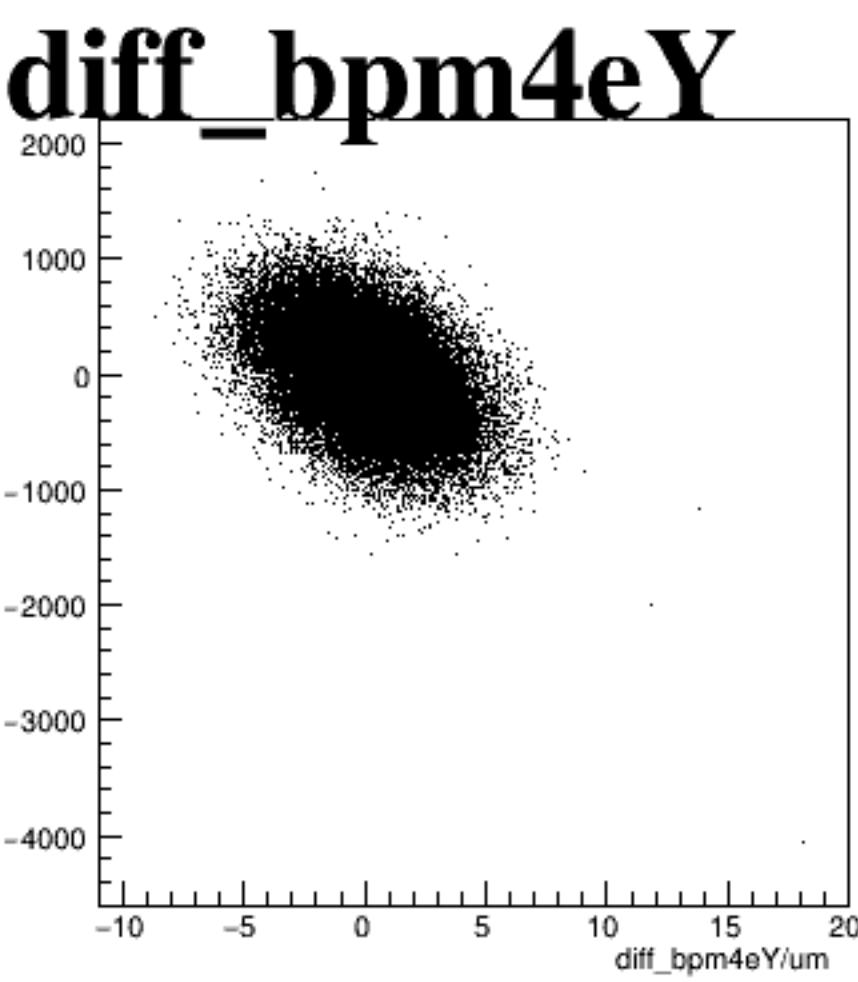
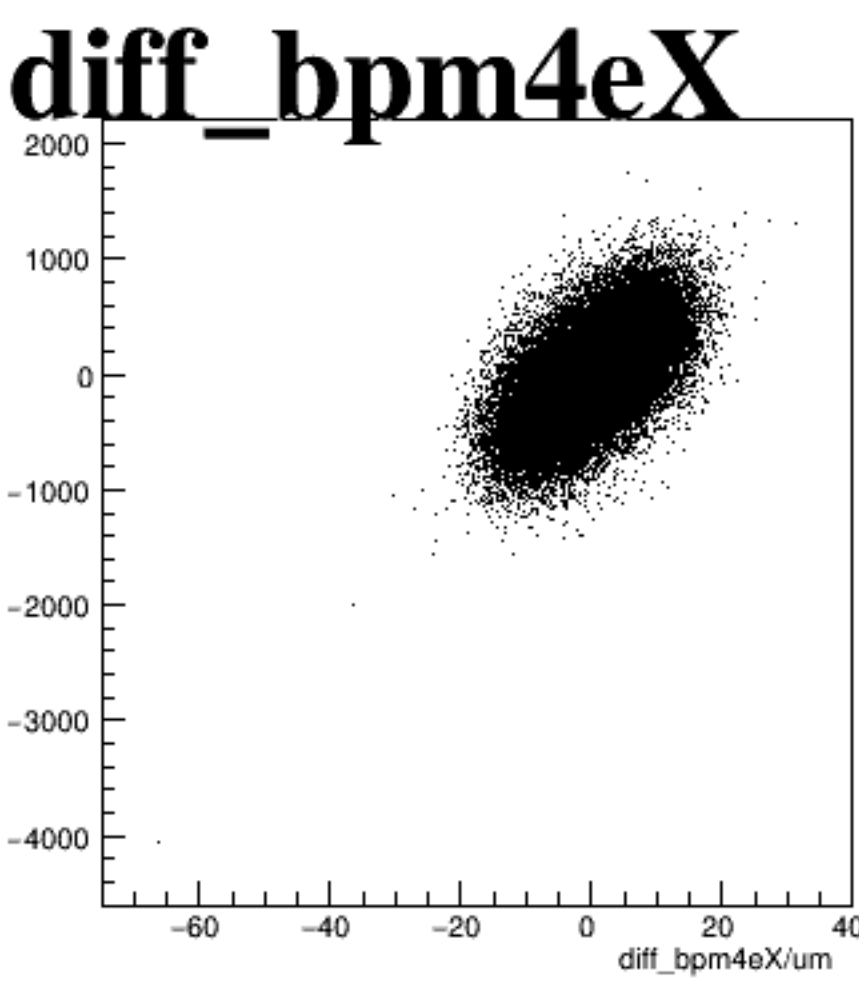
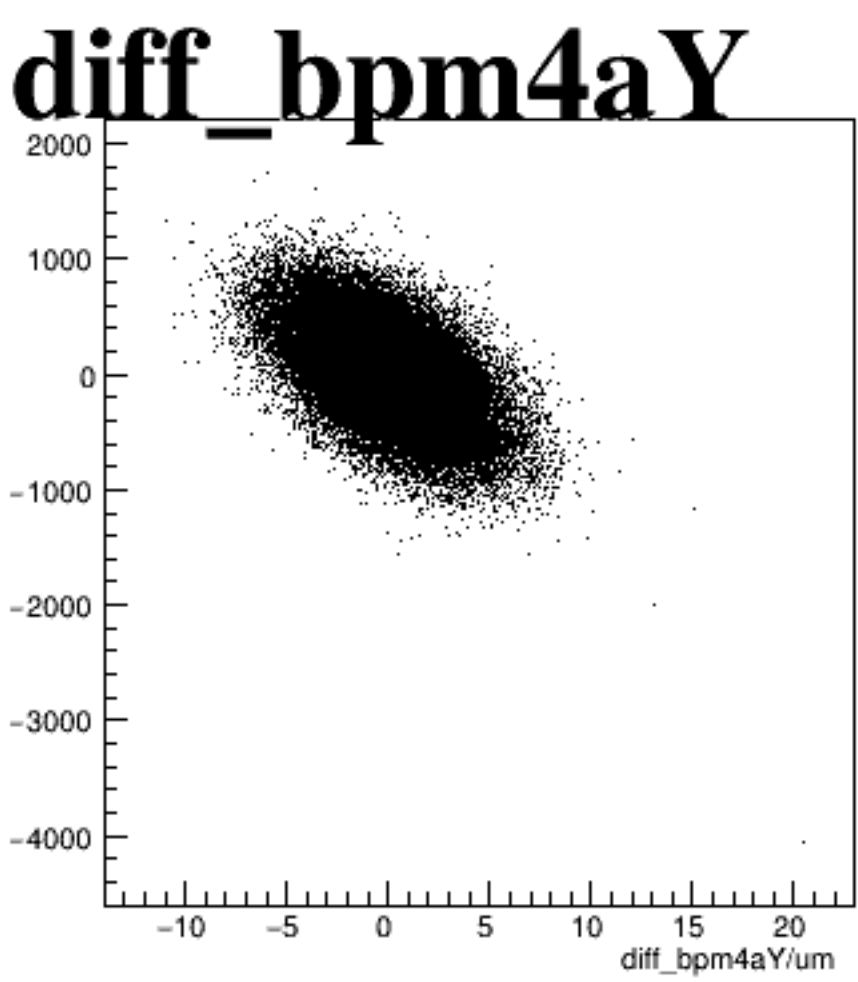
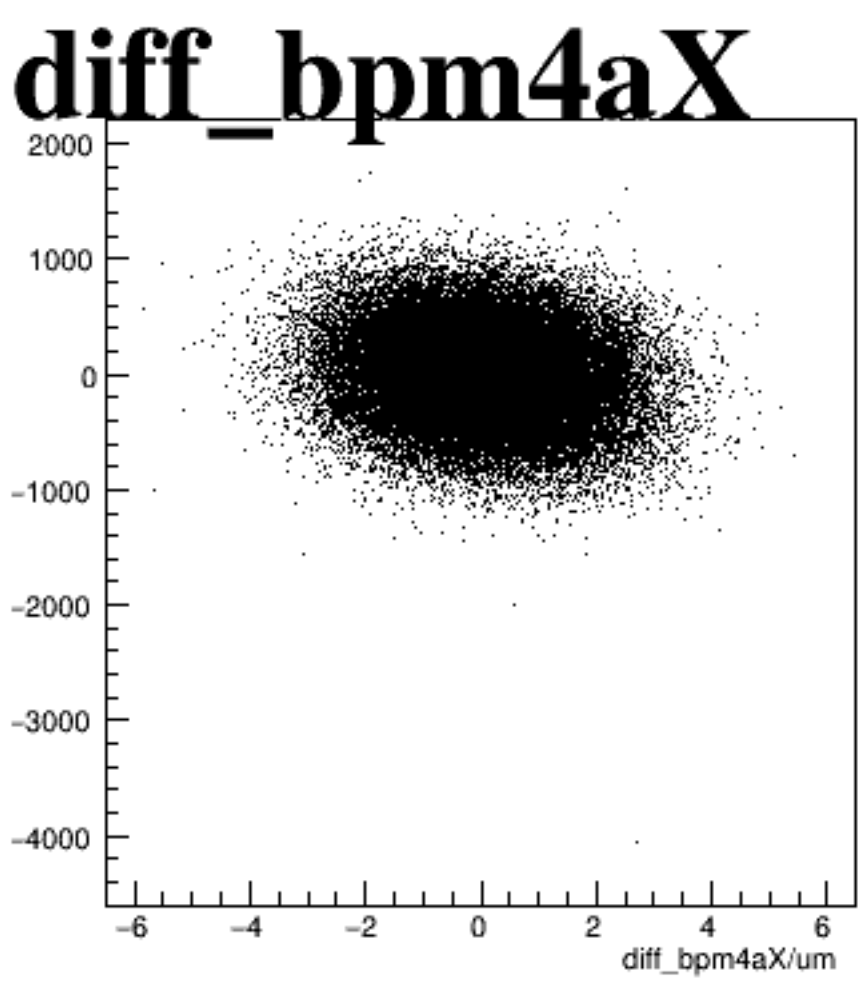
asym_usl



asym_dsl



asym_usr



asym_dsr

