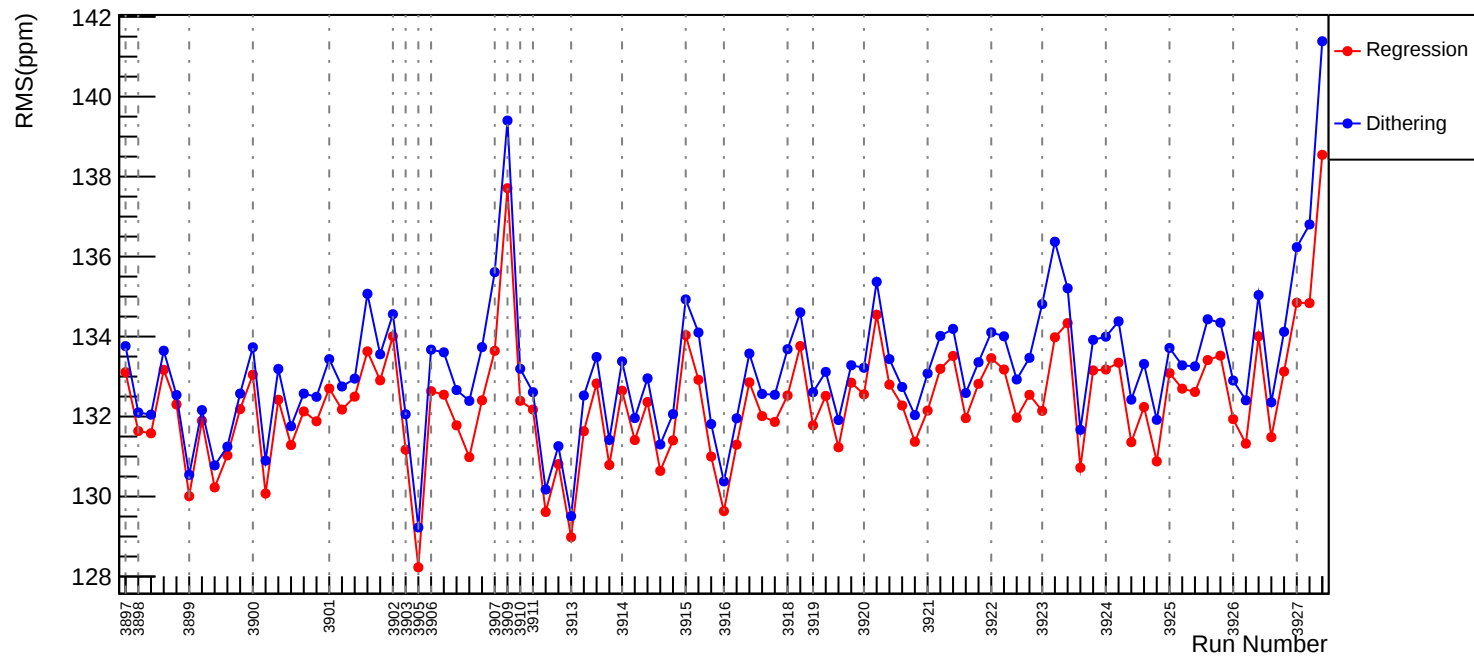
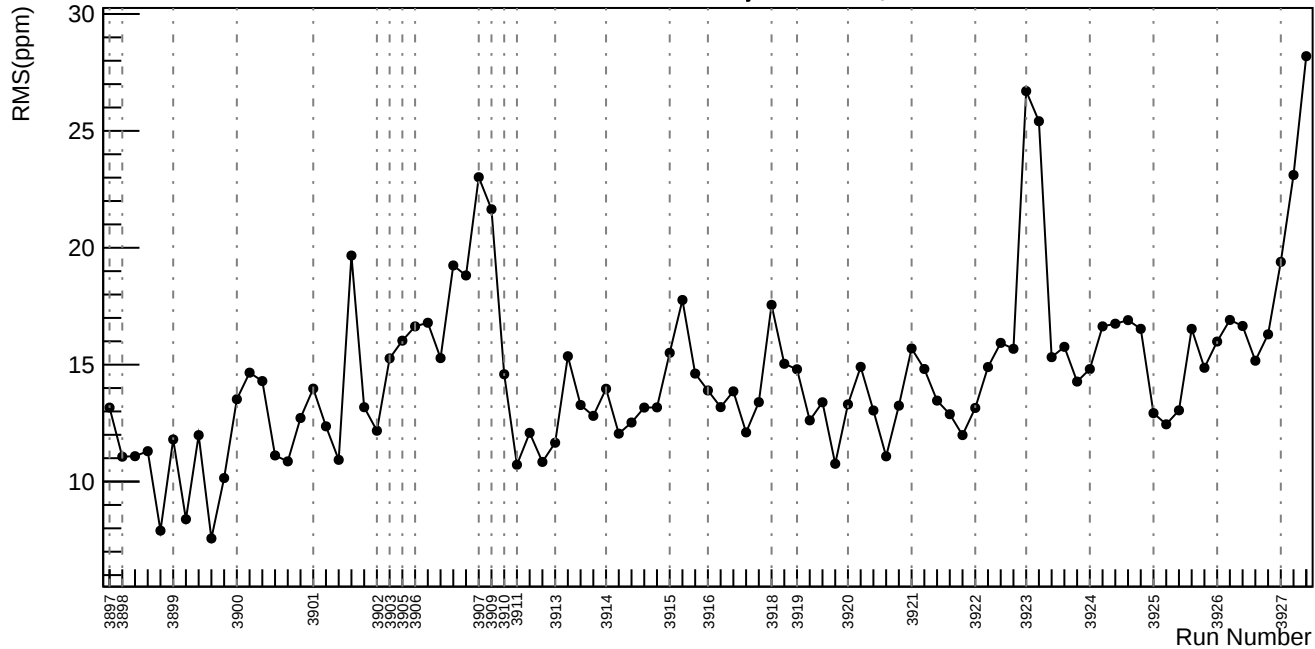


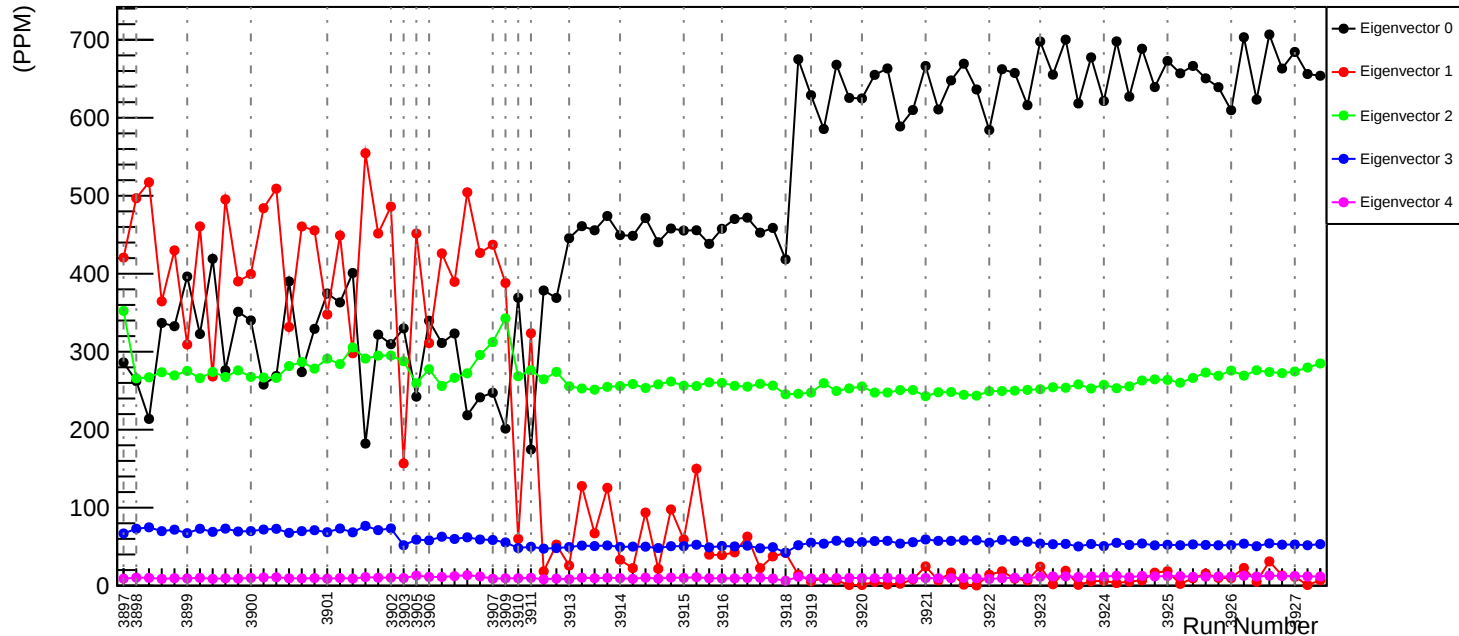
Slug32: Corrected RMS (ppm): asym_usl



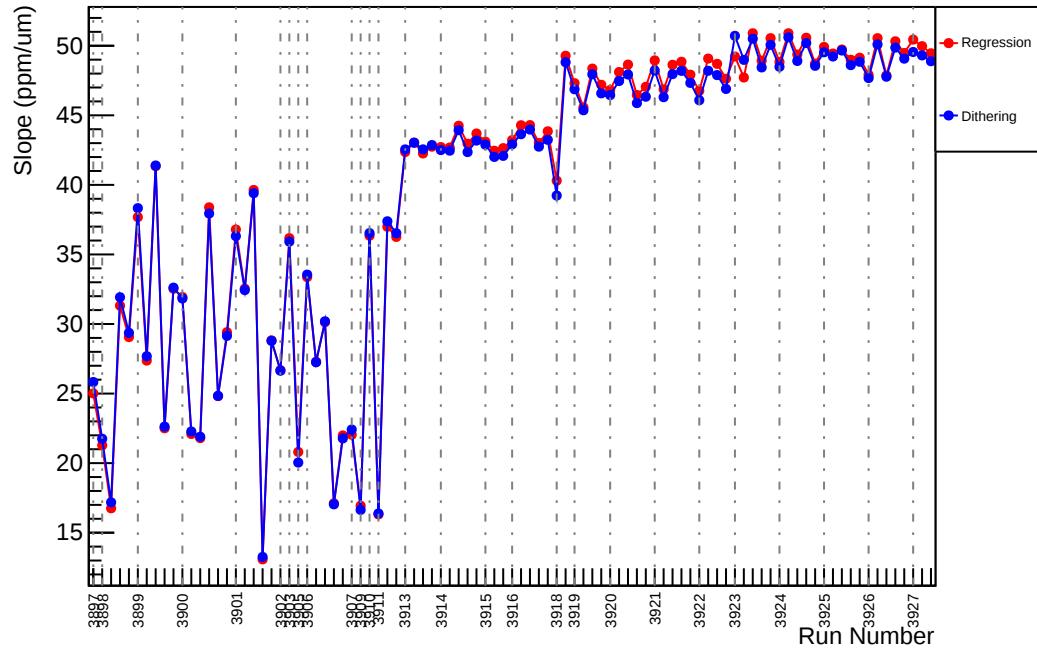
Slug32: asym_usl: $\sqrt{\sigma_{\text{dit}}^2 - \sigma_{\text{reg}}^2}$ (ppm)



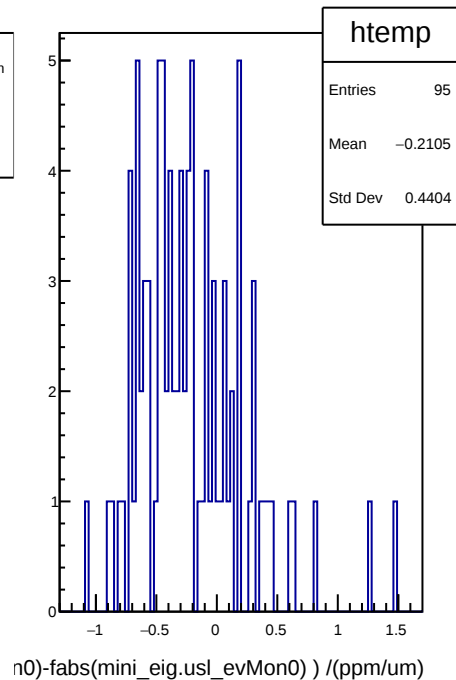
Dithering Corrections Width (ppm) by Eigenvectors



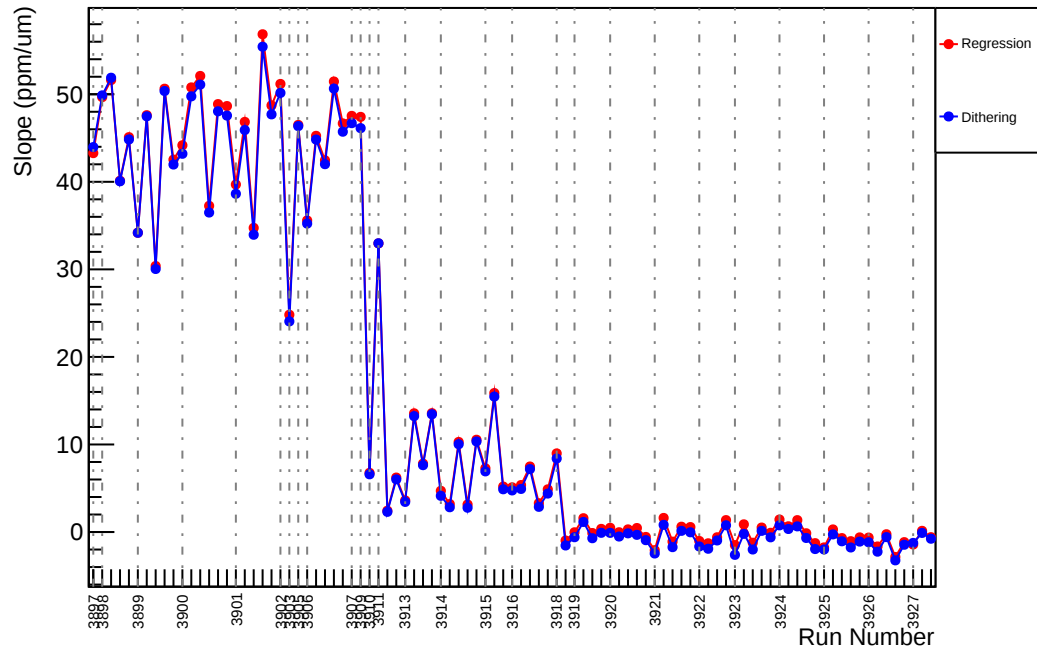
Slug32: Slope : usl_vs_evMon0 (ppm/um)



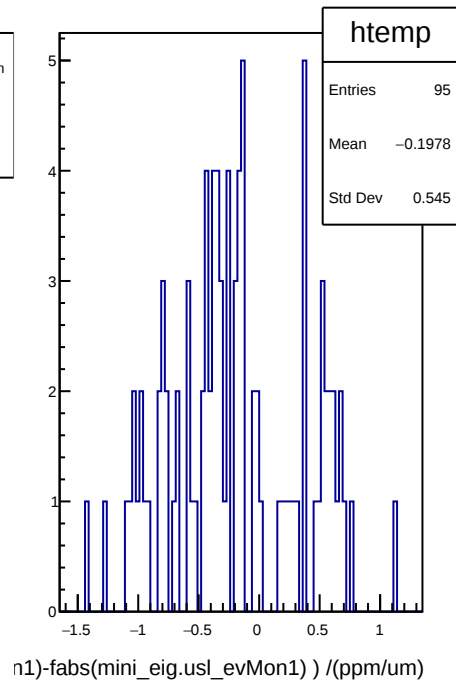
|dit_slope| - |reg_slope| (ppm/um)



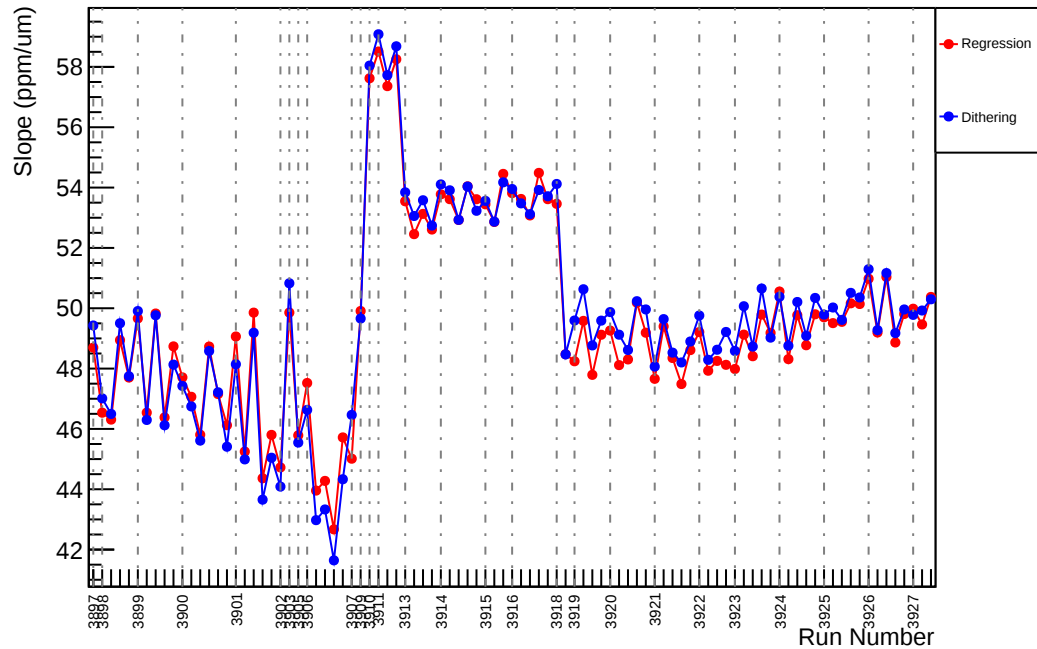
Slug32: Slope : usl_vs_evMon1 (ppm/um)



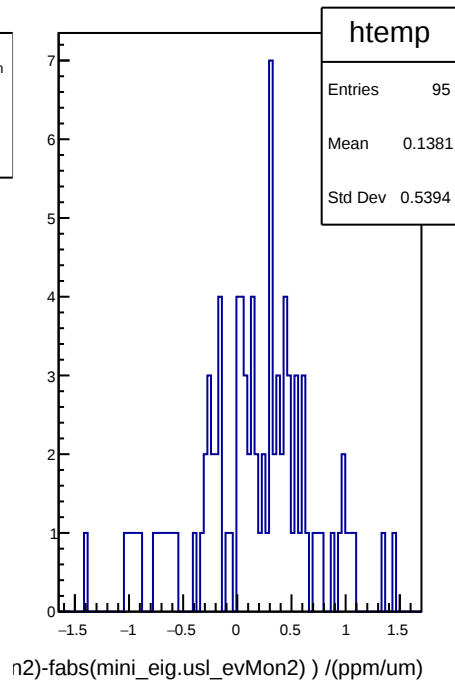
|dit_slope| - |reg_slope| (ppm/um)



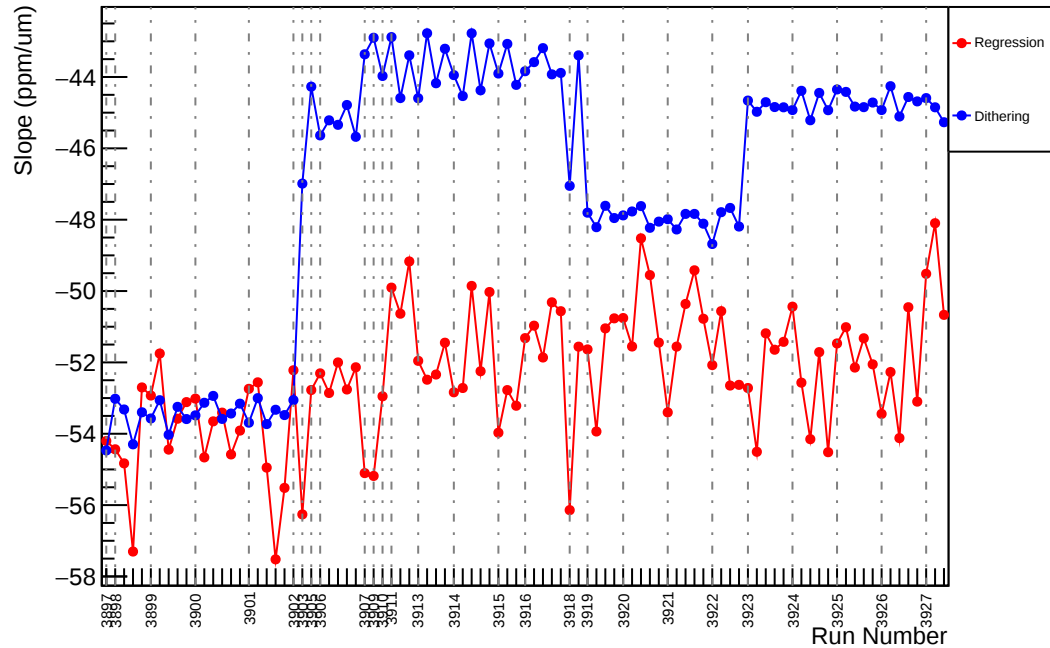
Slug32: Slope : usl_vs_evMon2 (ppm/um)



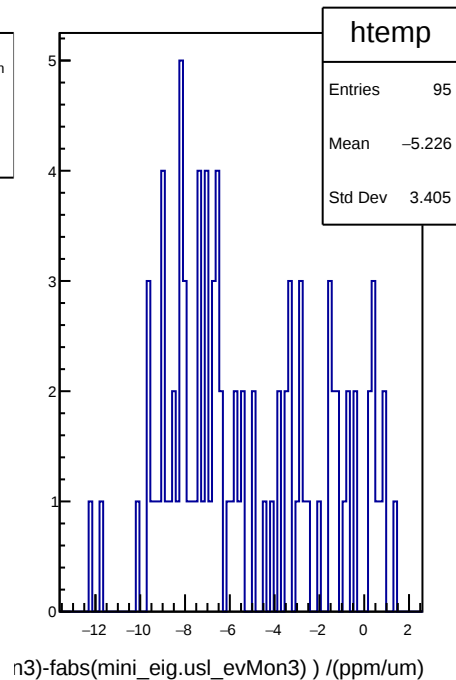
|dit_slope| - |reg_slope| (ppm/um)



Slug32: Slope : usl_vs_evMon3 (ppm/um)

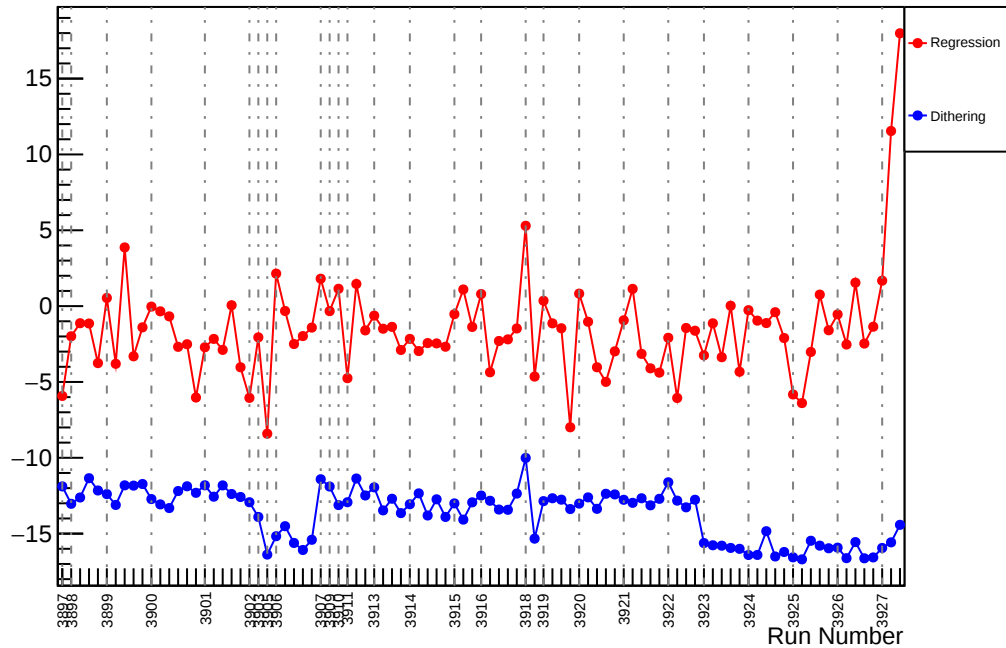


|dit_slope| - |reg_slope| (ppm/um)



Slug32: Slope : usl_vs_evMon4 (ppm/um)

Slope (ppm/um)



|dit_slope| - |reg_slope| (ppm/um)

