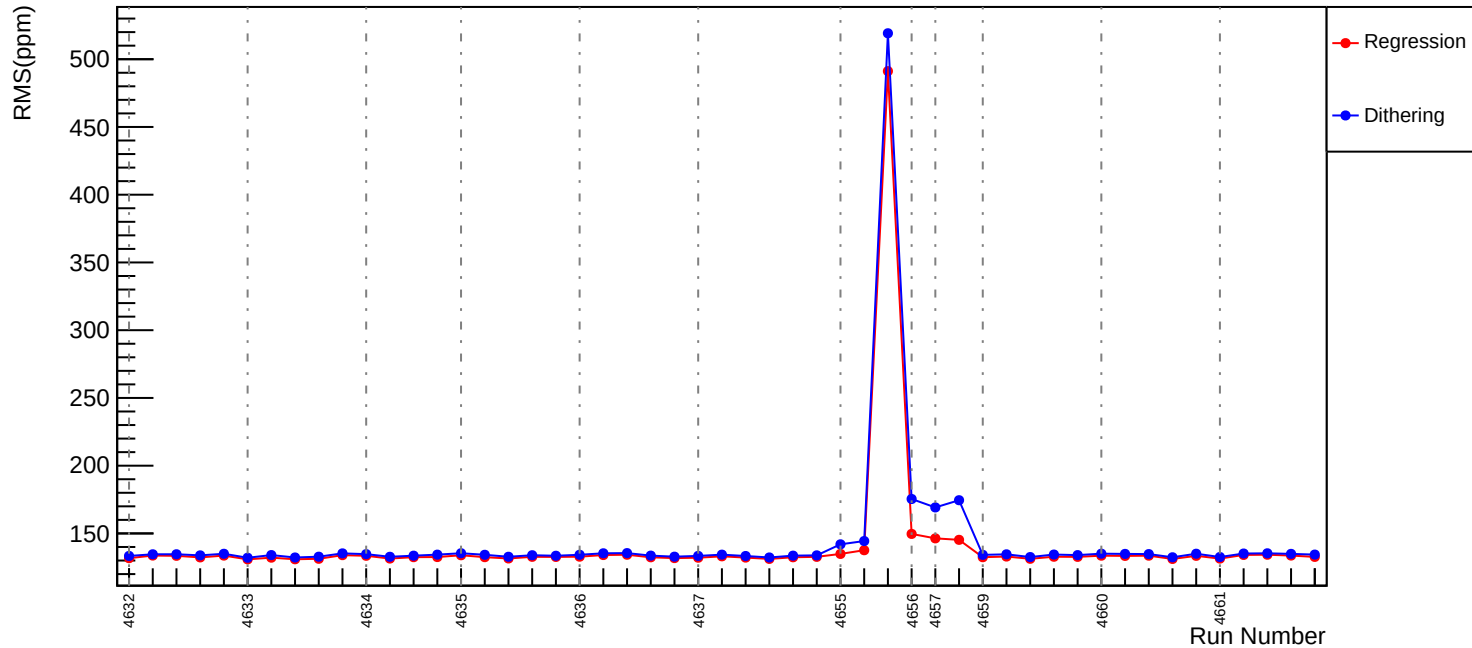
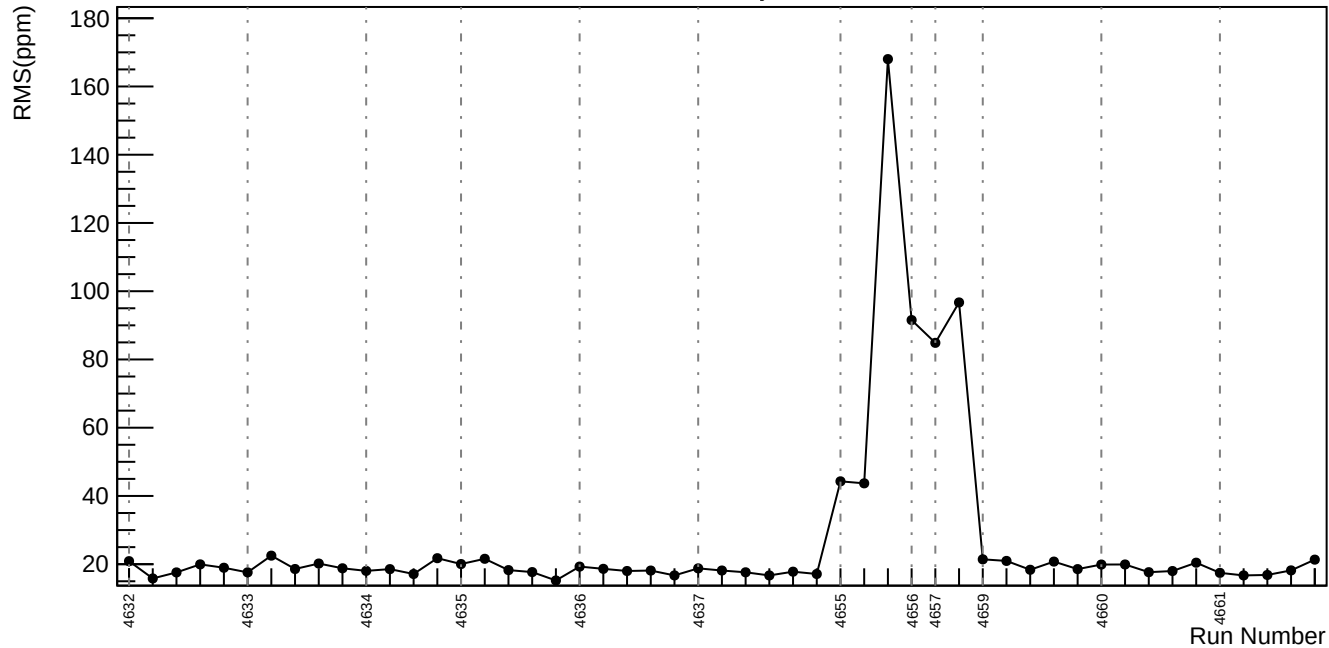


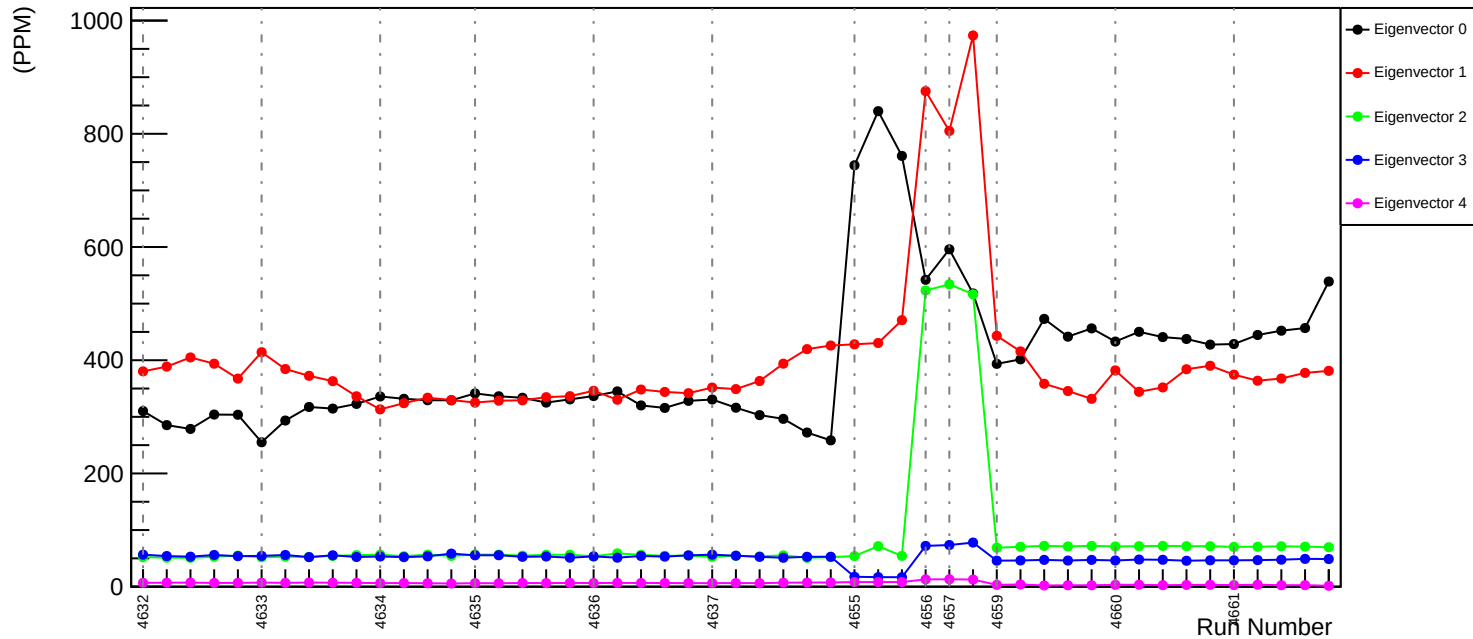
Slug72: Corrected RMS (ppm): asym_usl



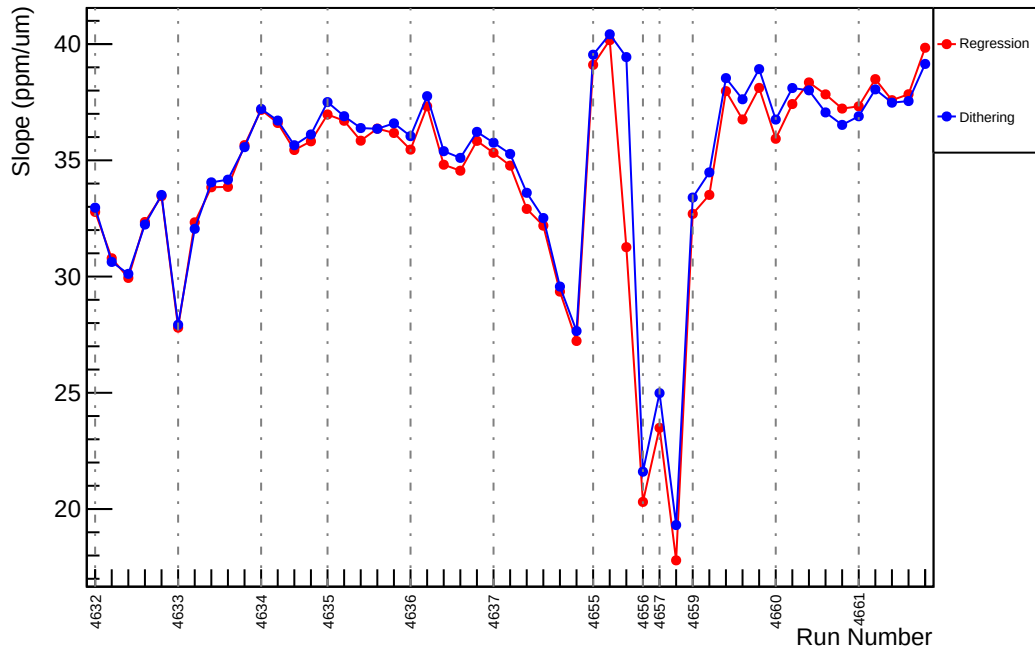
Slug72: asym_usl: $\sqrt{\sigma_{\text{dit}}^2 - \sigma_{\text{reg}}^2}$ (ppm)



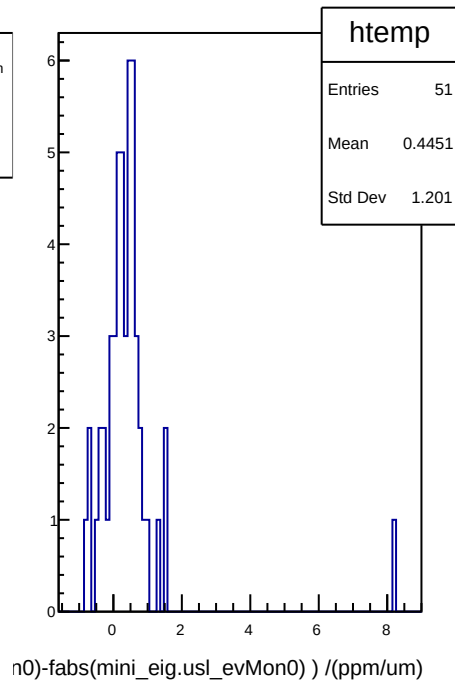
Dithering Corrections Width (ppm) by Eigenvectors



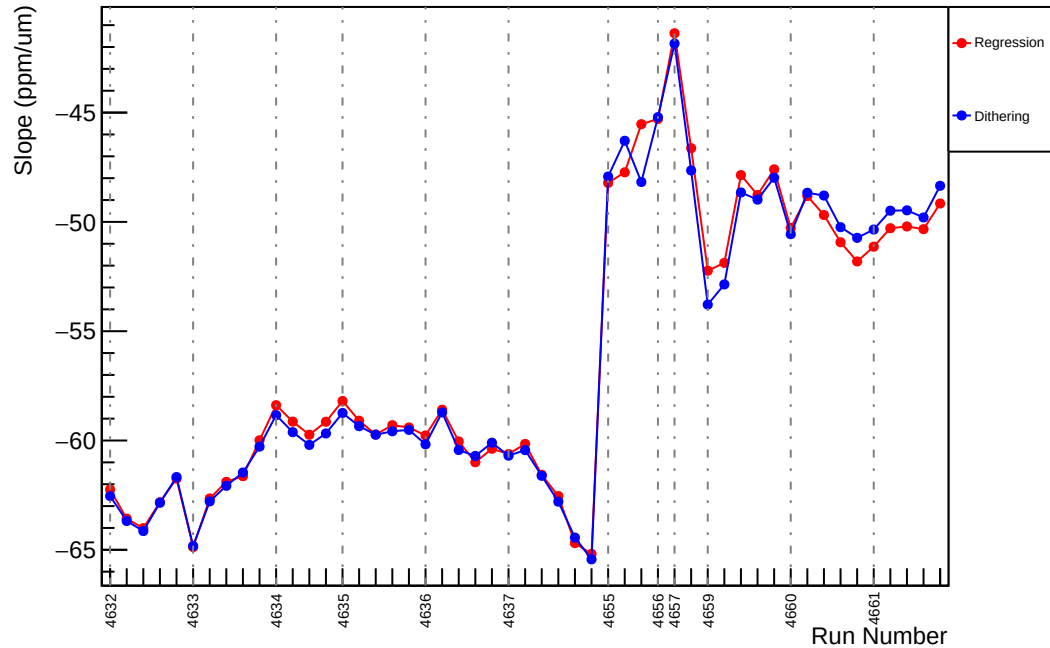
Slug72: Slope : usl_vs_evMon0 (ppm/um)



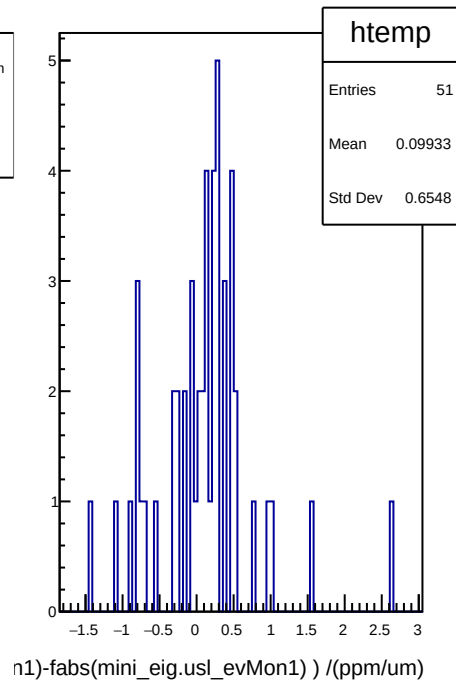
$|\text{dit_slope}| - |\text{reg_slope}|$ (ppm/um)



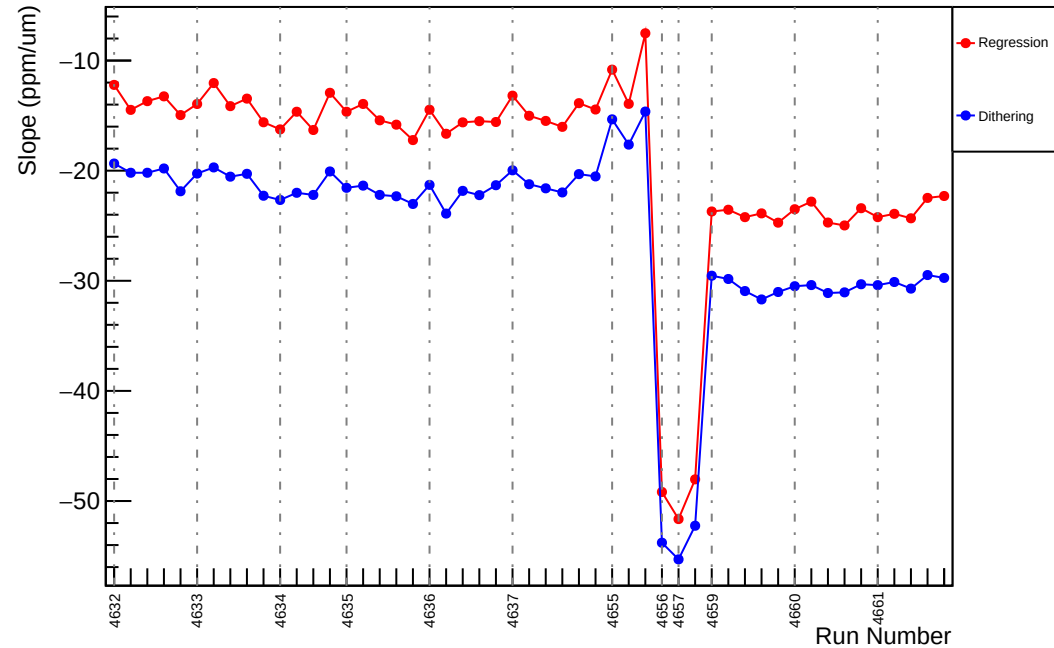
Slug72: Slope : usl_vs_evMon1 (ppm/um)



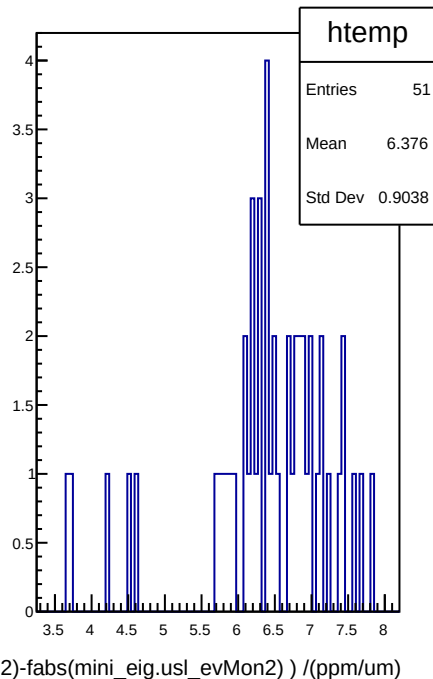
|dit_slope| - |reg_slope| (ppm/um)



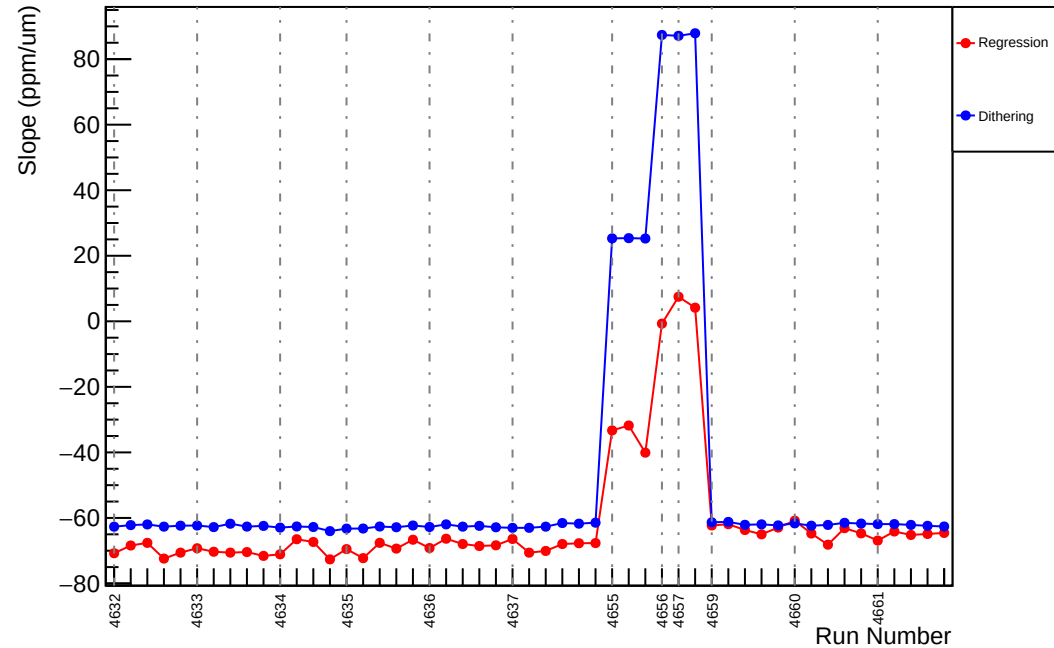
Slug72: Slope : usl_vs_evMon2 (ppm/um)



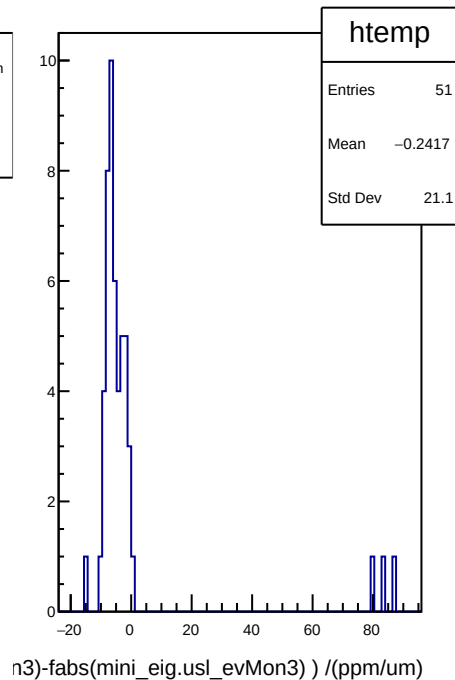
|dit_slope| - |reg_slope| (ppm/um)



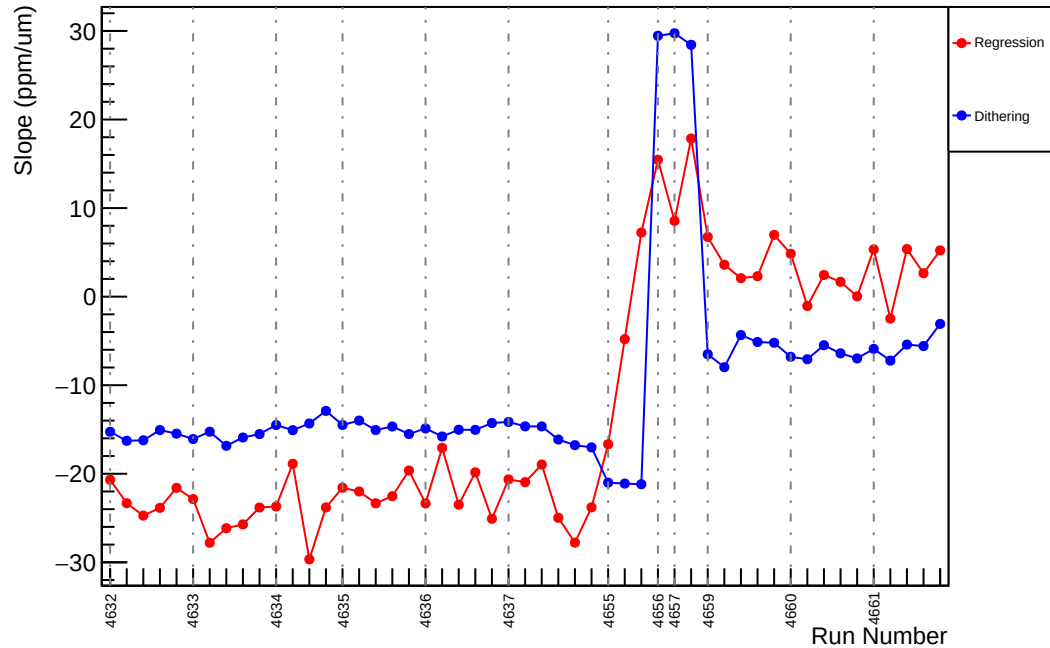
Slug72: Slope : usl_vs_evMon3 (ppm/um)



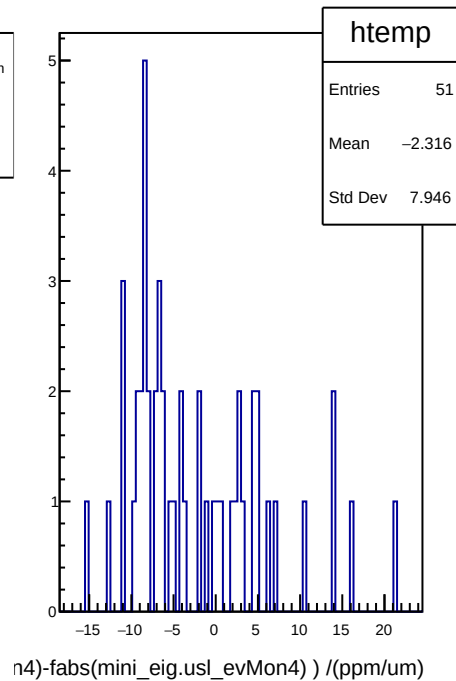
|dit_slope| - |reg_slope| (ppm/um)



Slug72: Slope : usl_vs_evMon4 (ppm/um)



|dit_slope| - |reg_slope| (ppm/um)



htemp

Entries 51

Mean -2.316

Std Dev 7.946

n4)-fabs(mini_eig.usl_evMon4))/(ppm/um)