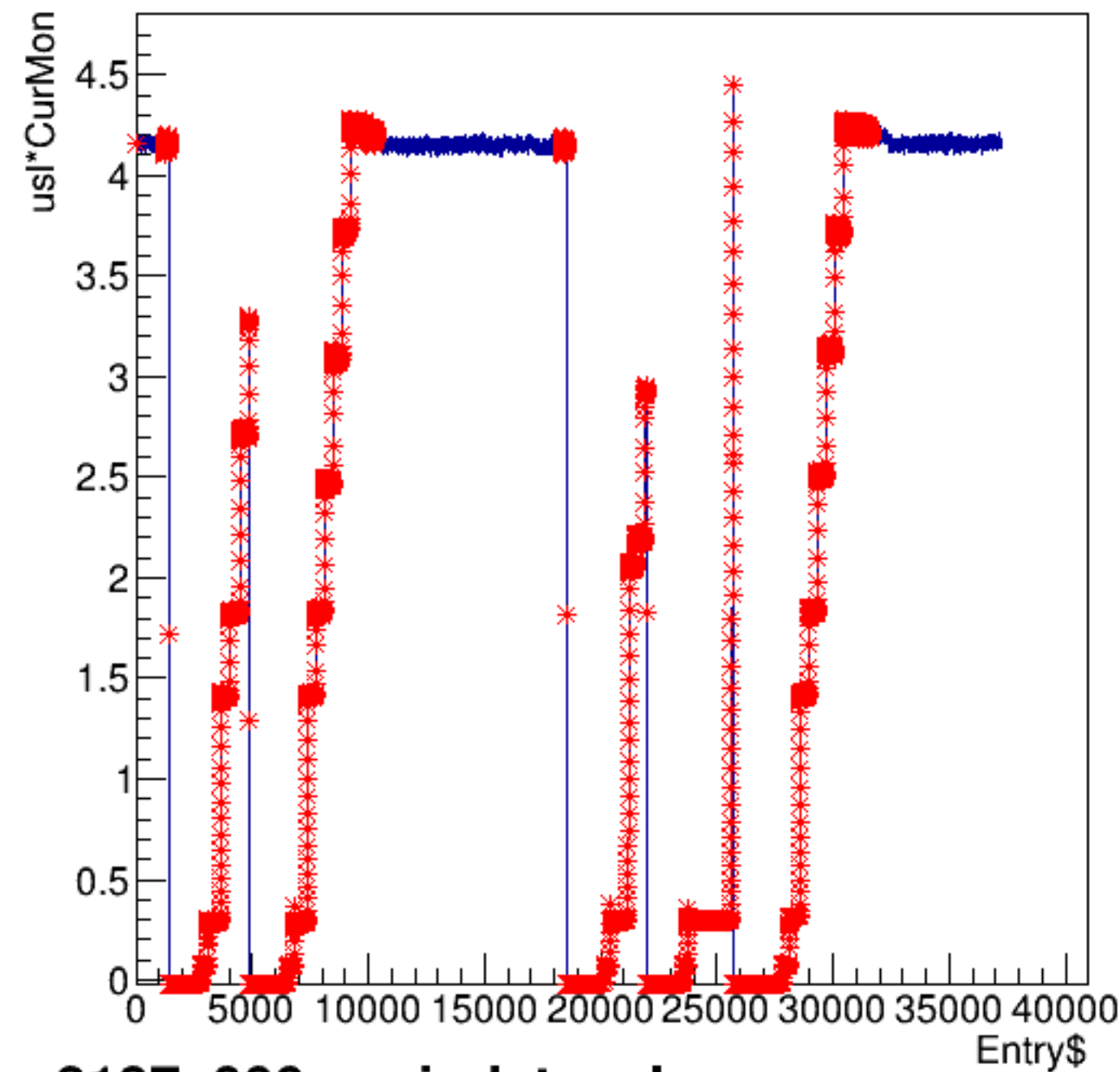
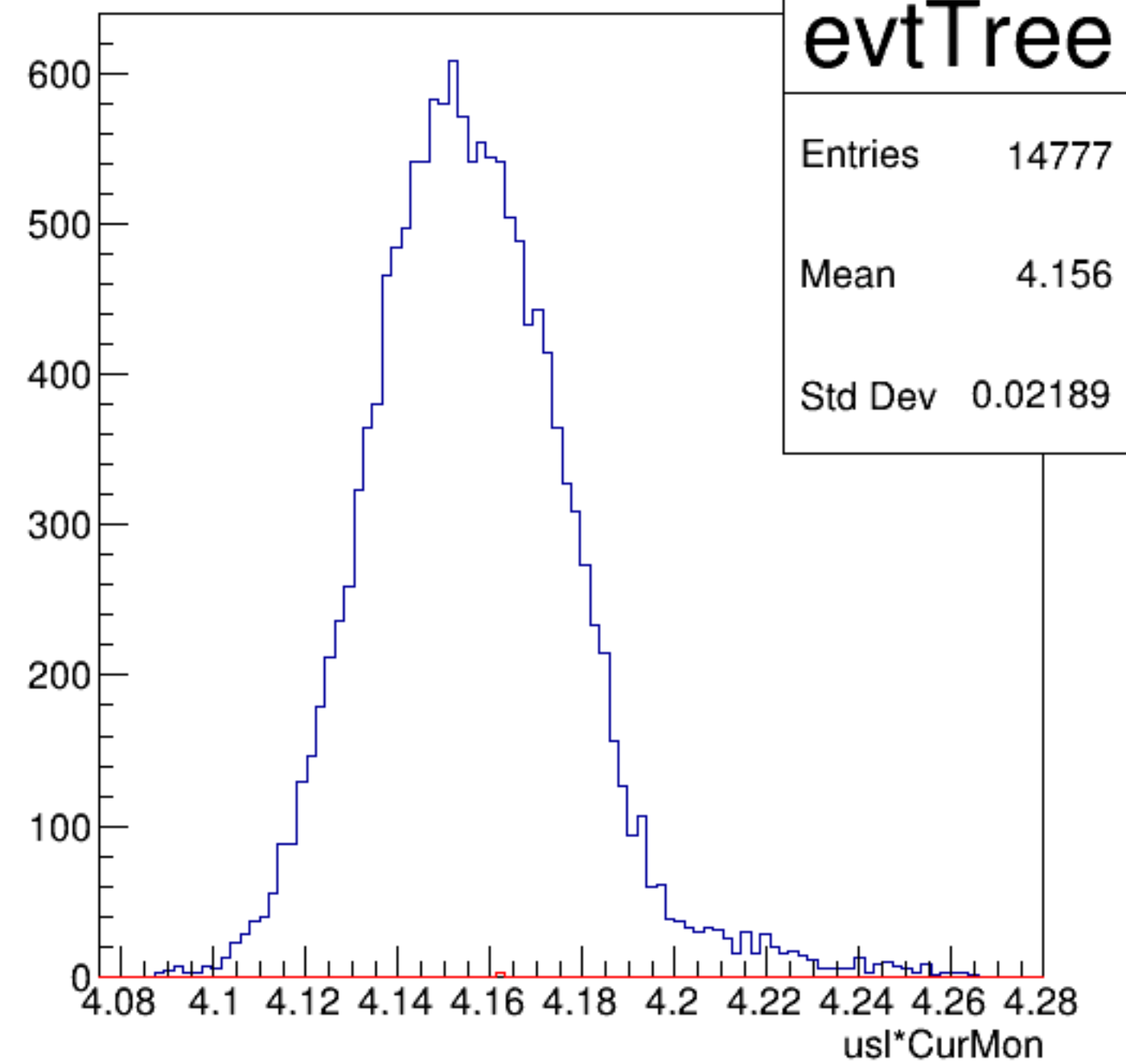


usl*CurMon:Entry\$



run3137_000_maindet_usl.png

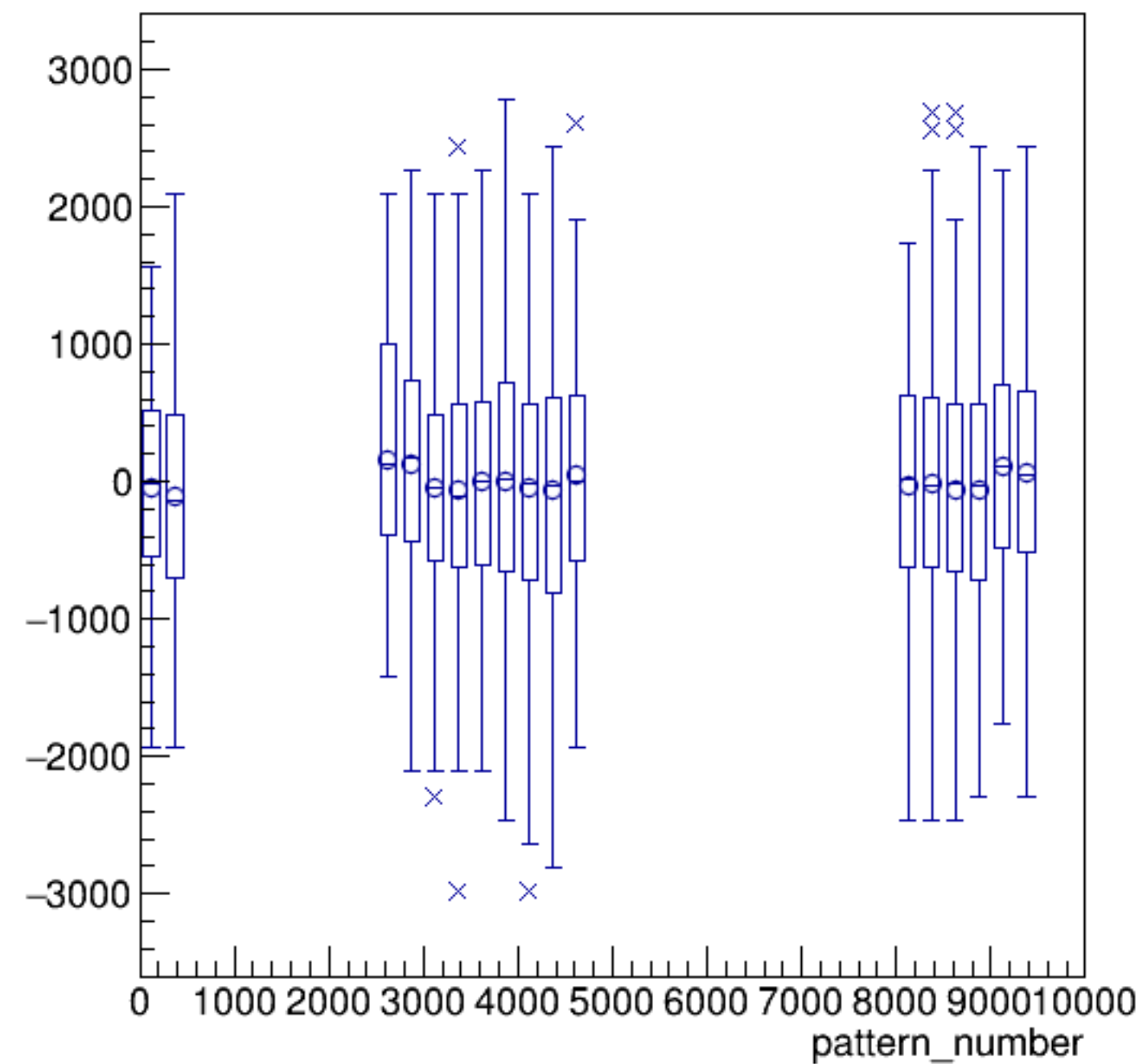
usl*CurMon {ErrorFlag==0}



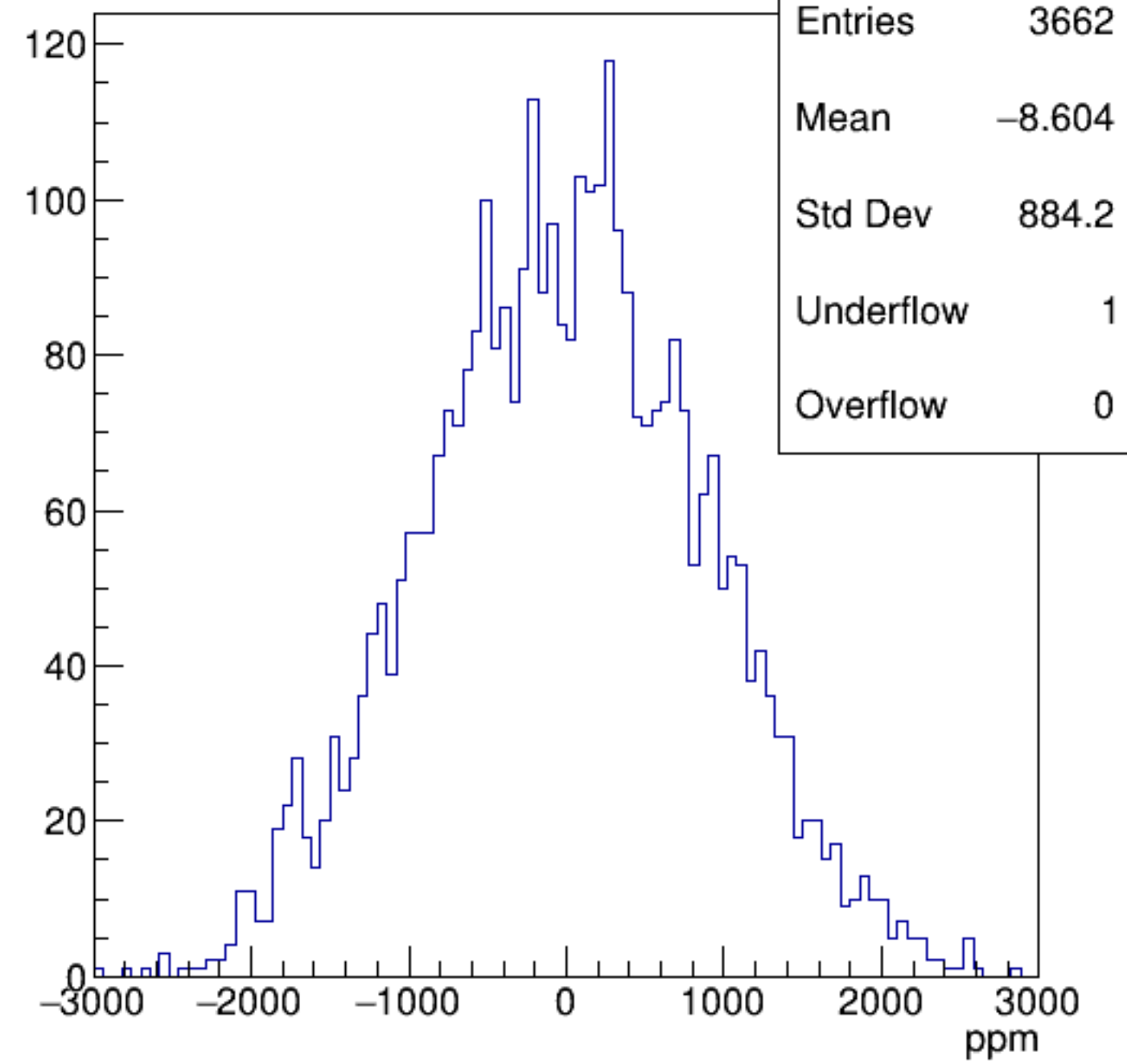
evtTree

Entries	14777
Mean	4.156
Std Dev	0.02189

asym_usl/ppm:pattern_number {ErrorFlag==0}

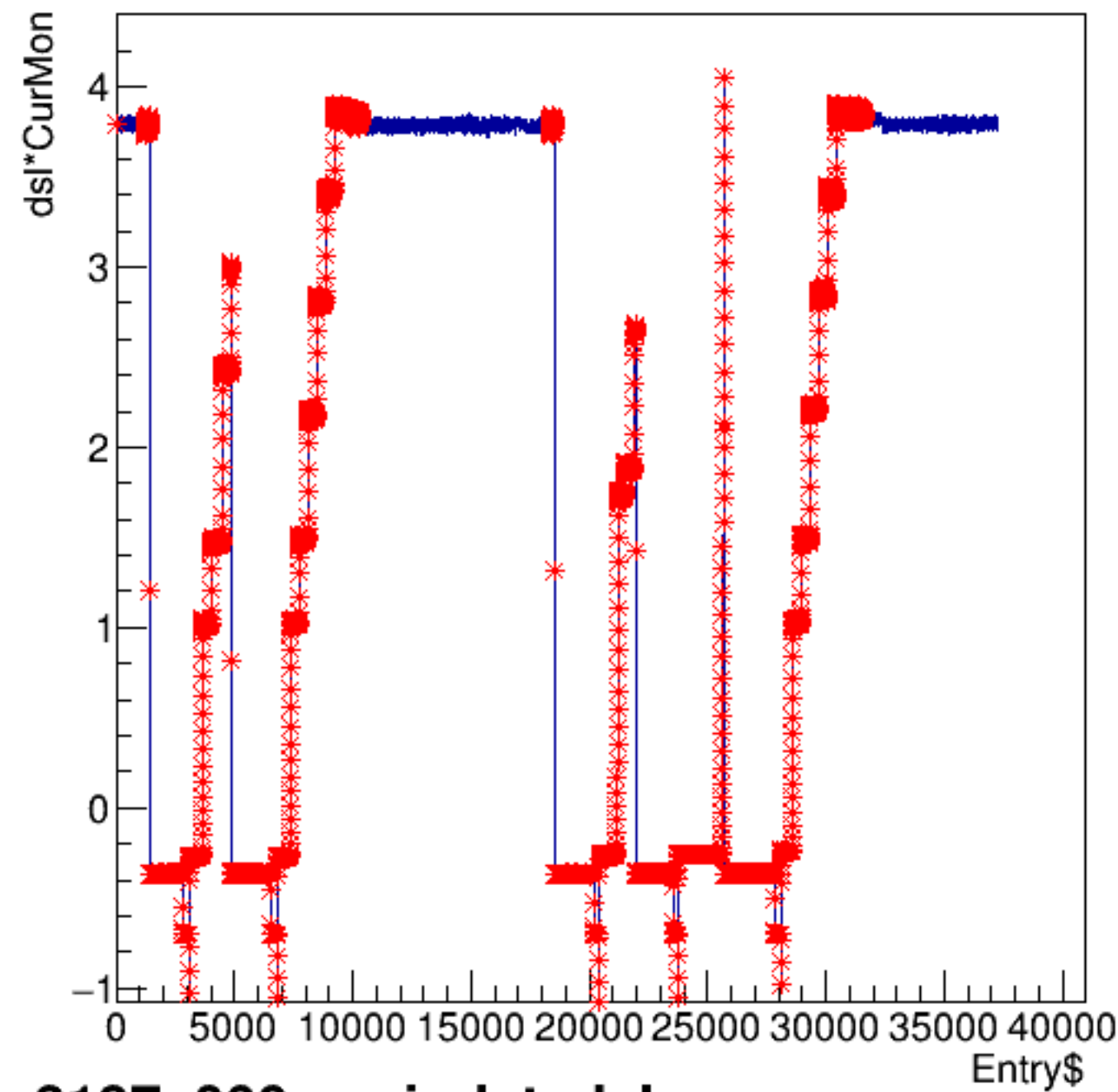


asym_usl



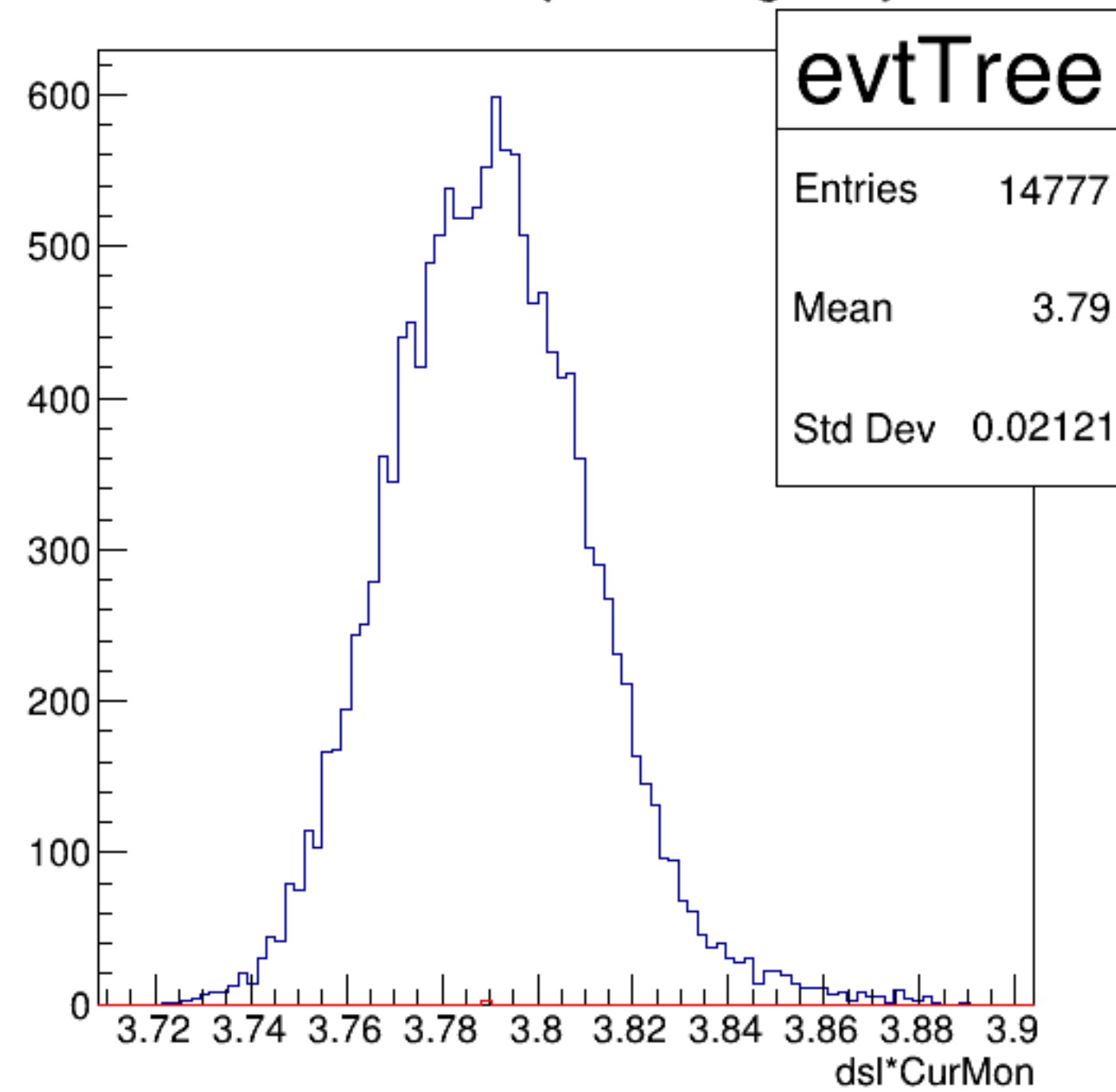
Entries	3662
Mean	-8.604
Std Dev	884.2
Underflow	1
Overflow	0

dsl*CurMon:Entry\$

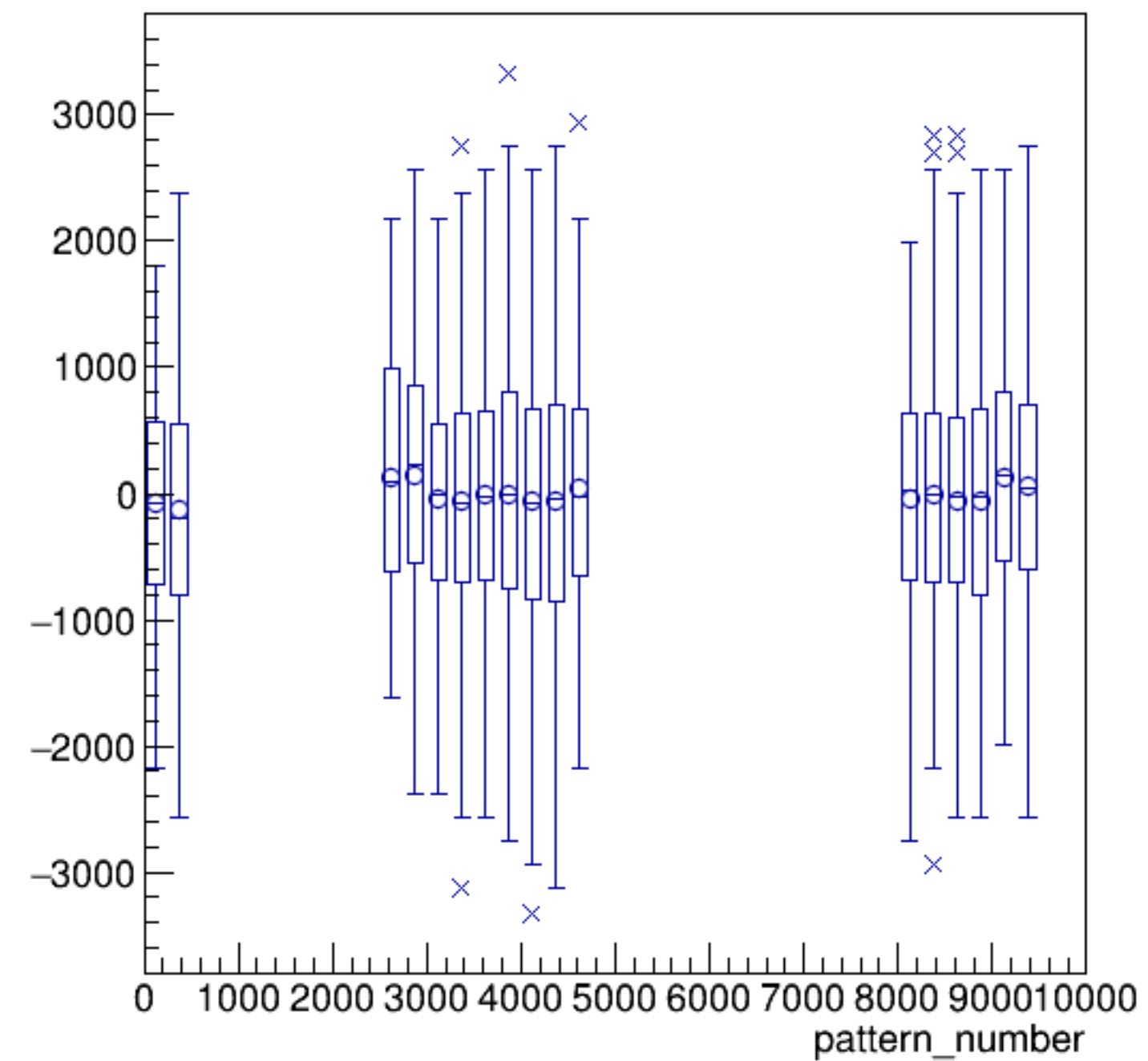


run3137_000_maindet_dsl.png

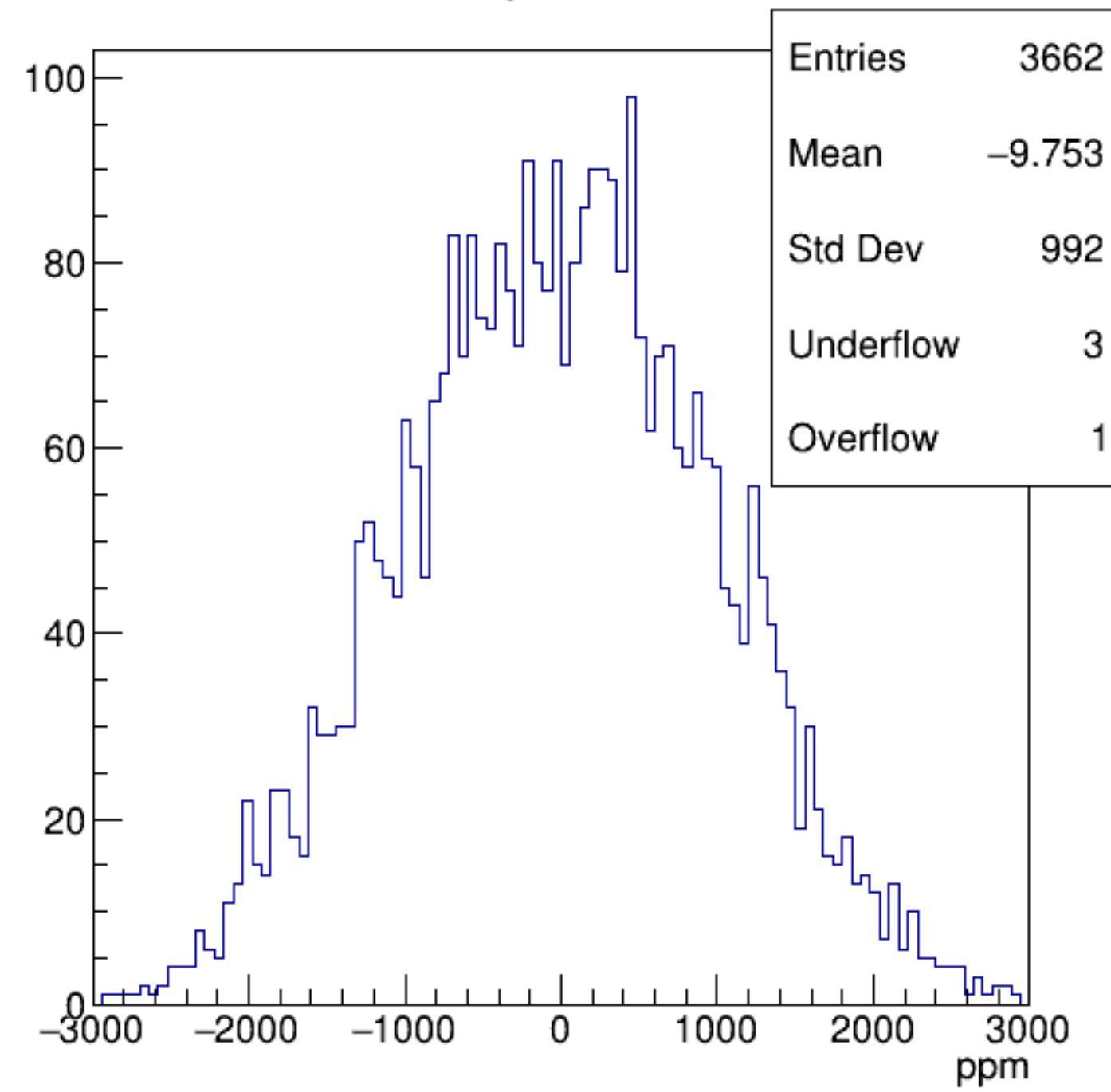
dsl*CurMon {ErrorFlag==0}



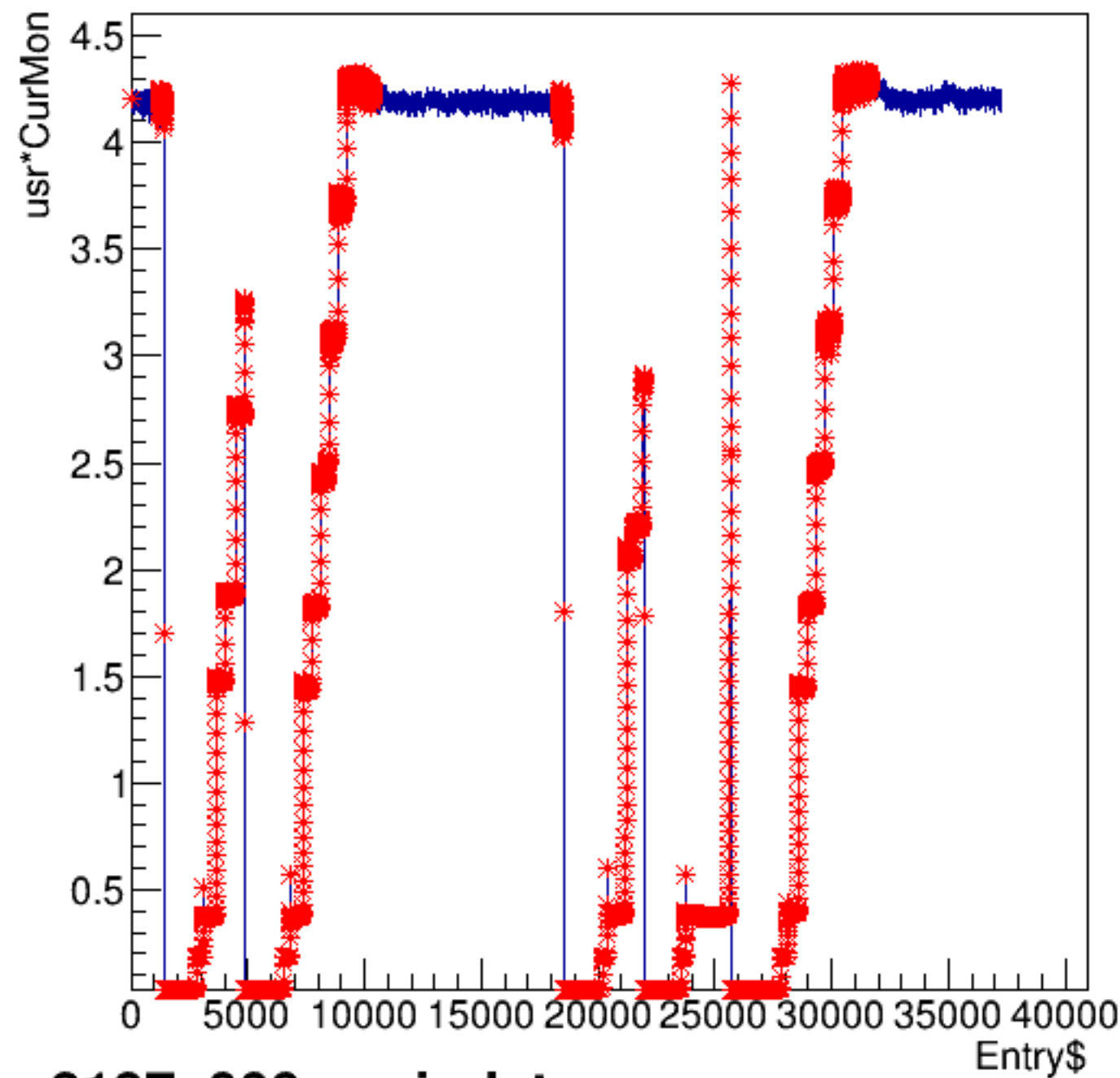
asym_dsl/ppm:pattern_number {ErrorFlag==0}



asym_dsl

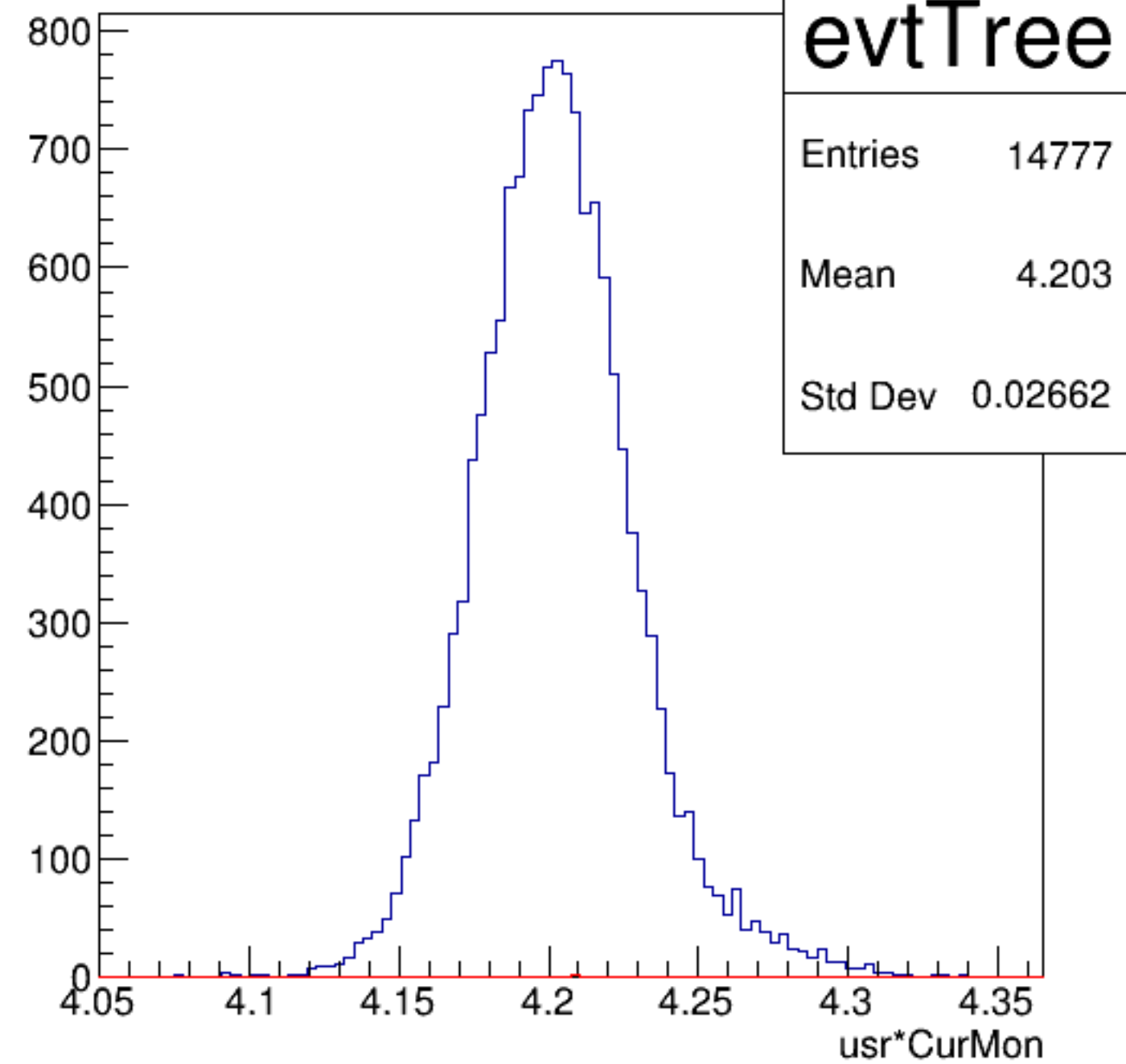


usr*CurMon:Entry\$

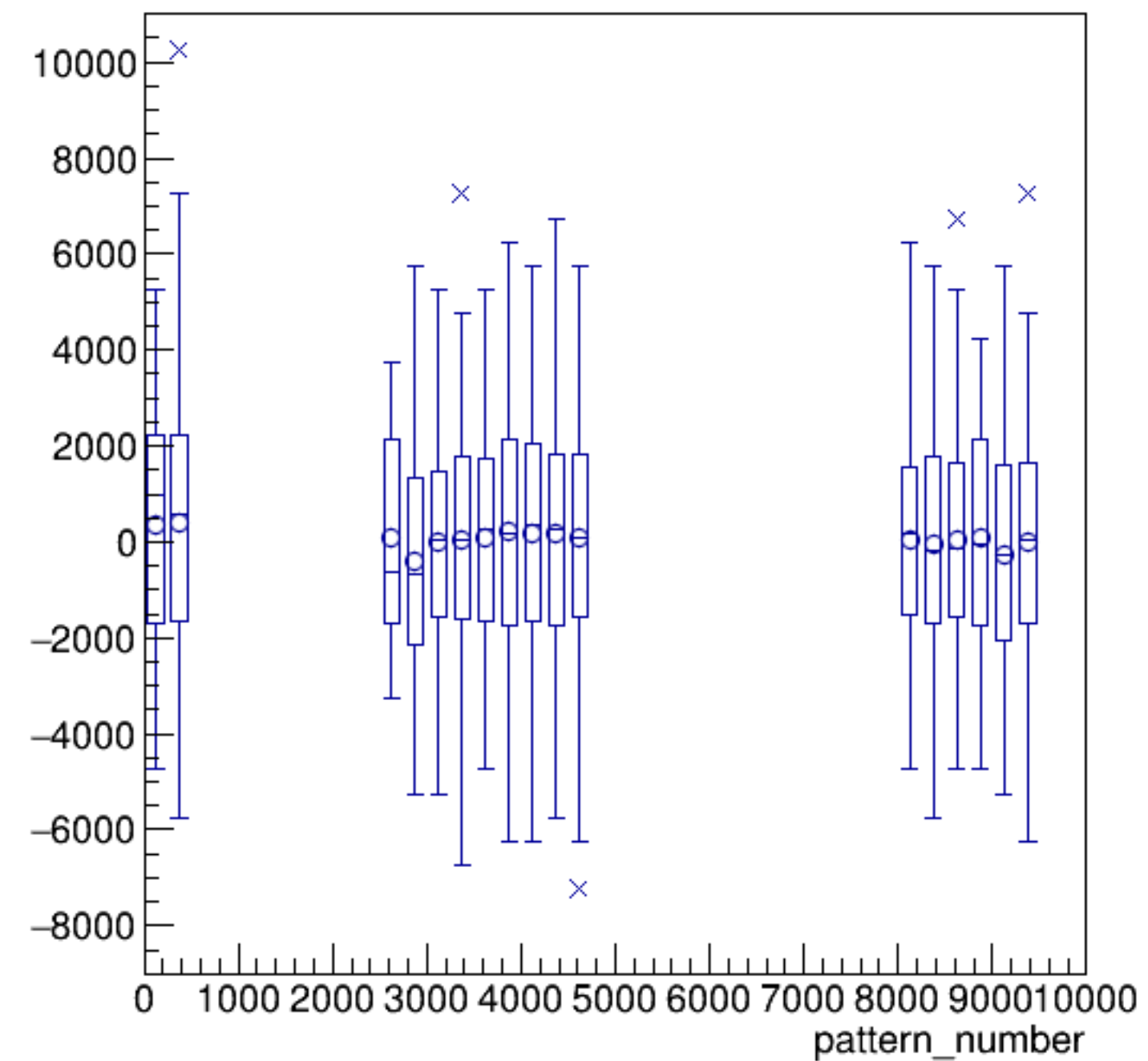


run3137_000_maindet_usr.png

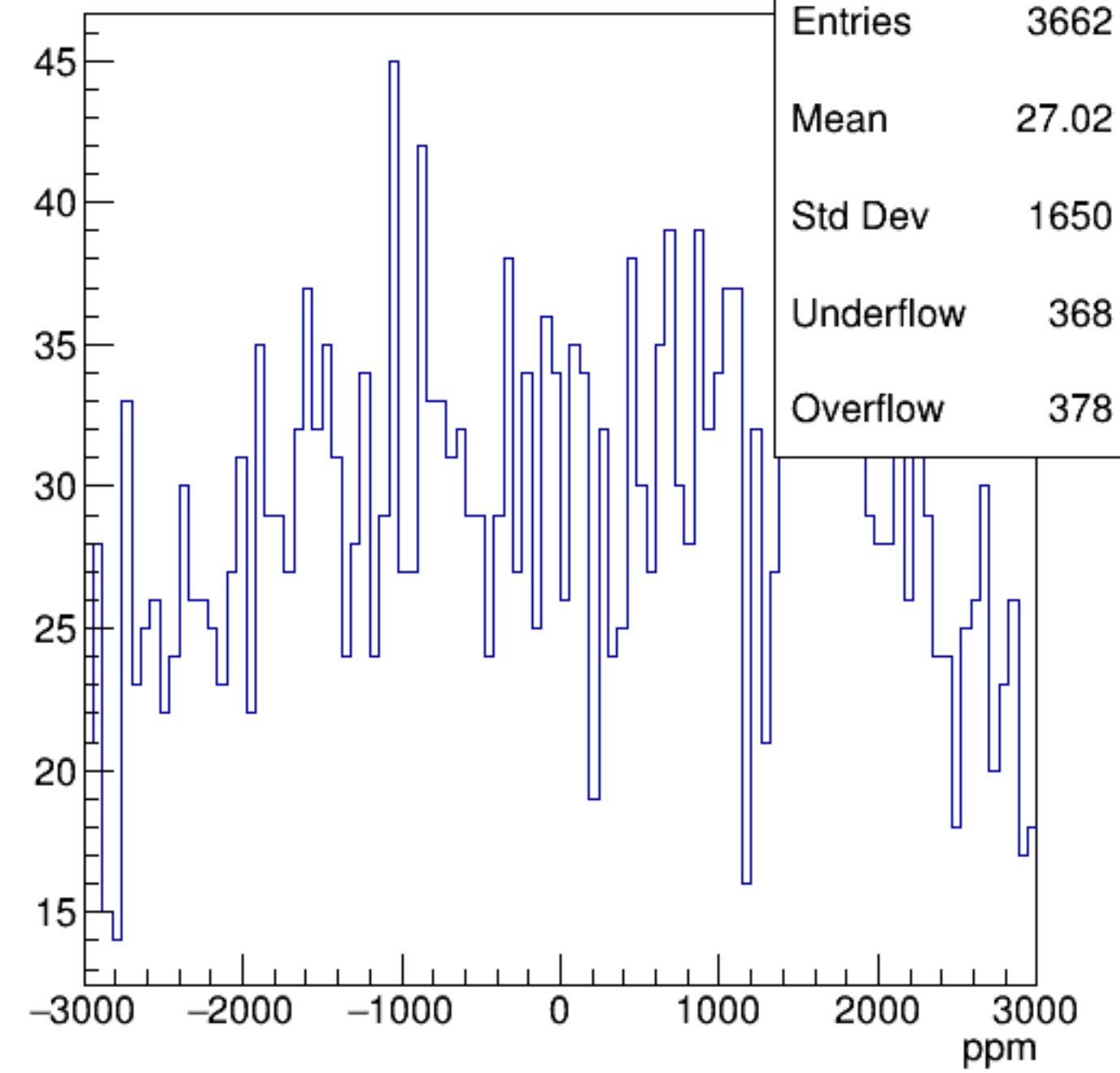
usr*CurMon {ErrorFlag==0}



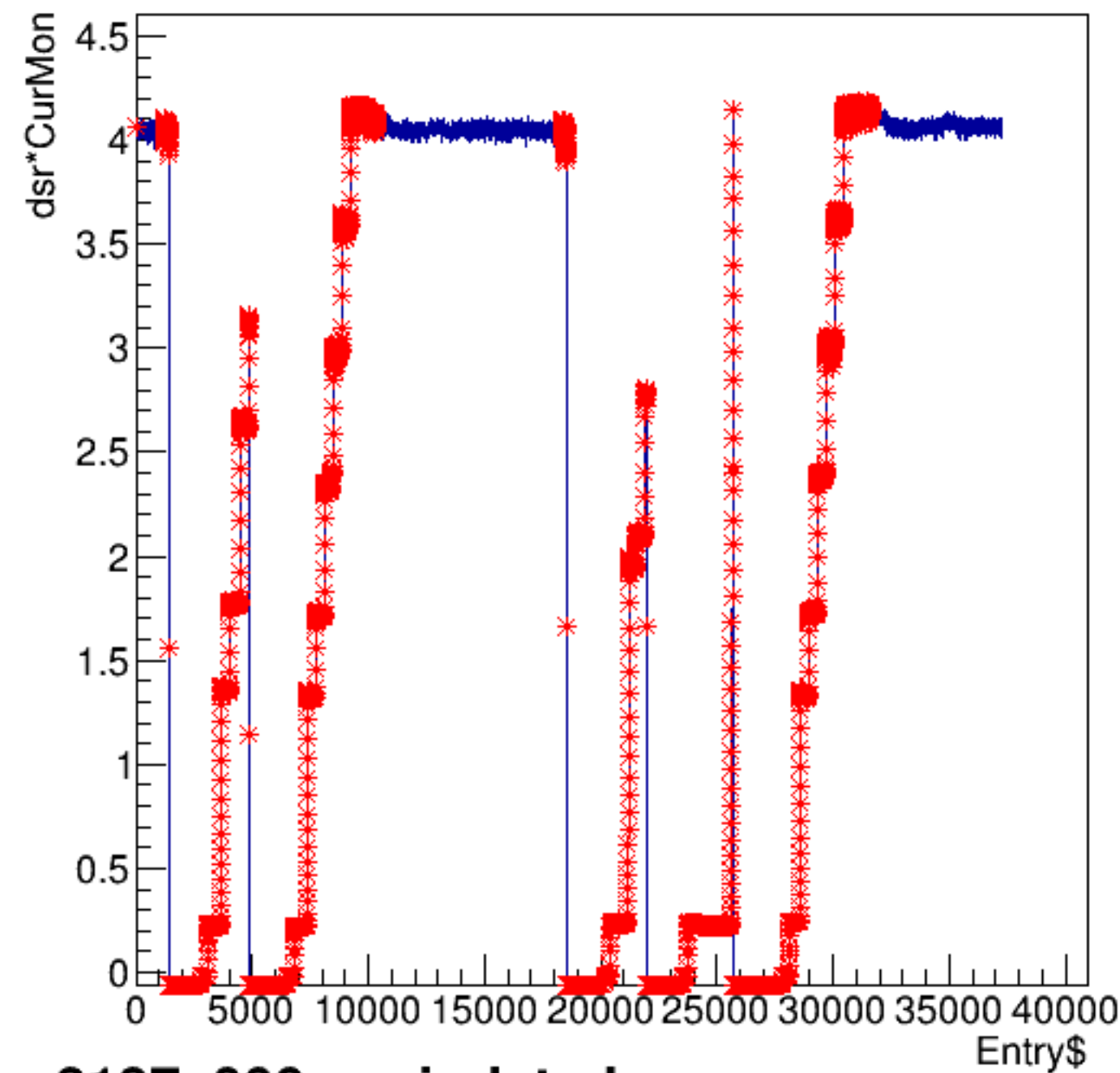
asym_usr/ppm:pattern_number {ErrorFlag==0}



asym_usr

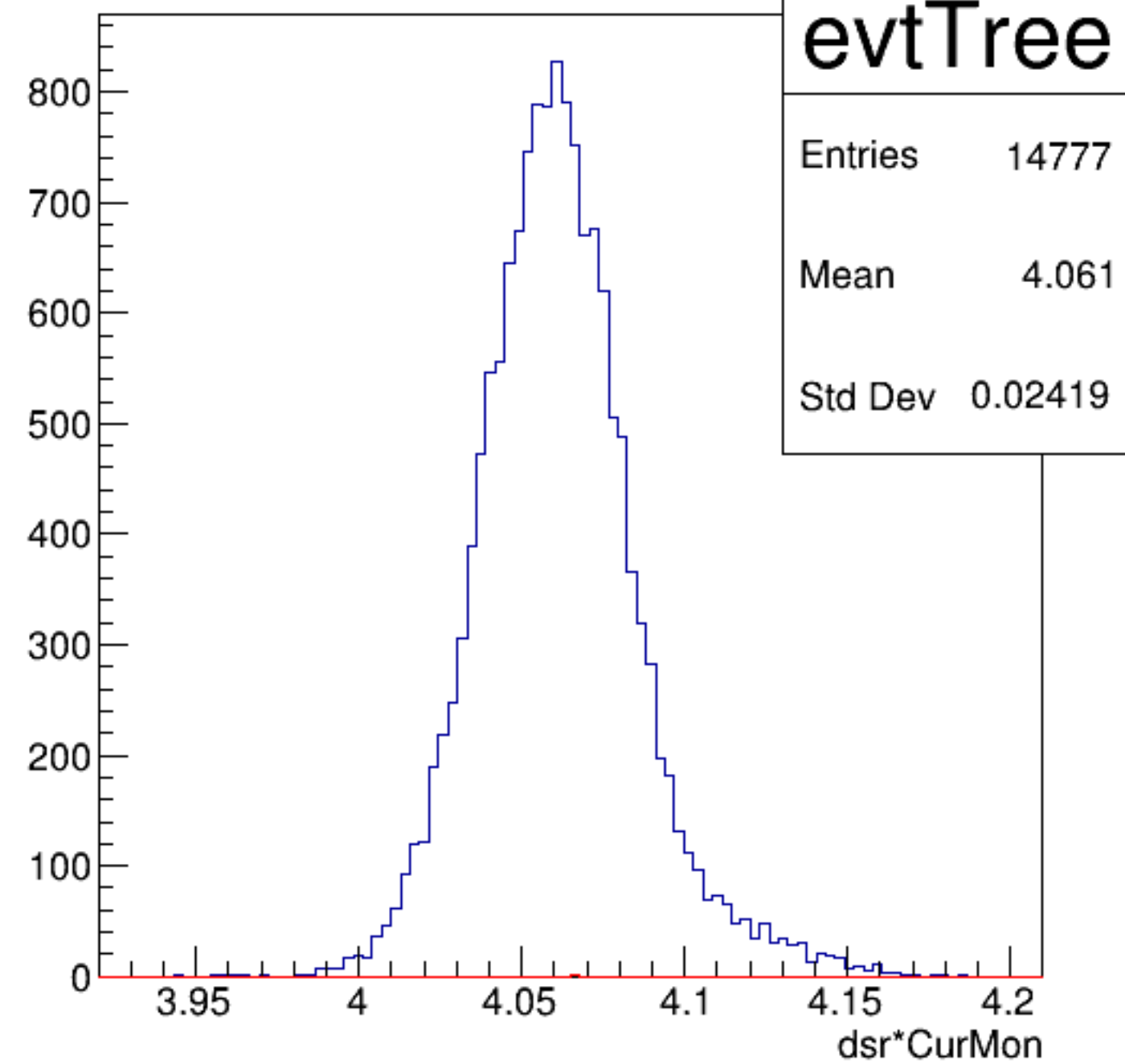


dsr*CurMon:Entry\$

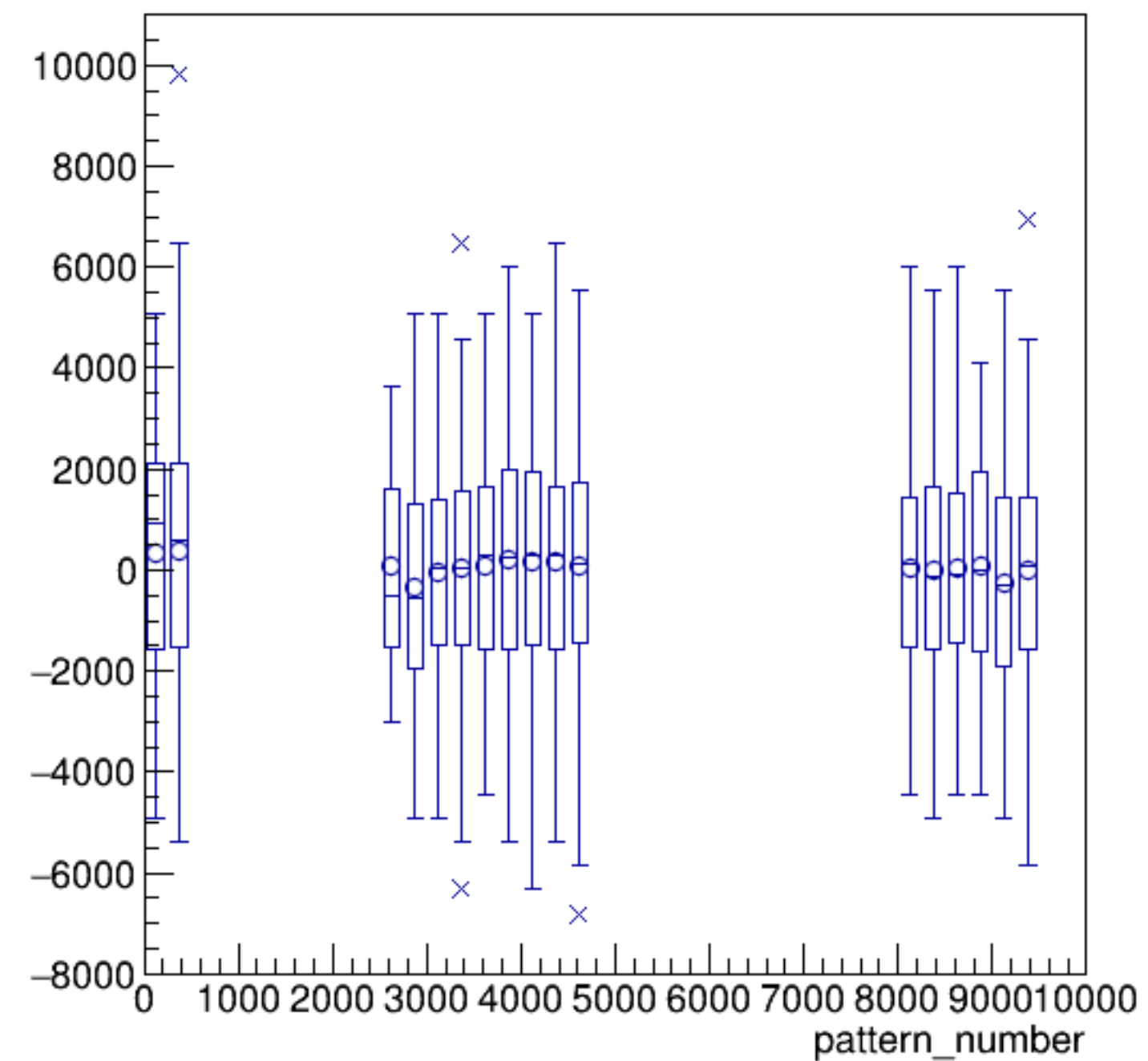


run3137_000_maindet_dsr.png

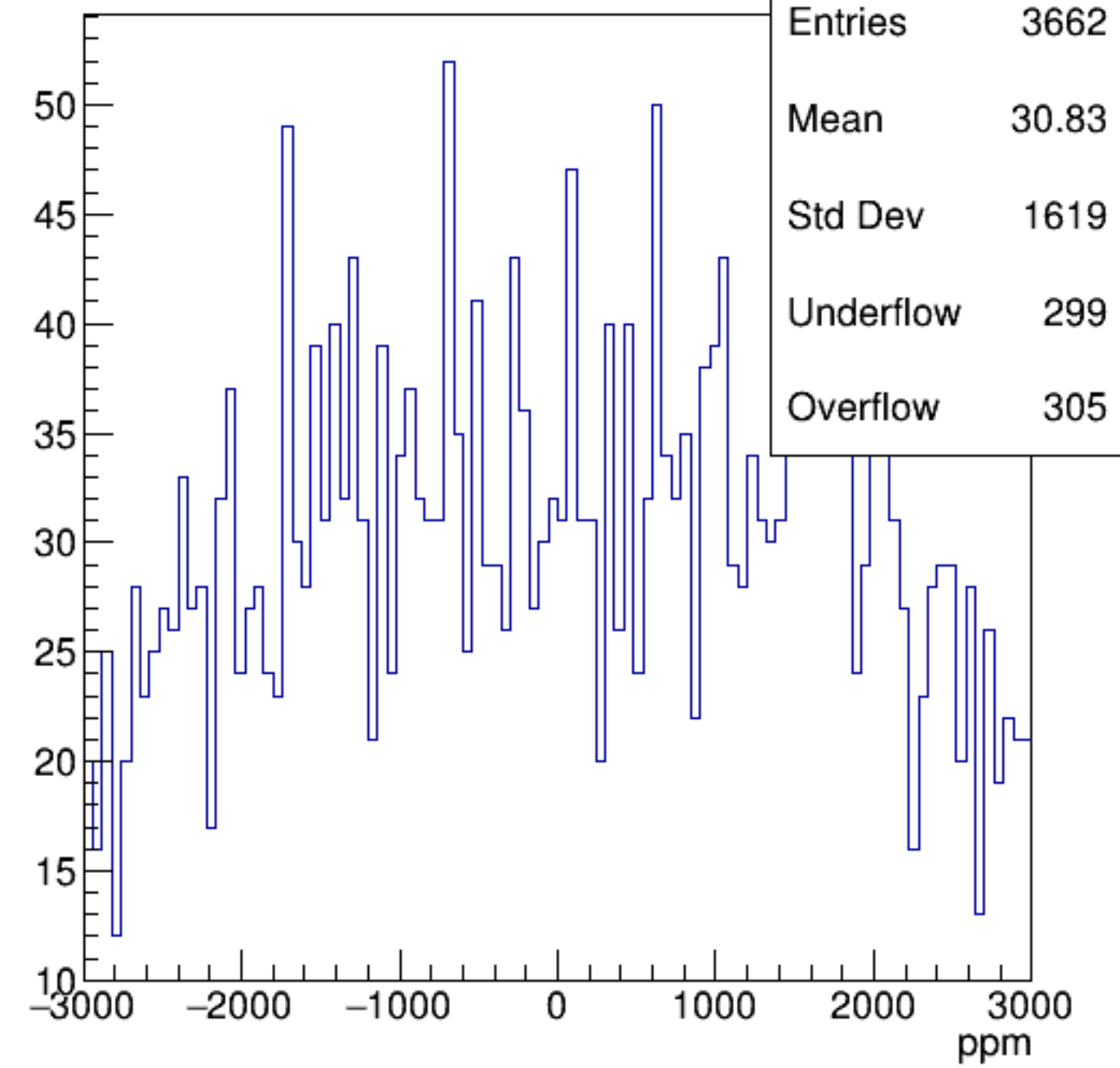
dsr*CurMon {ErrorFlag==0}



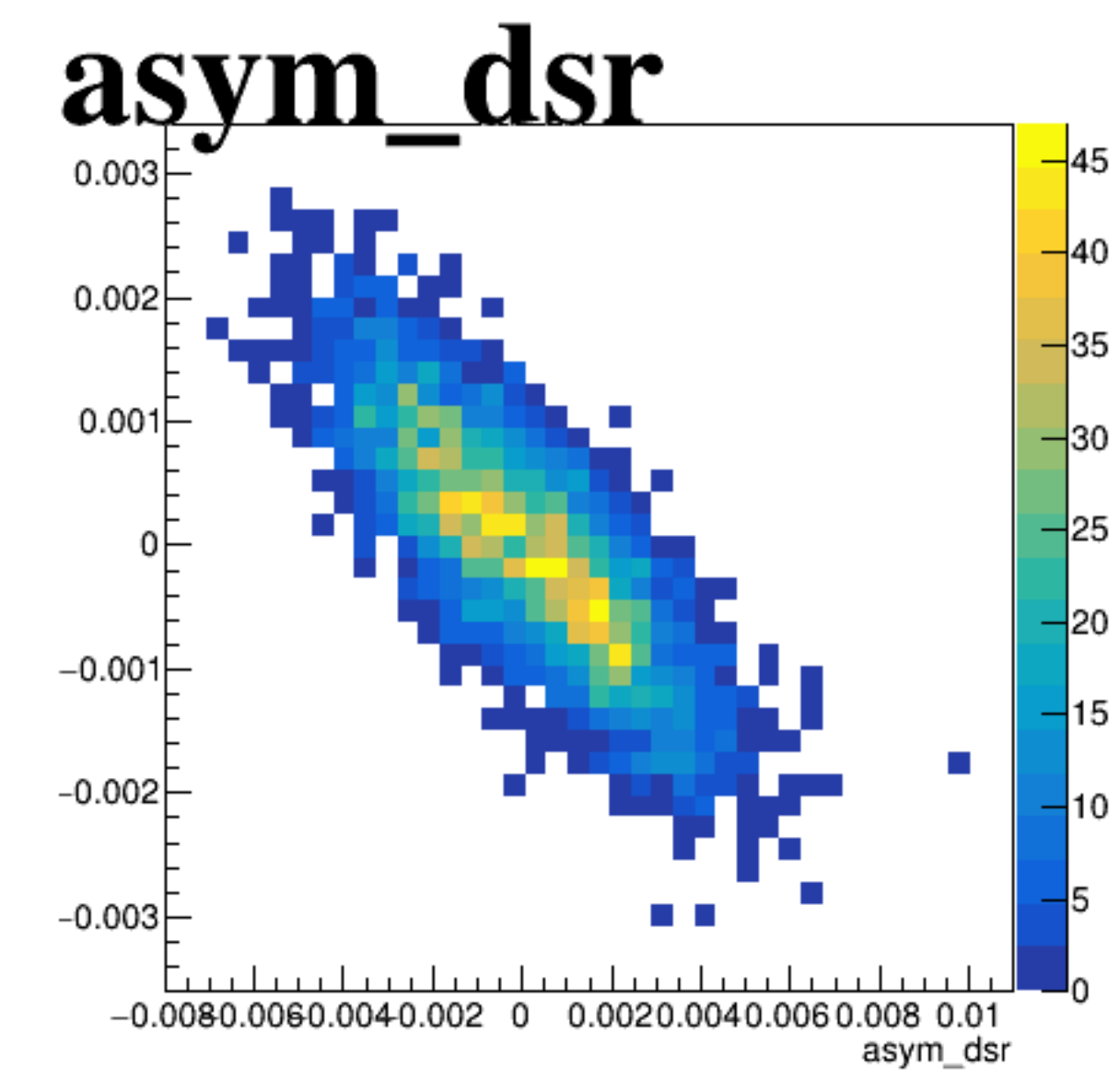
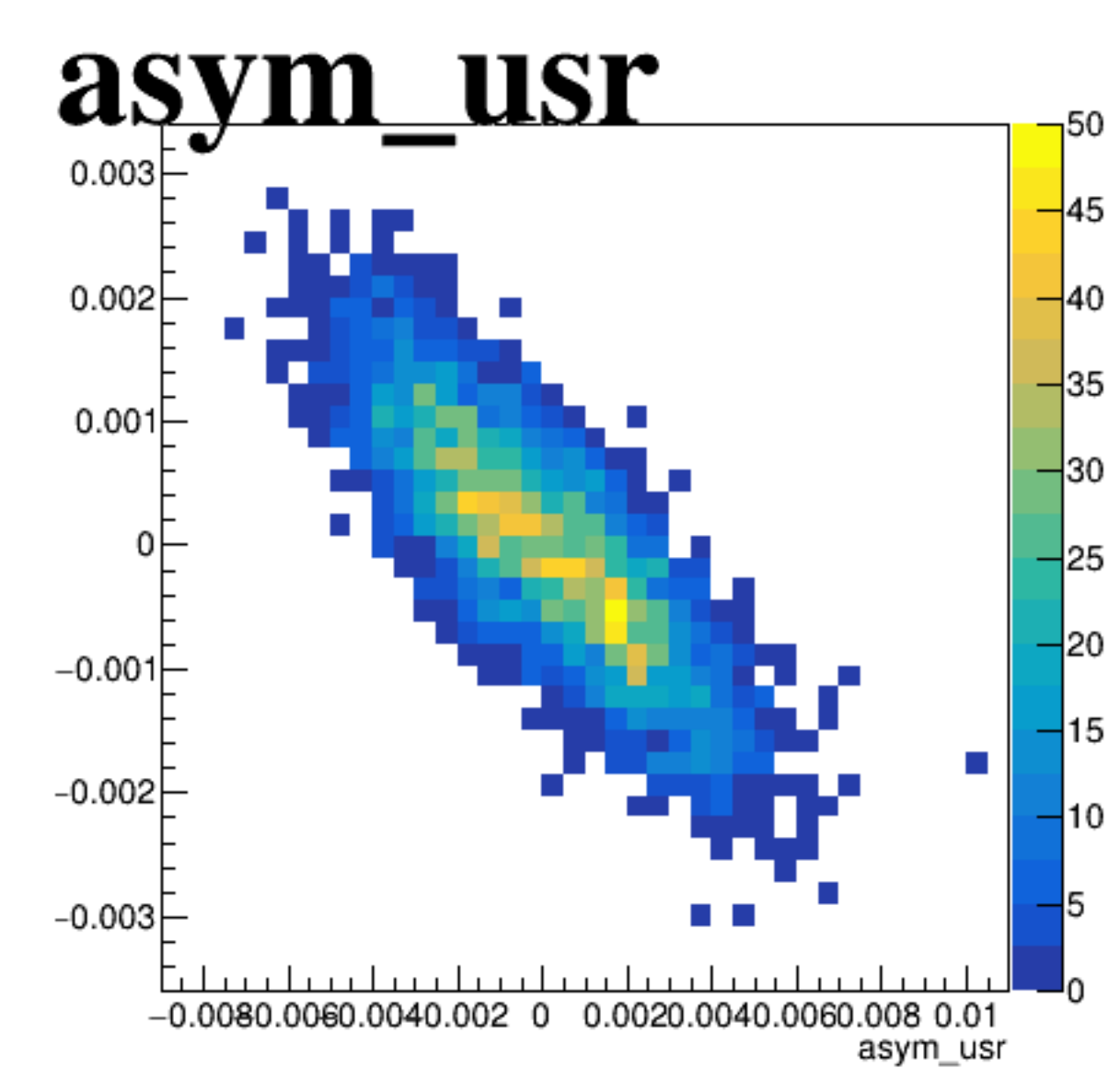
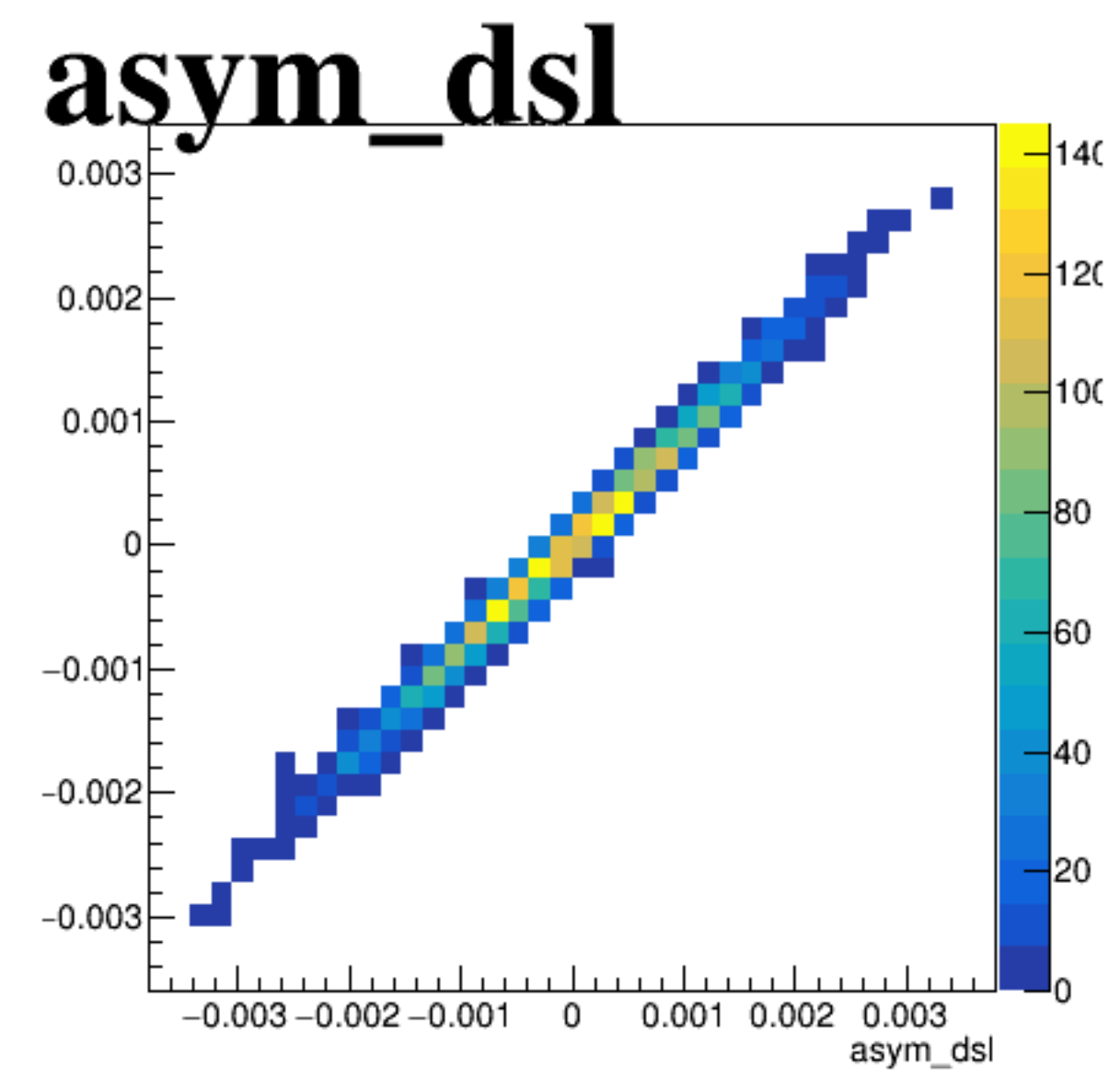
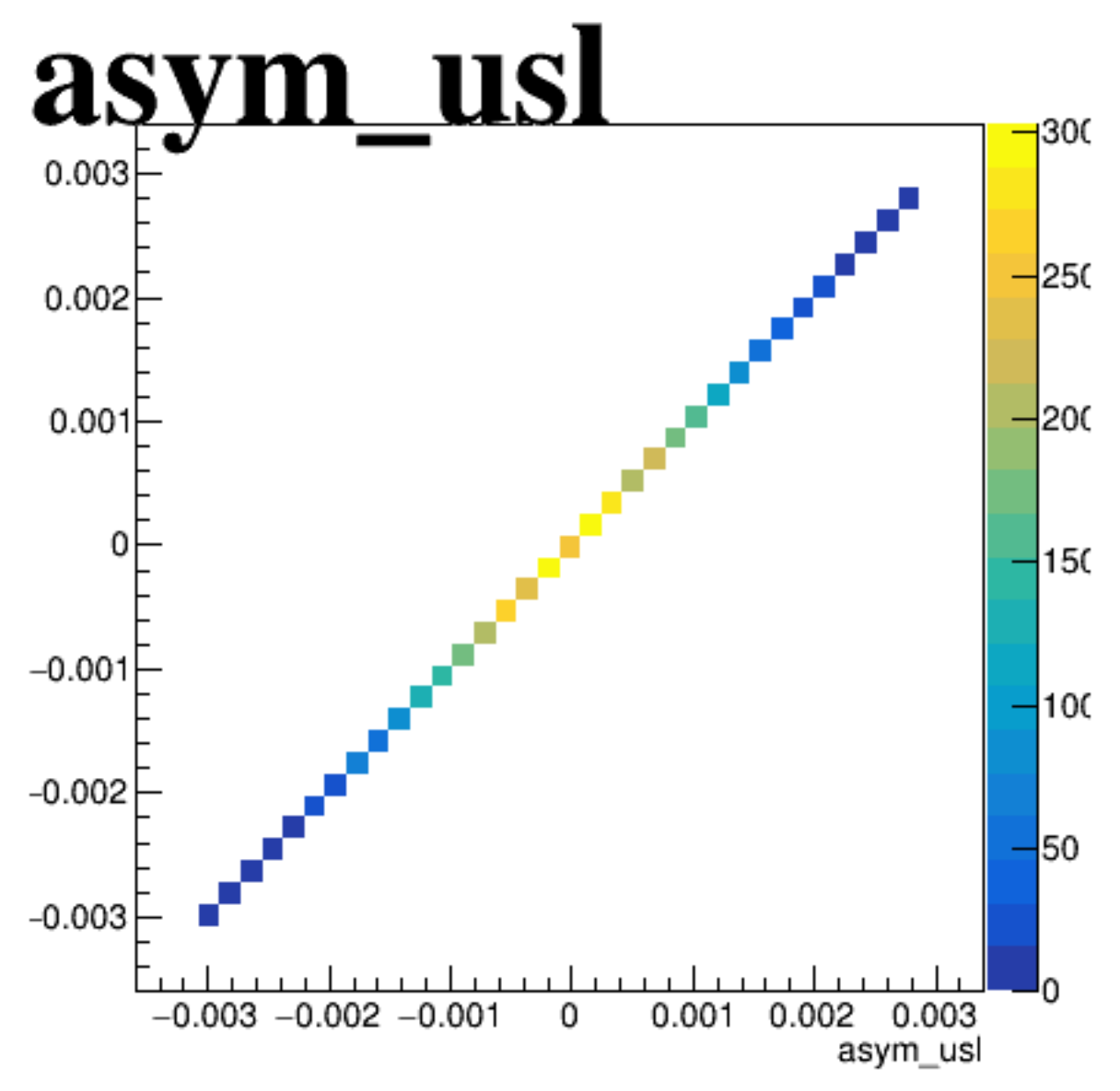
asym_dsr/ppm:pattern_number {ErrorFlag==0}



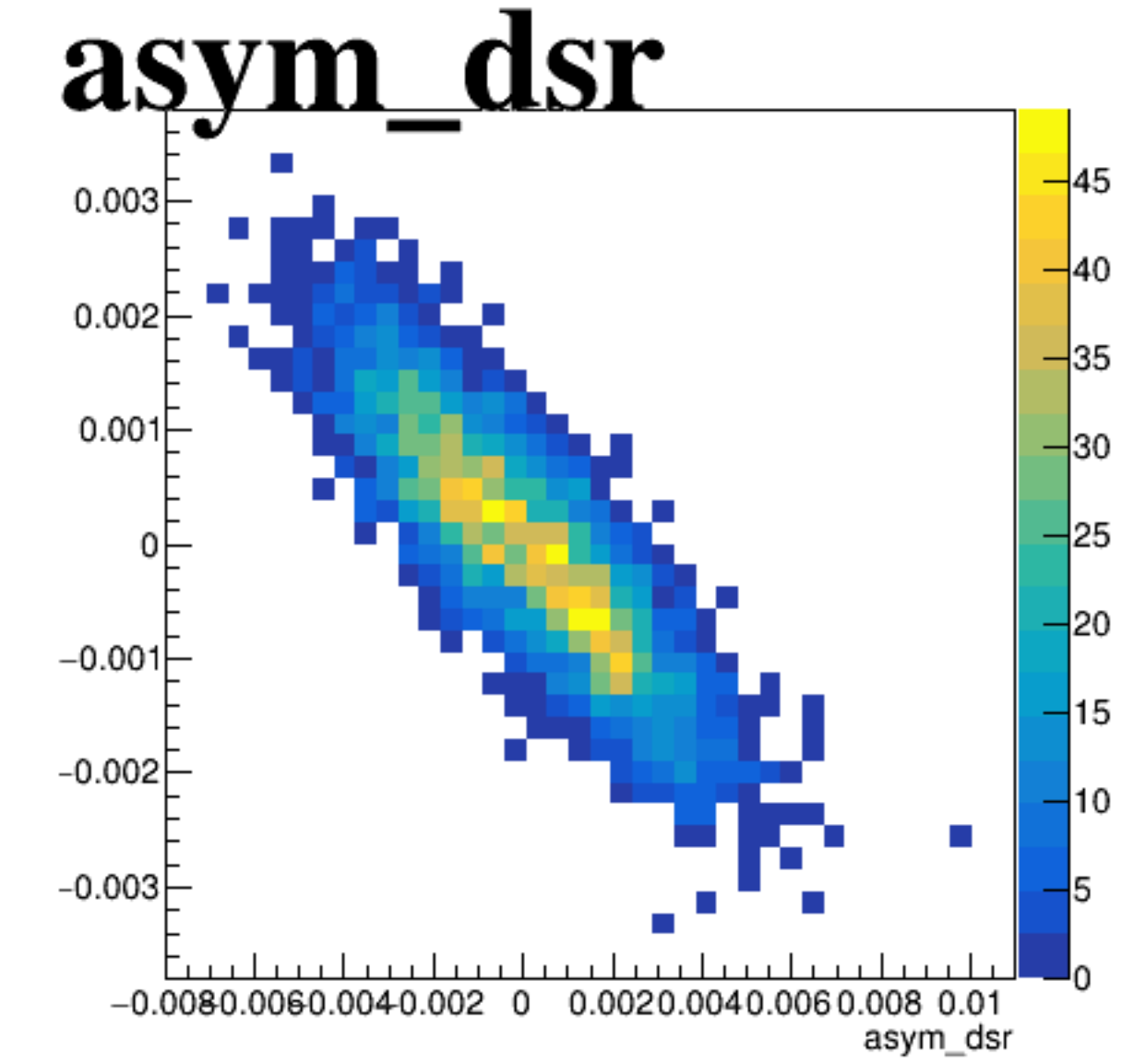
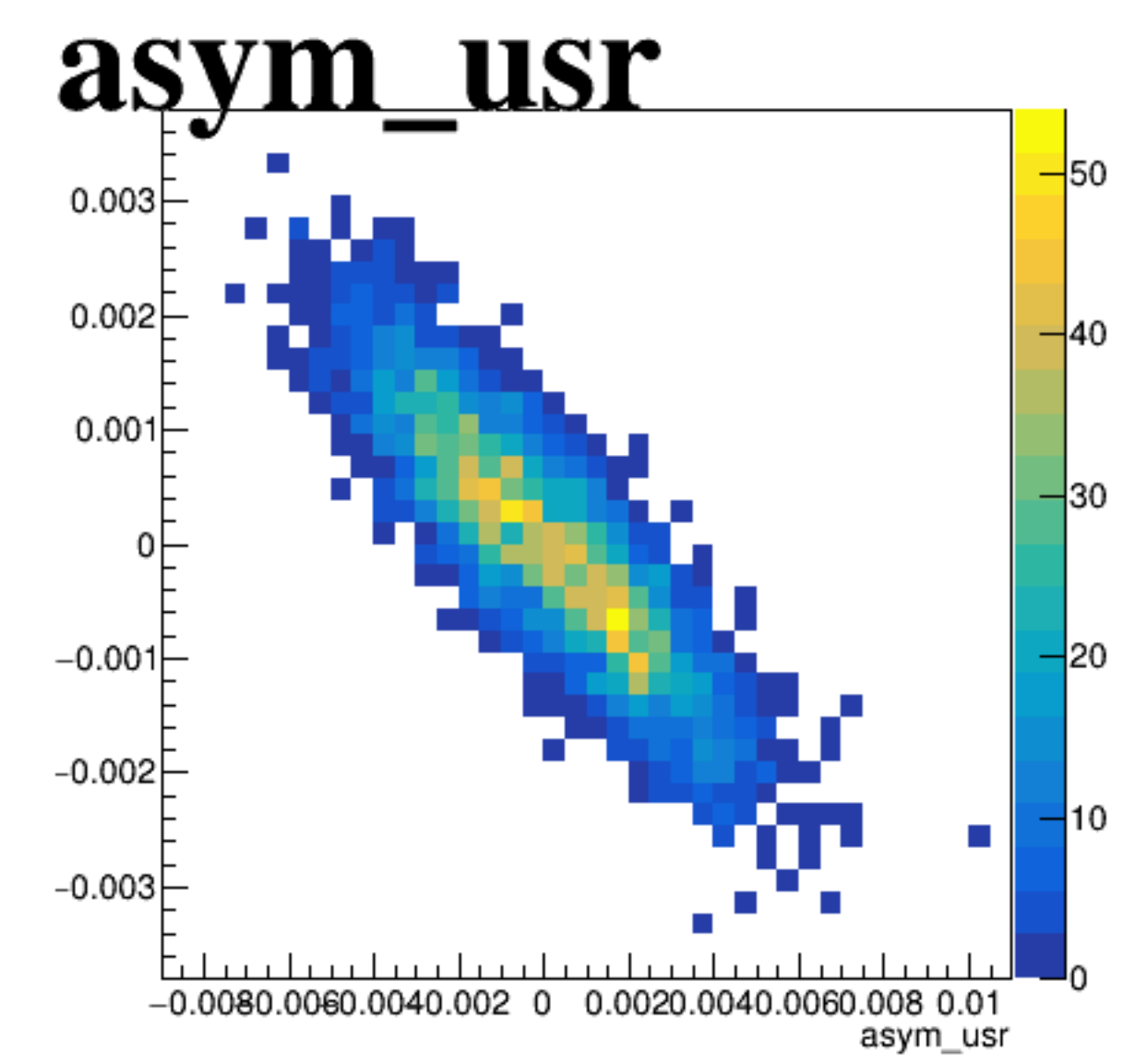
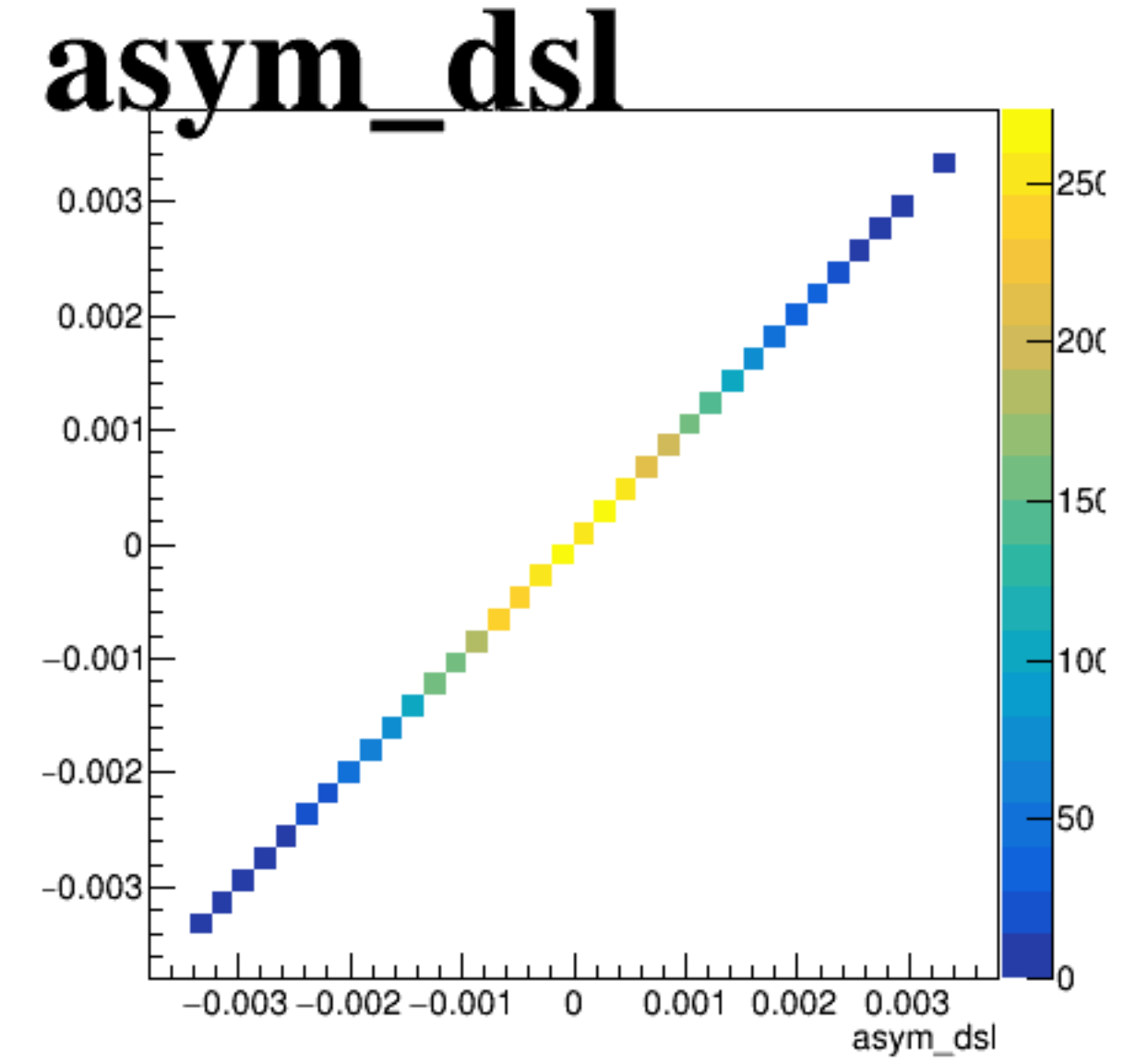
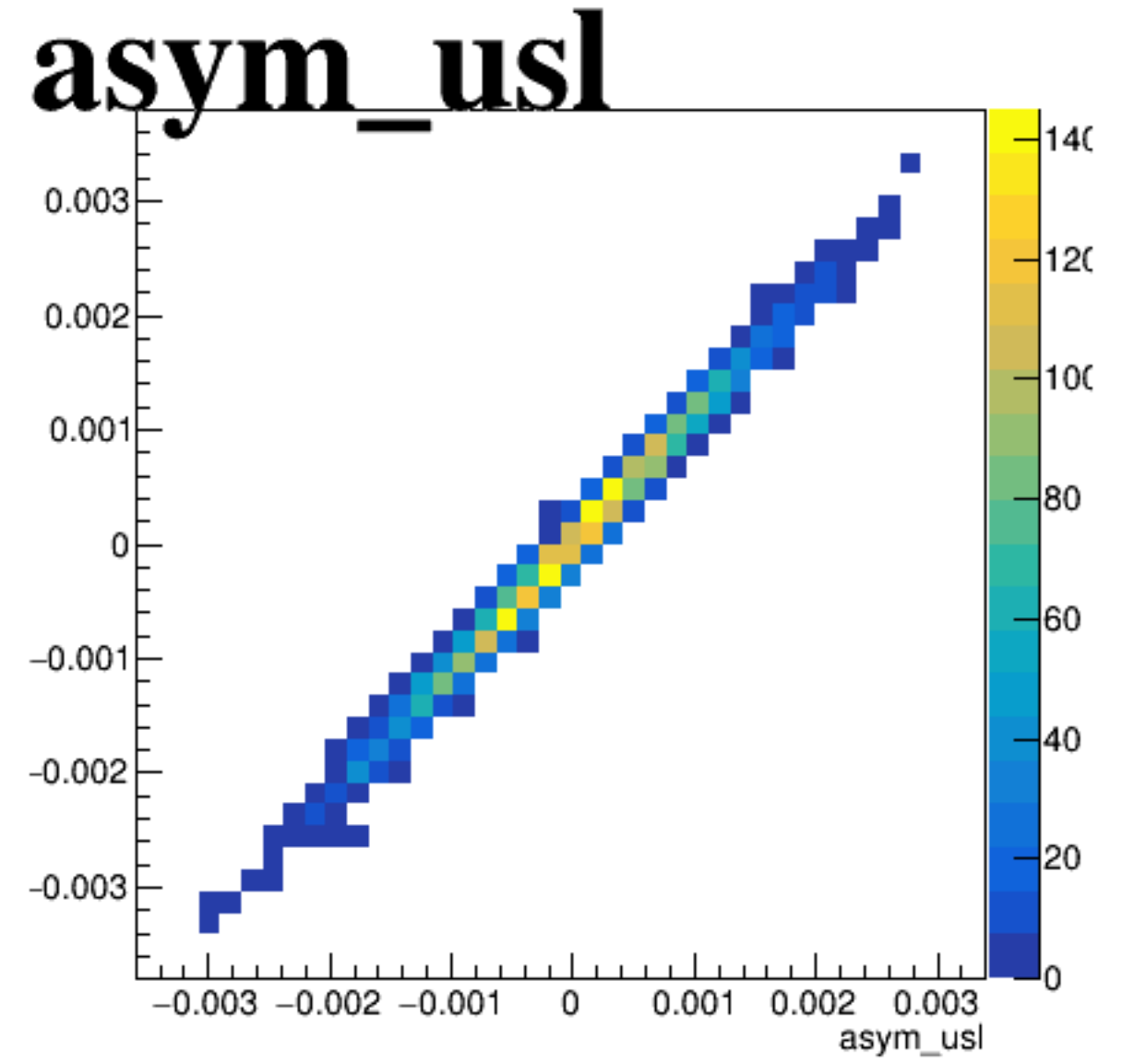
asym_dsr



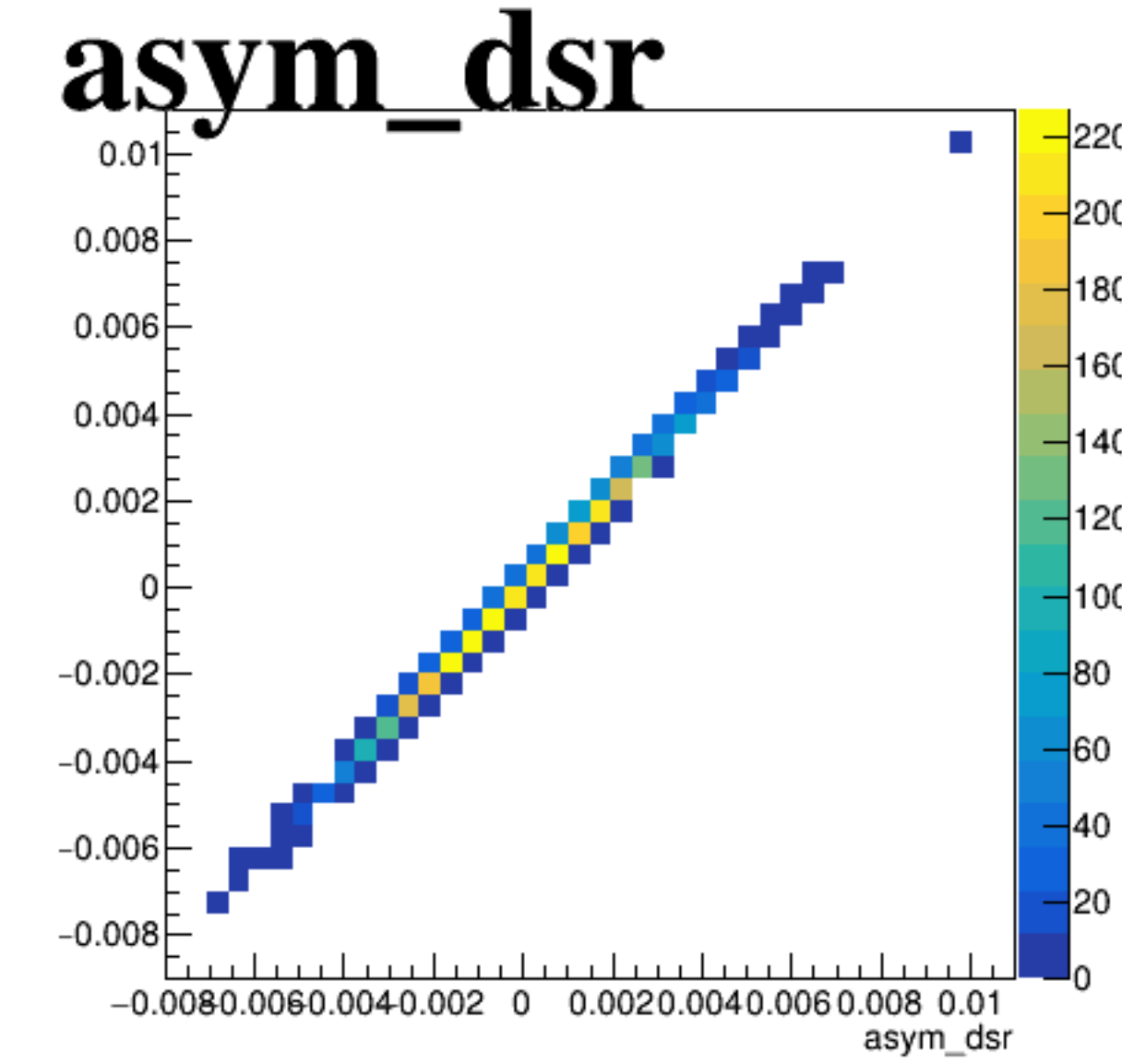
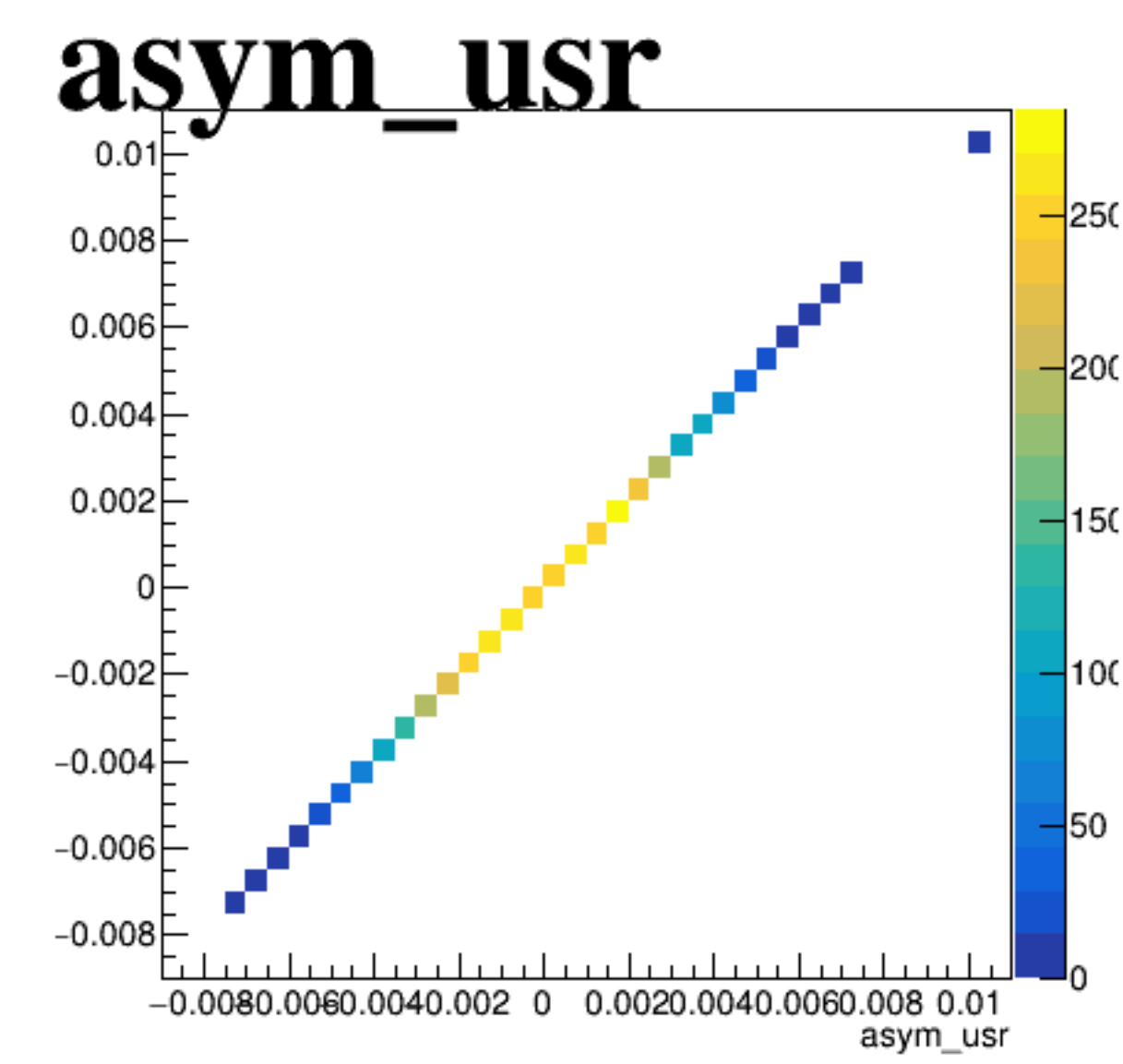
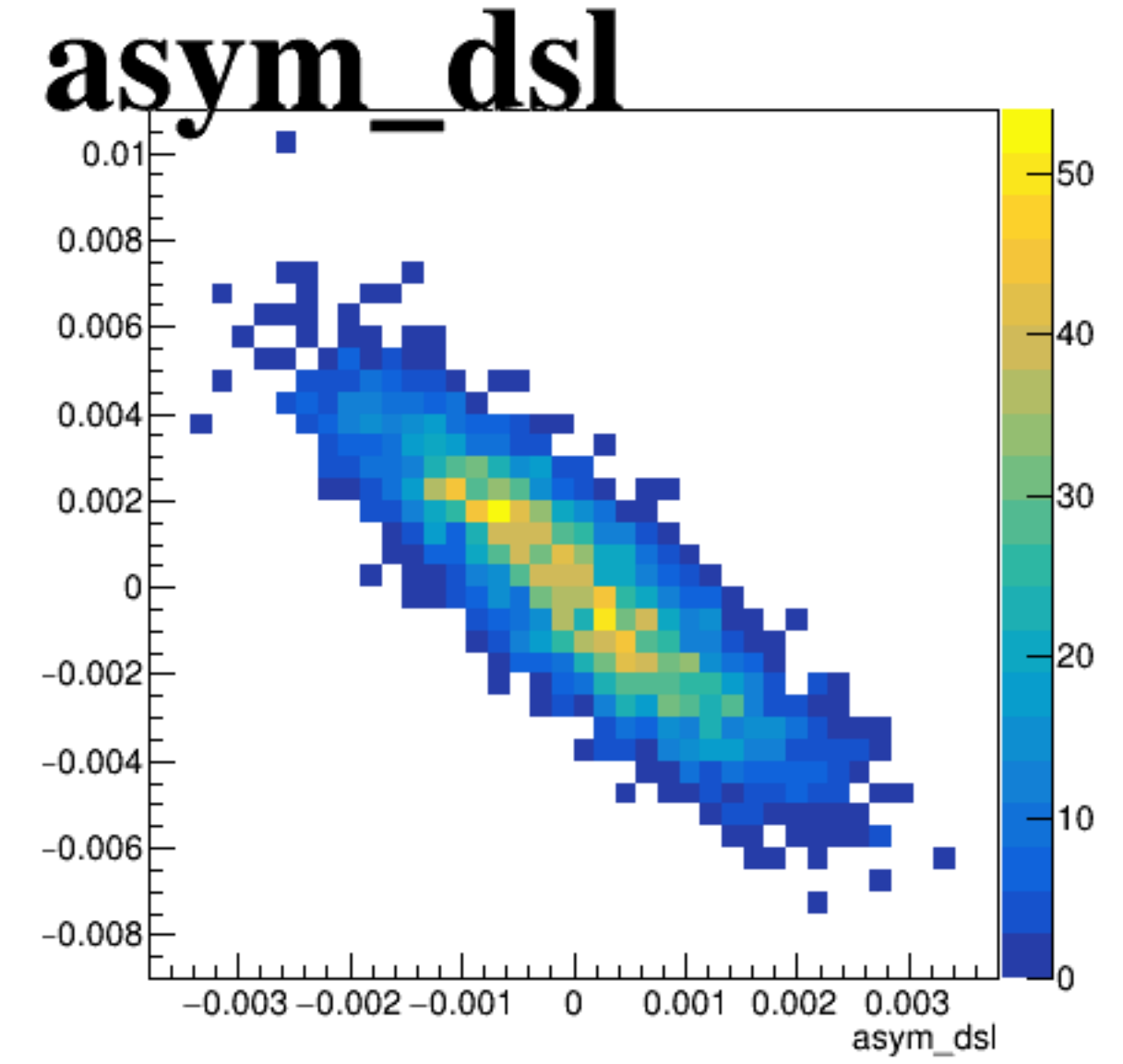
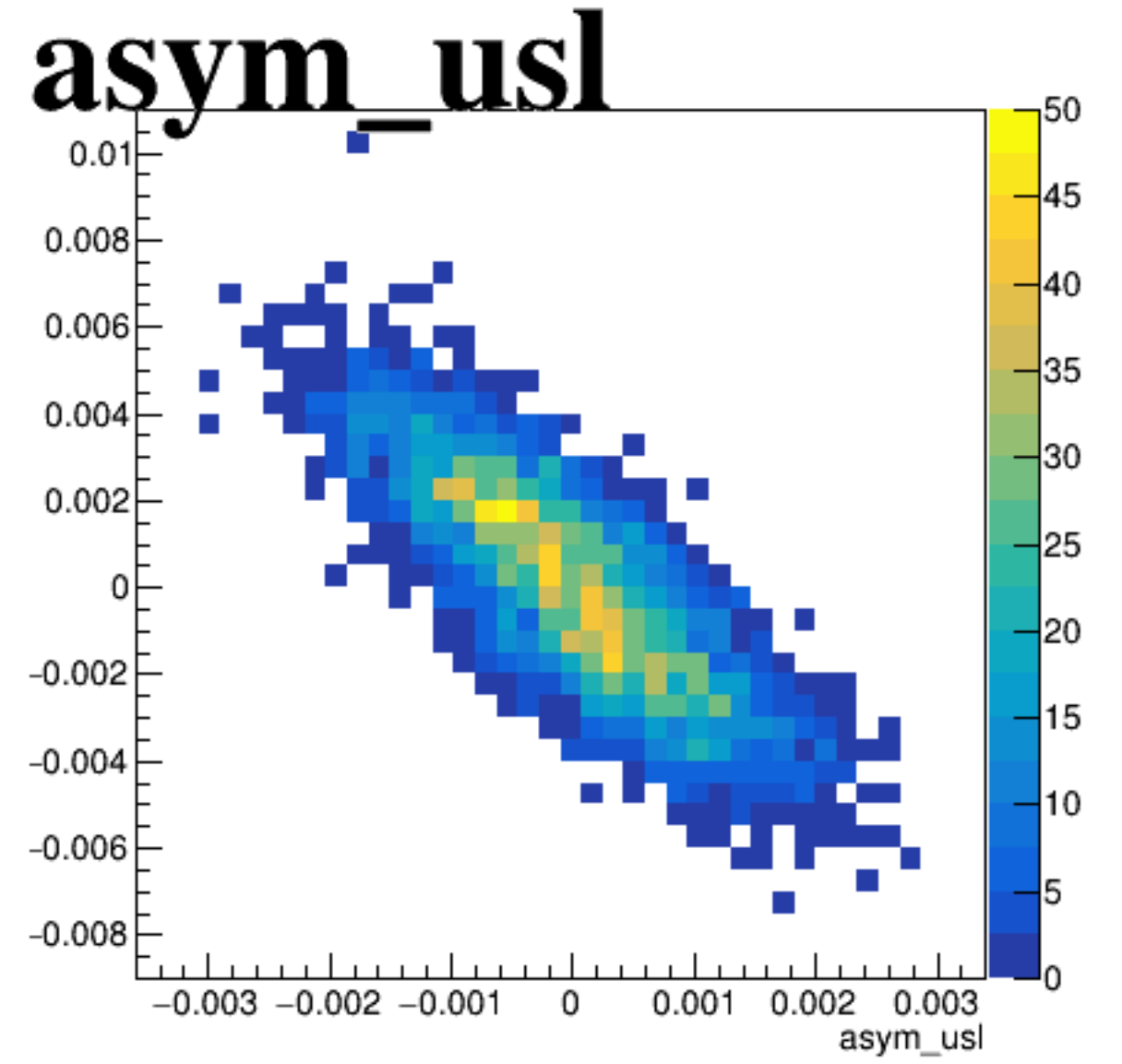
asym_usl



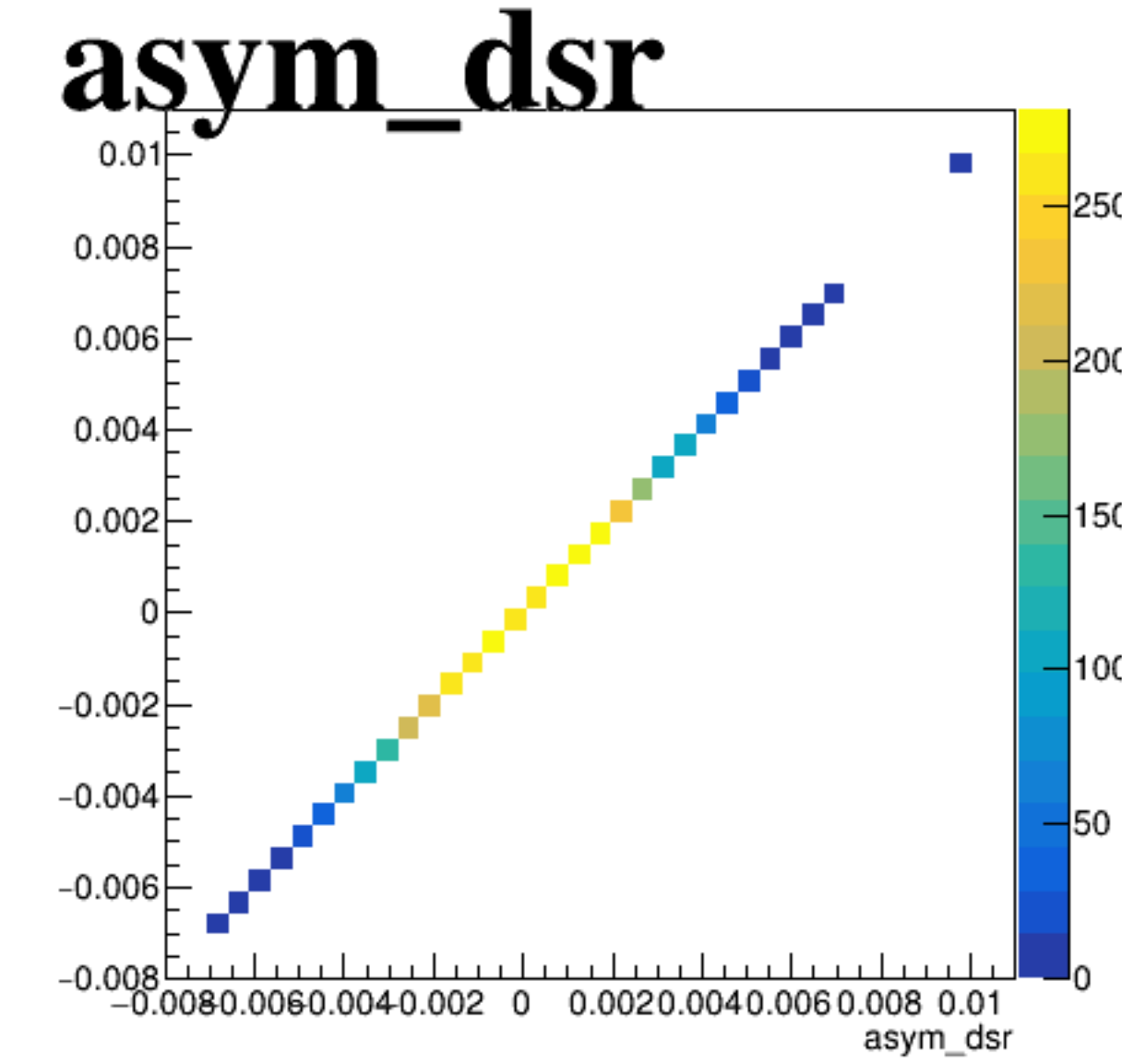
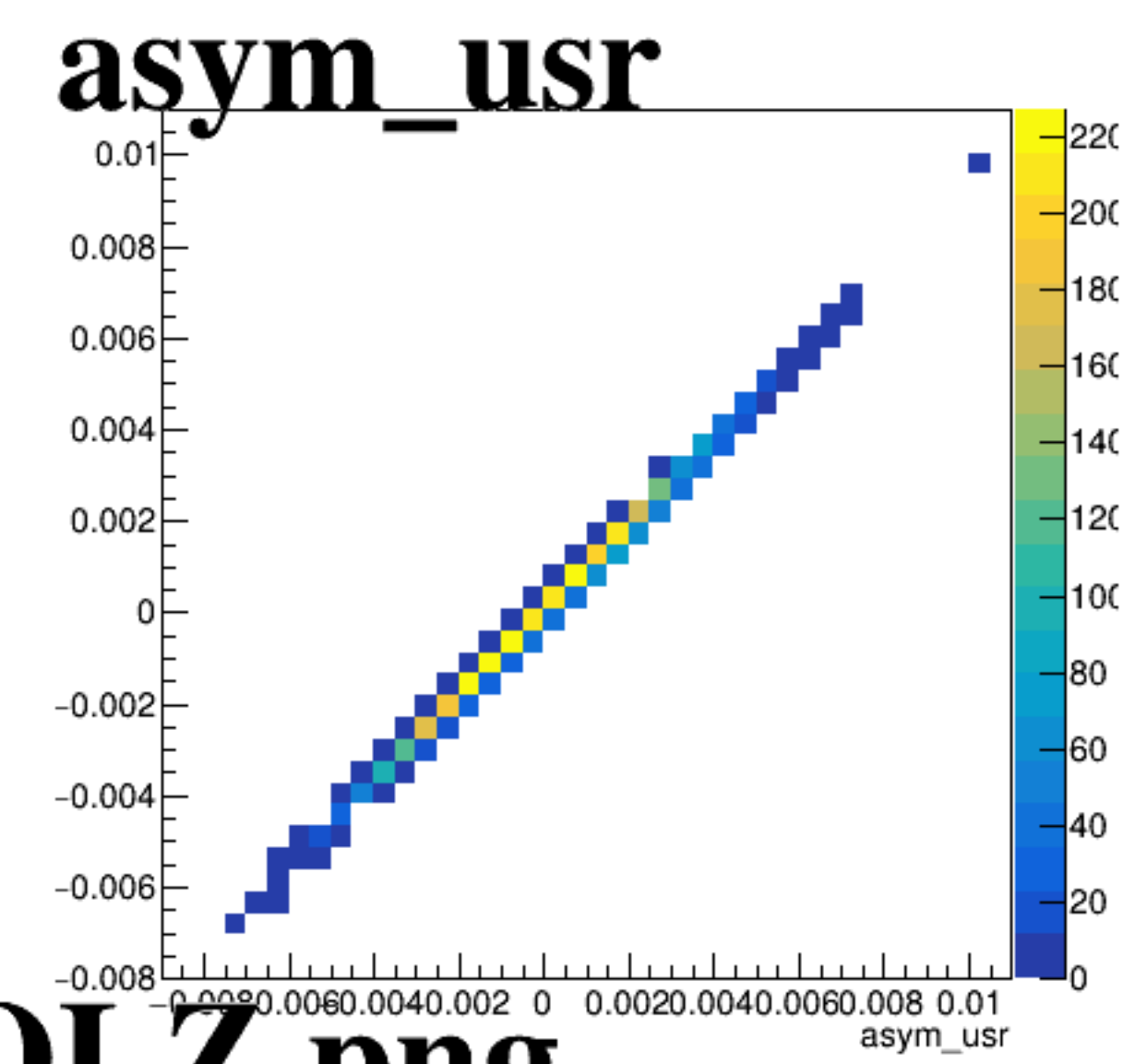
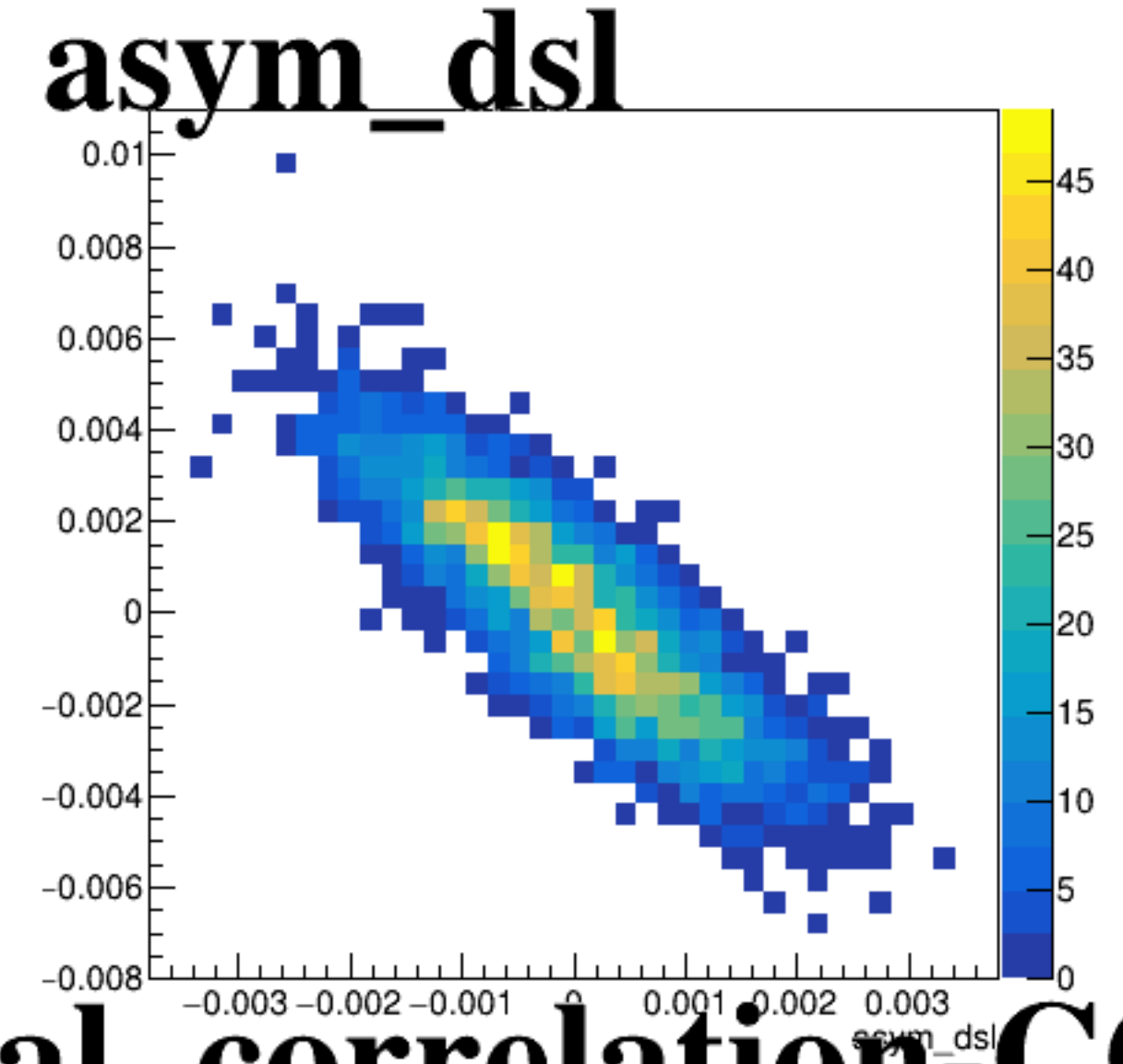
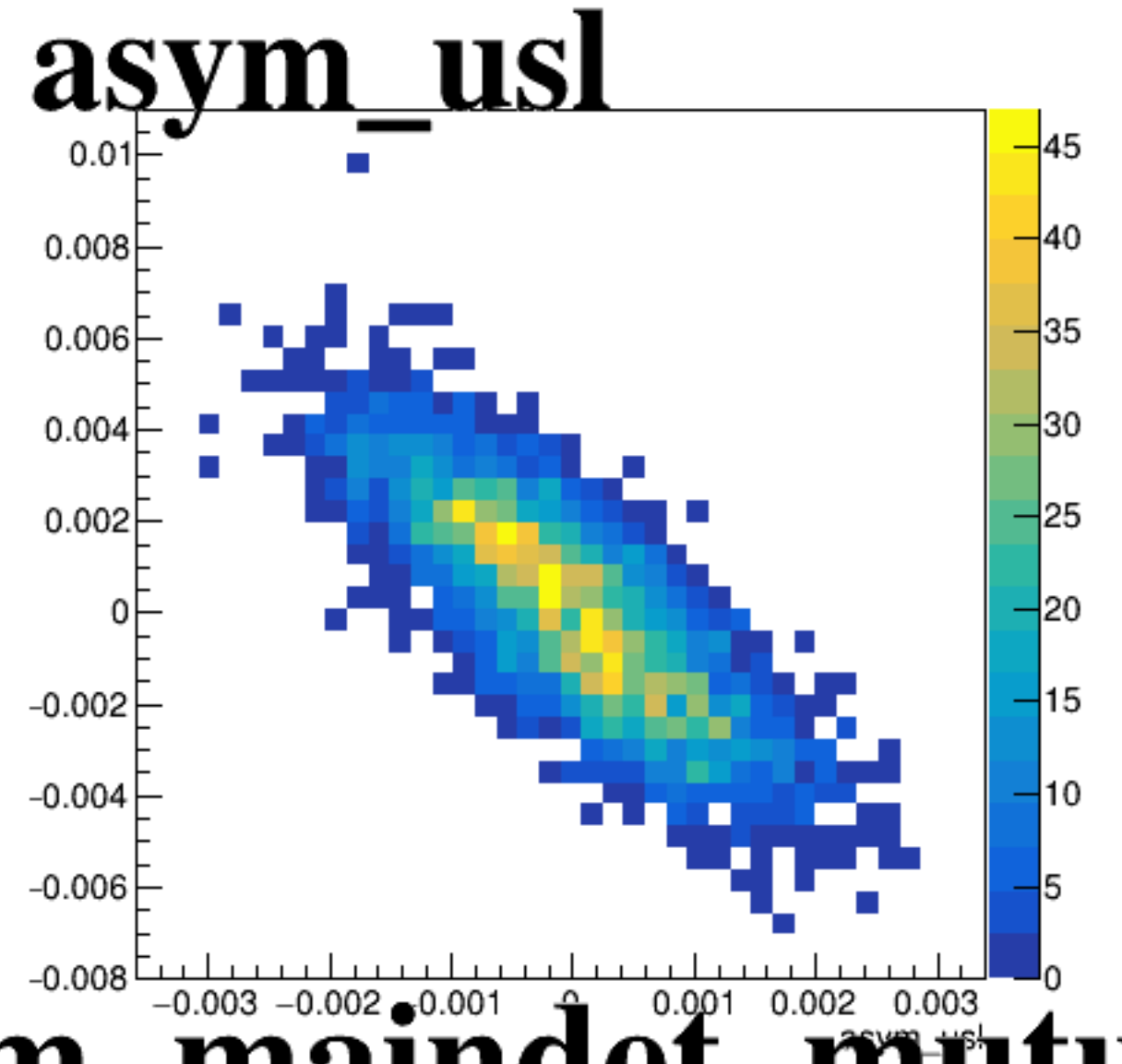
asym_dsl



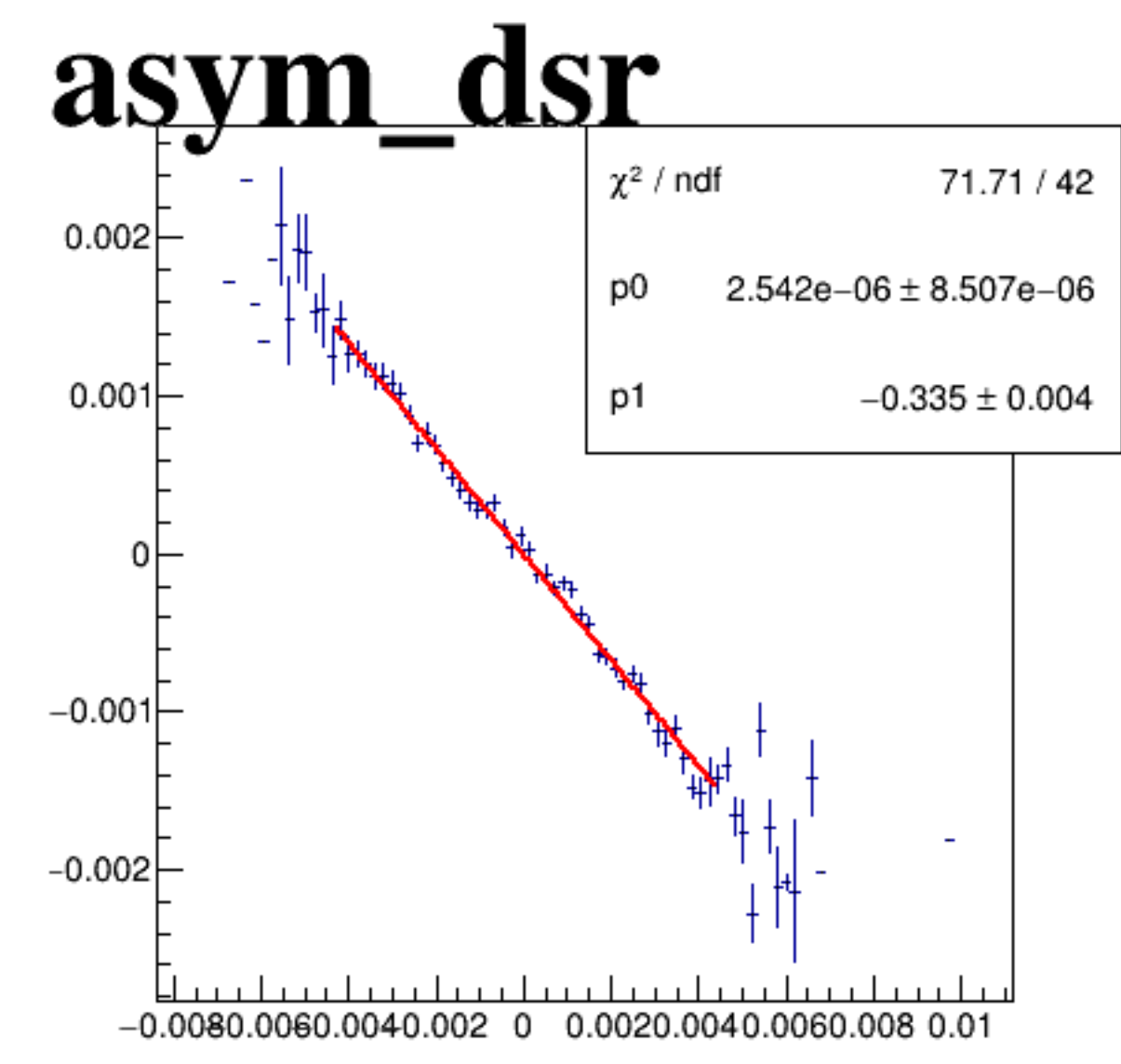
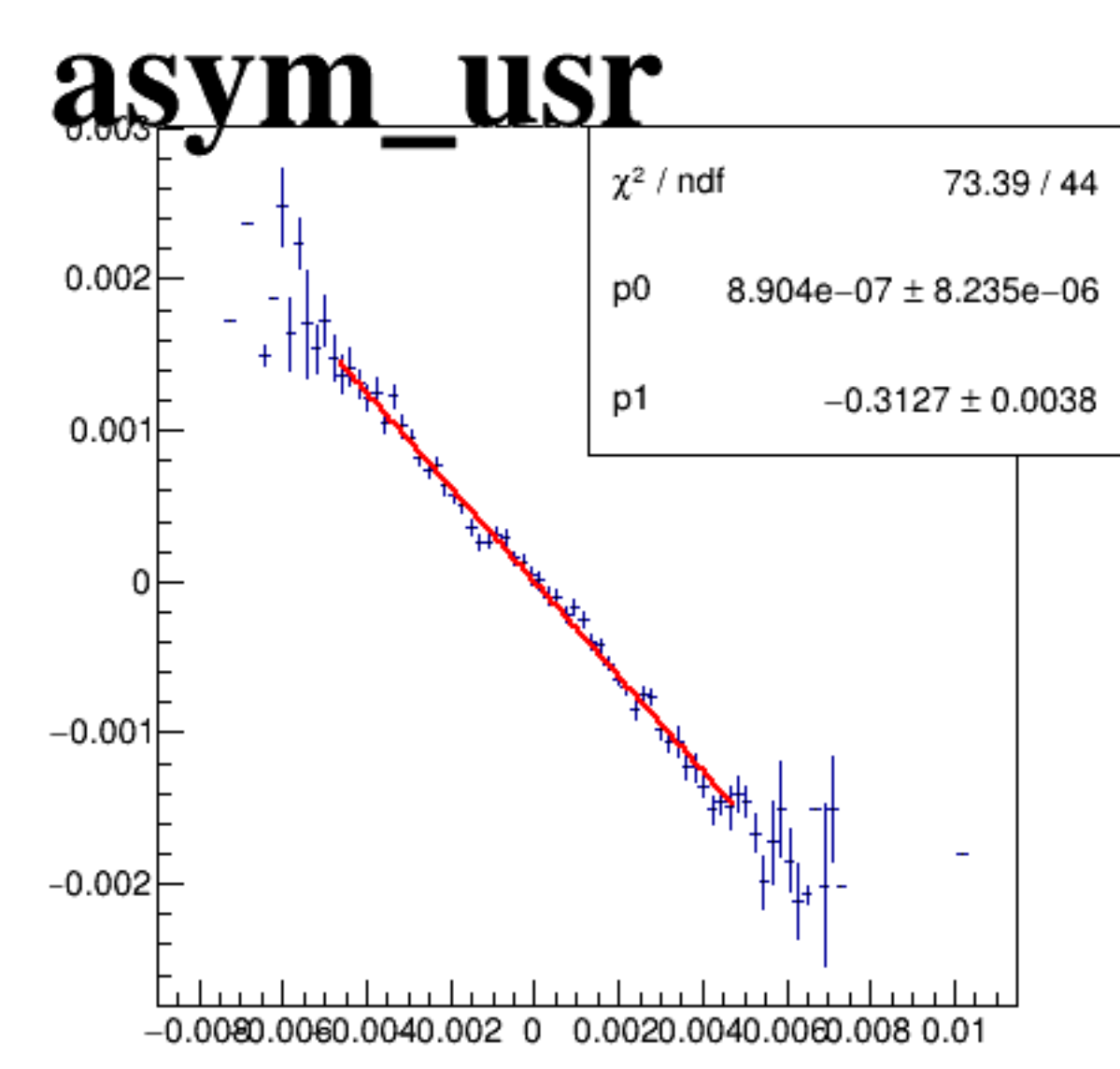
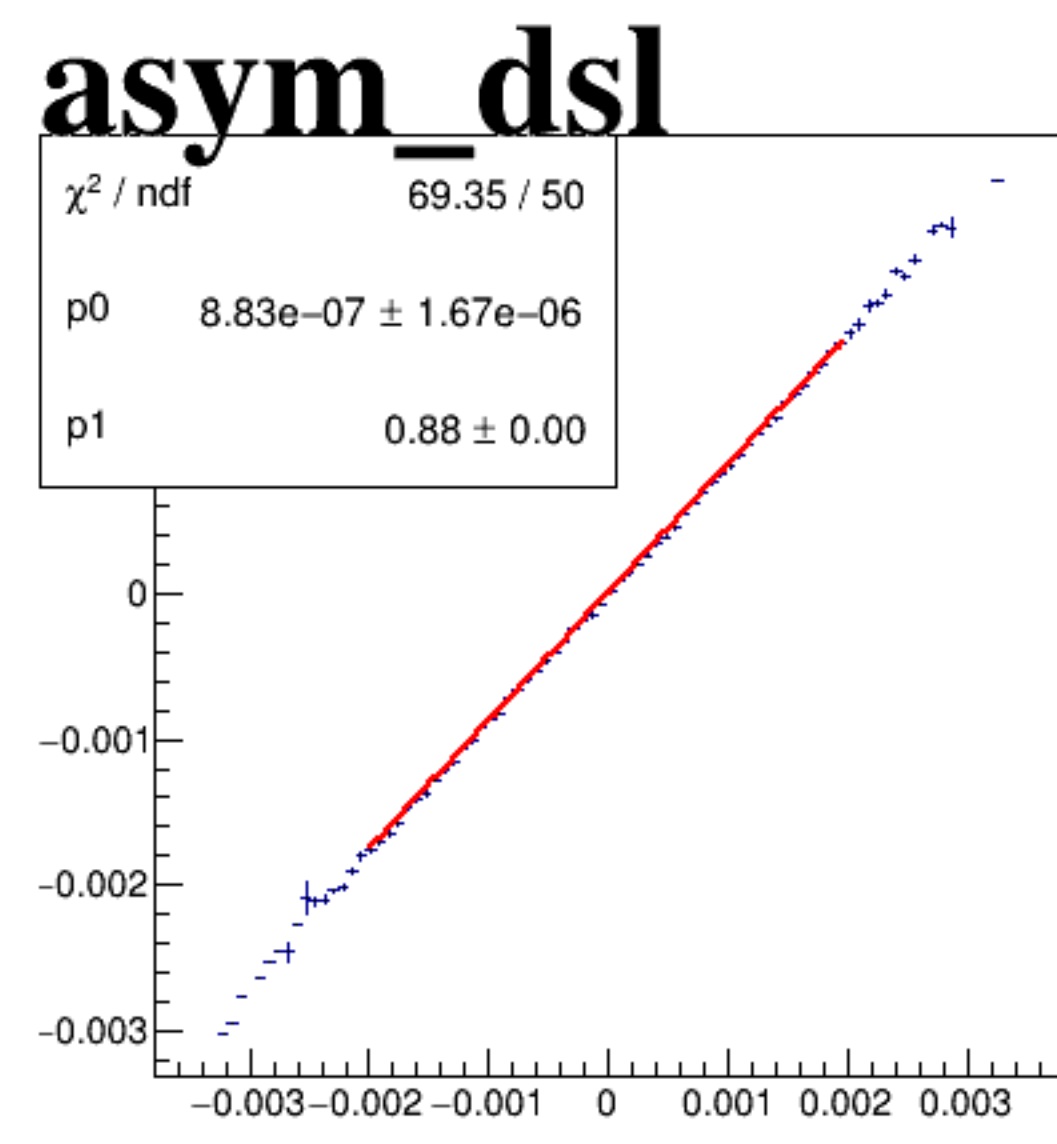
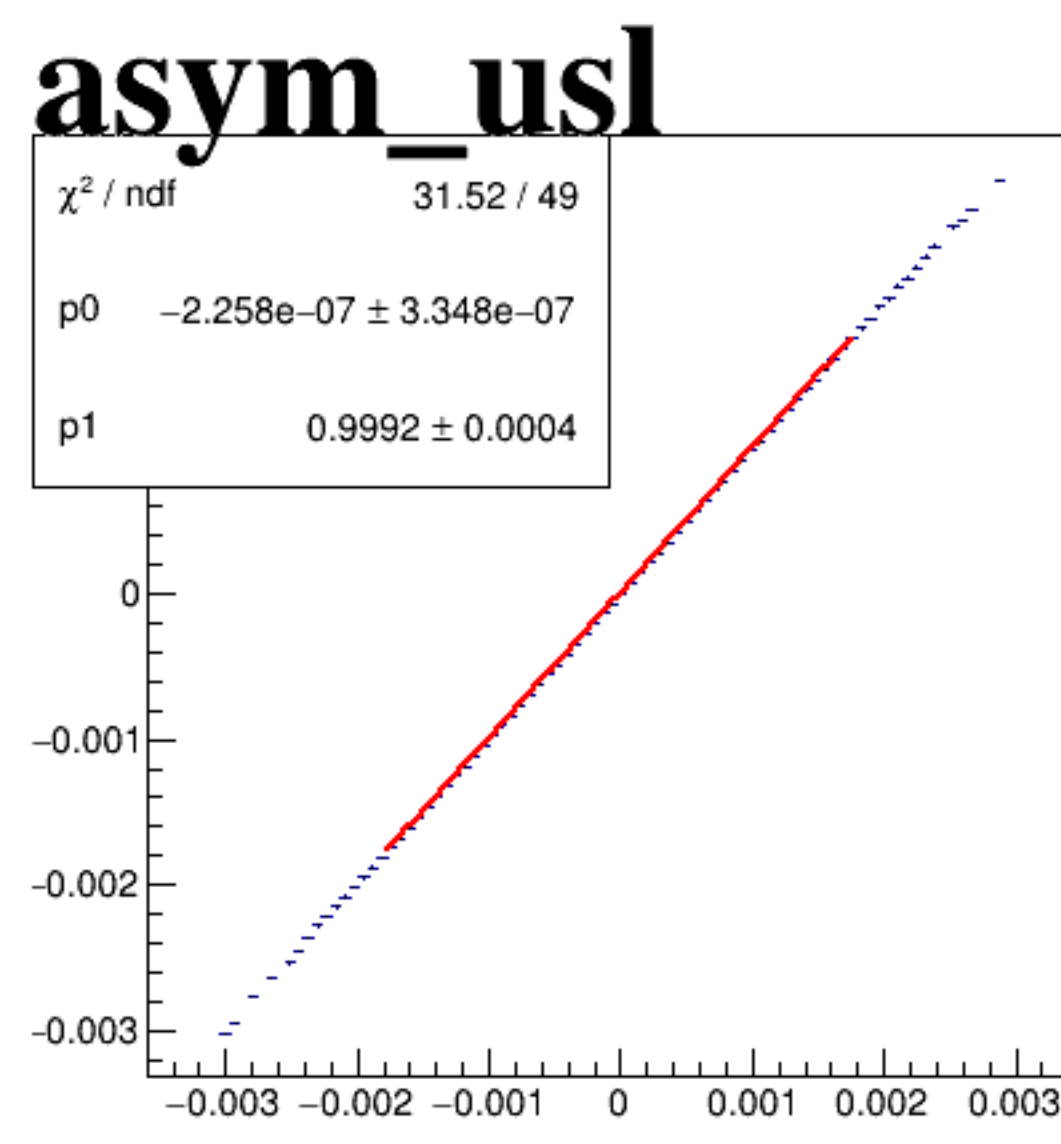
asym_usr



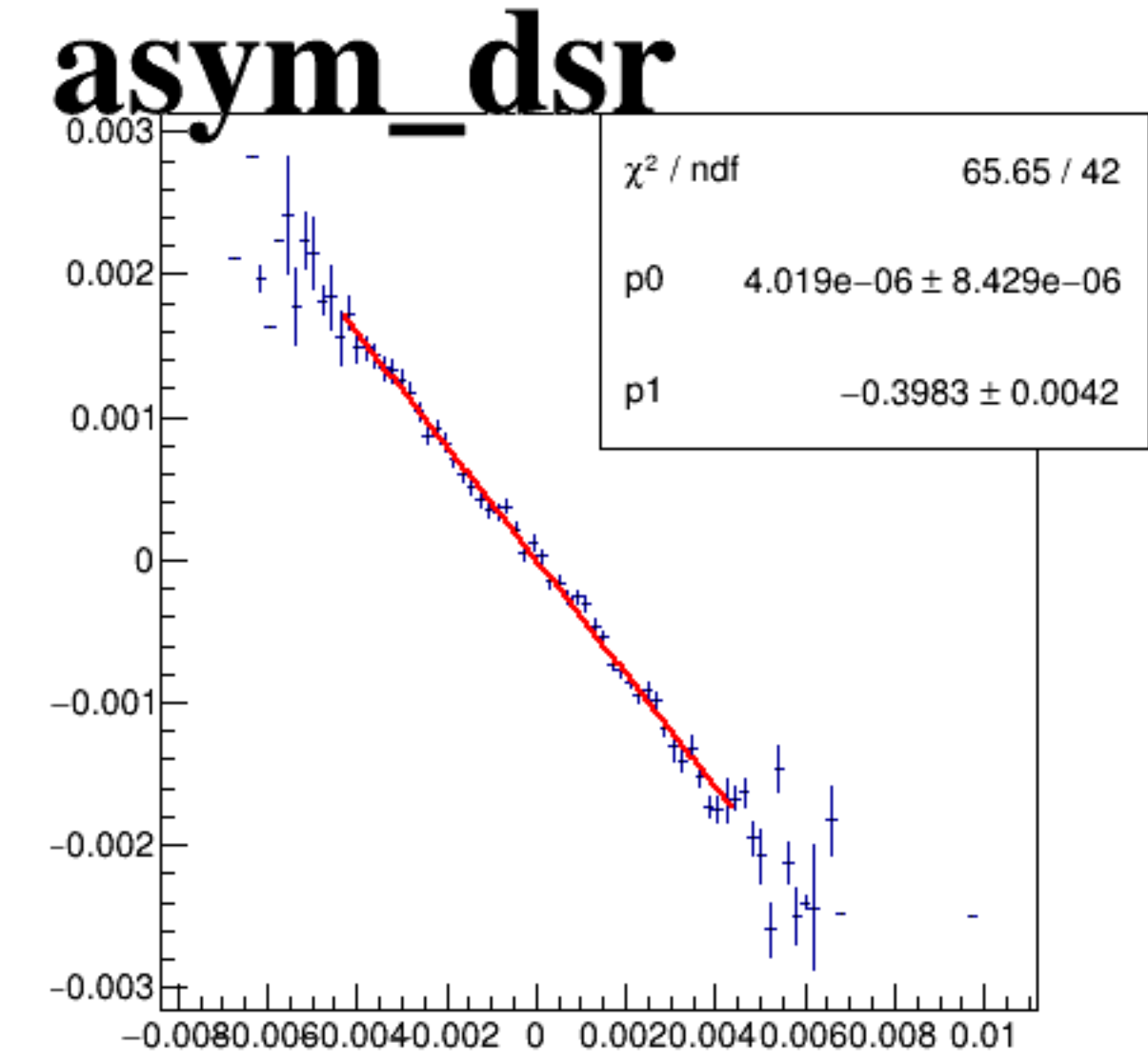
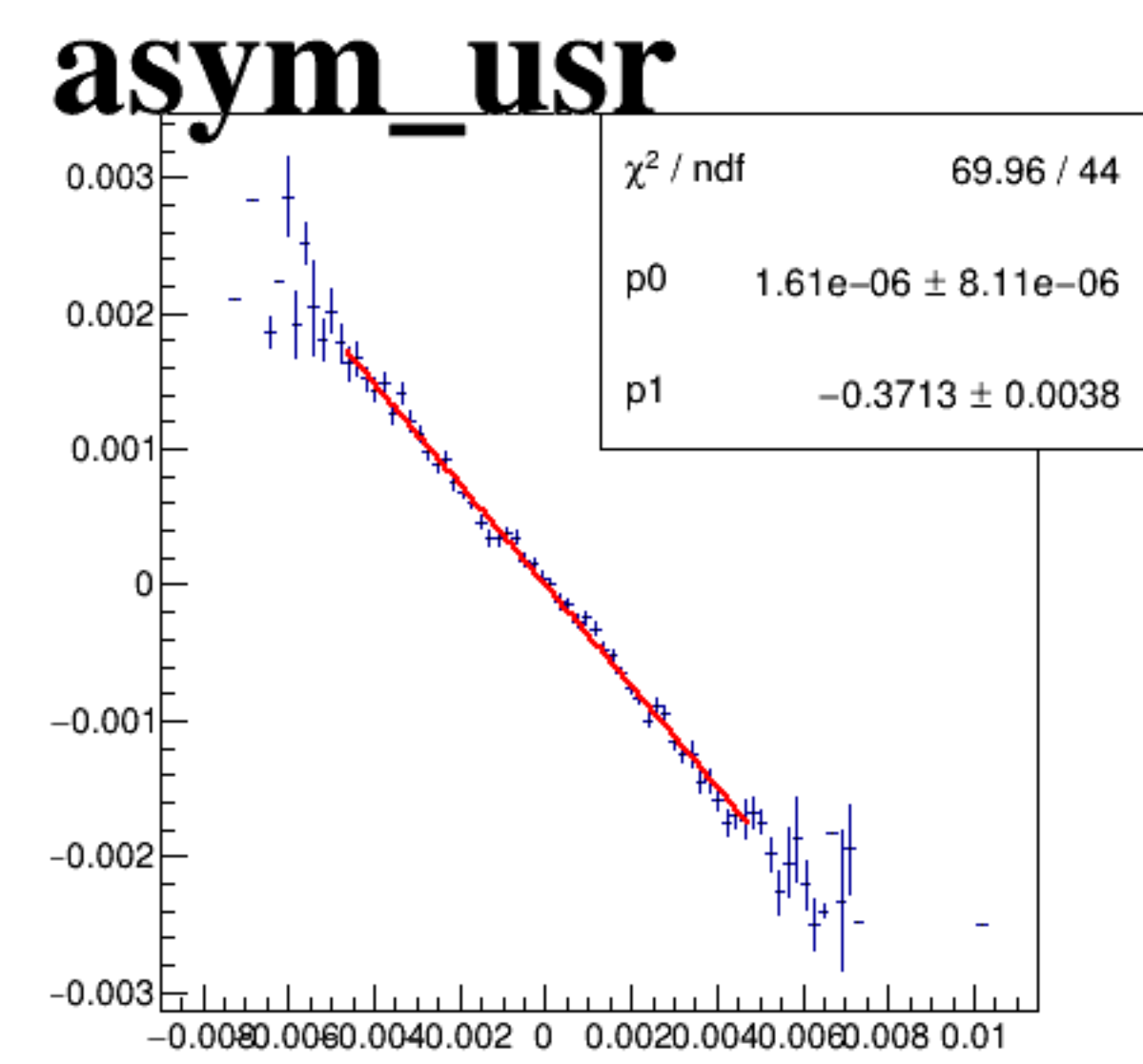
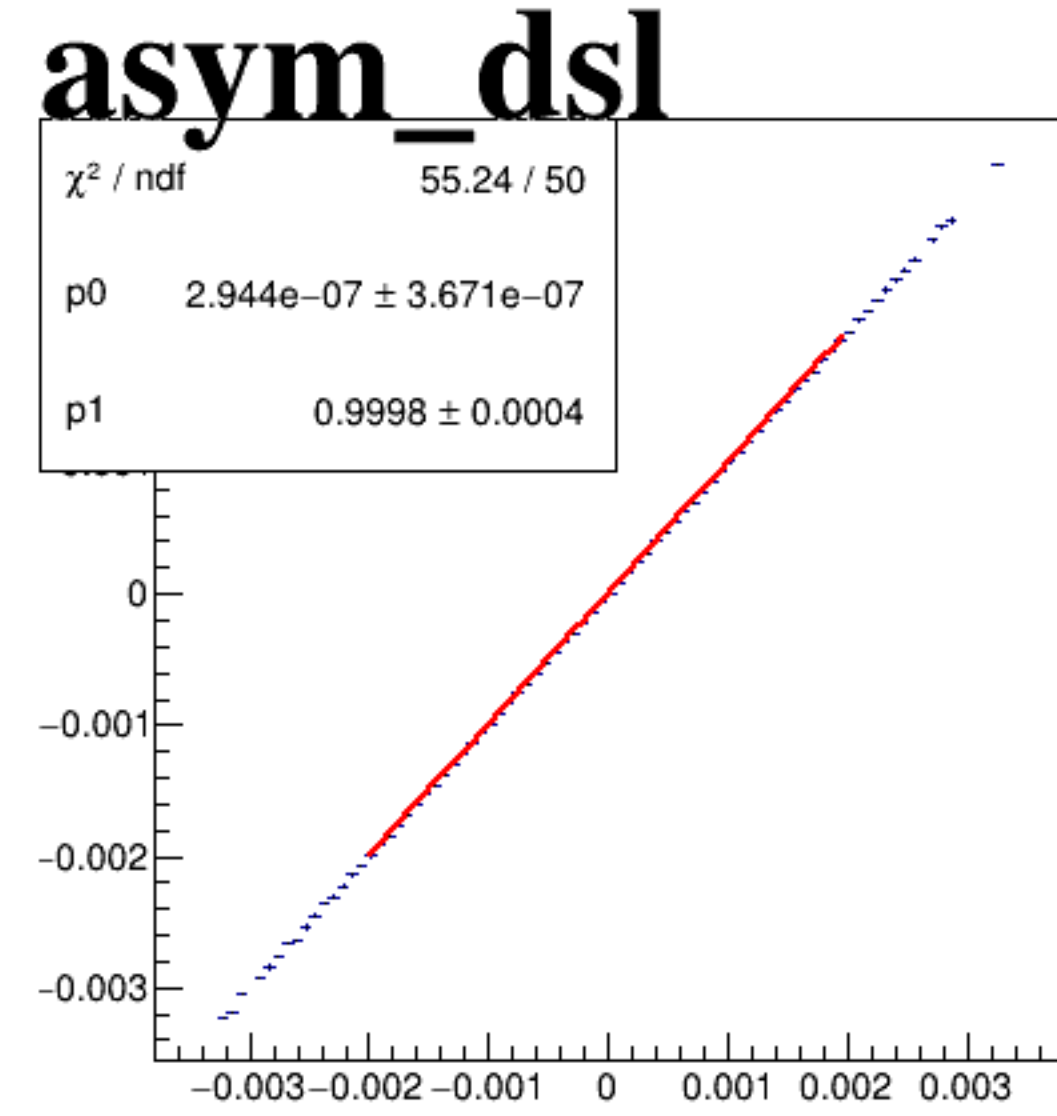
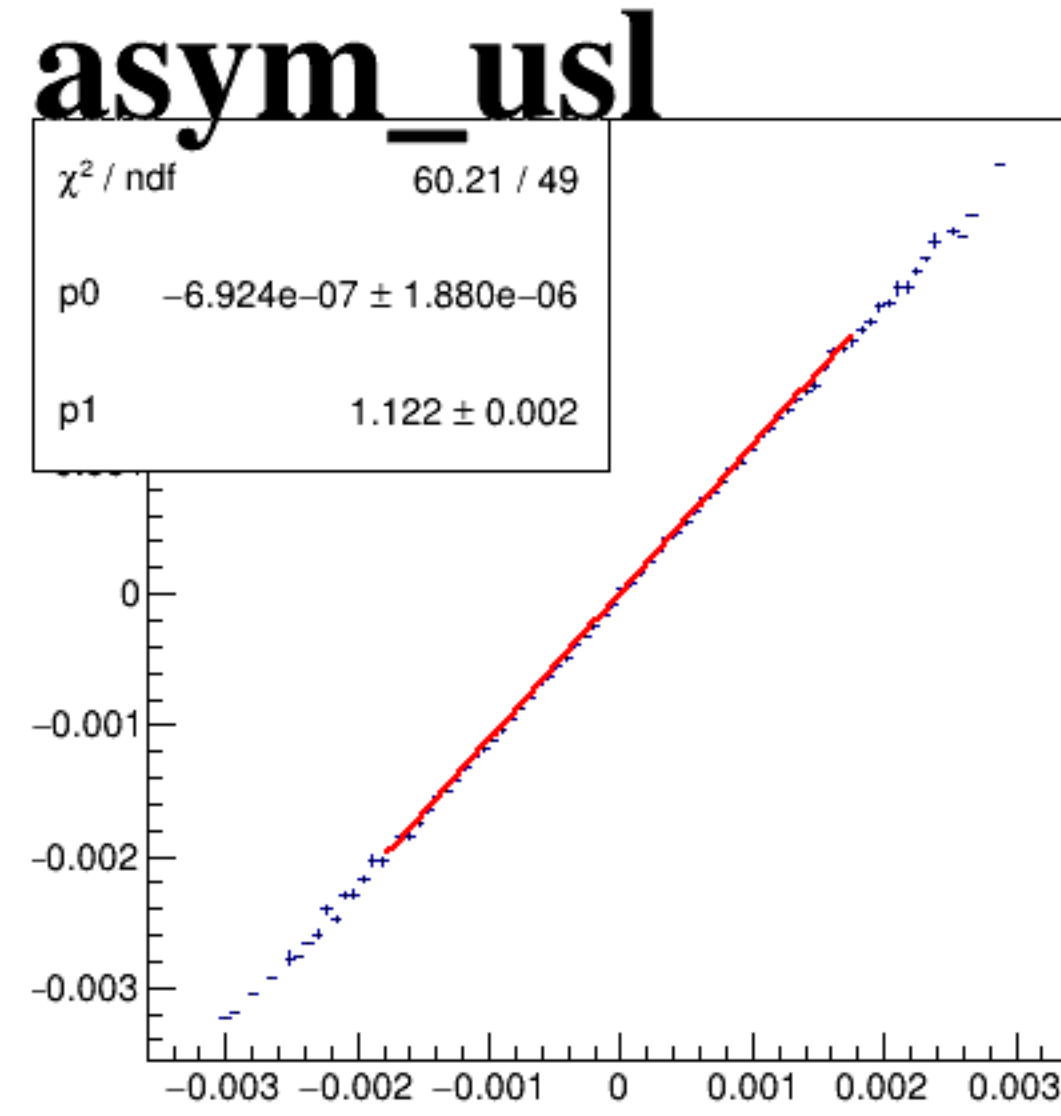
asym_dsr



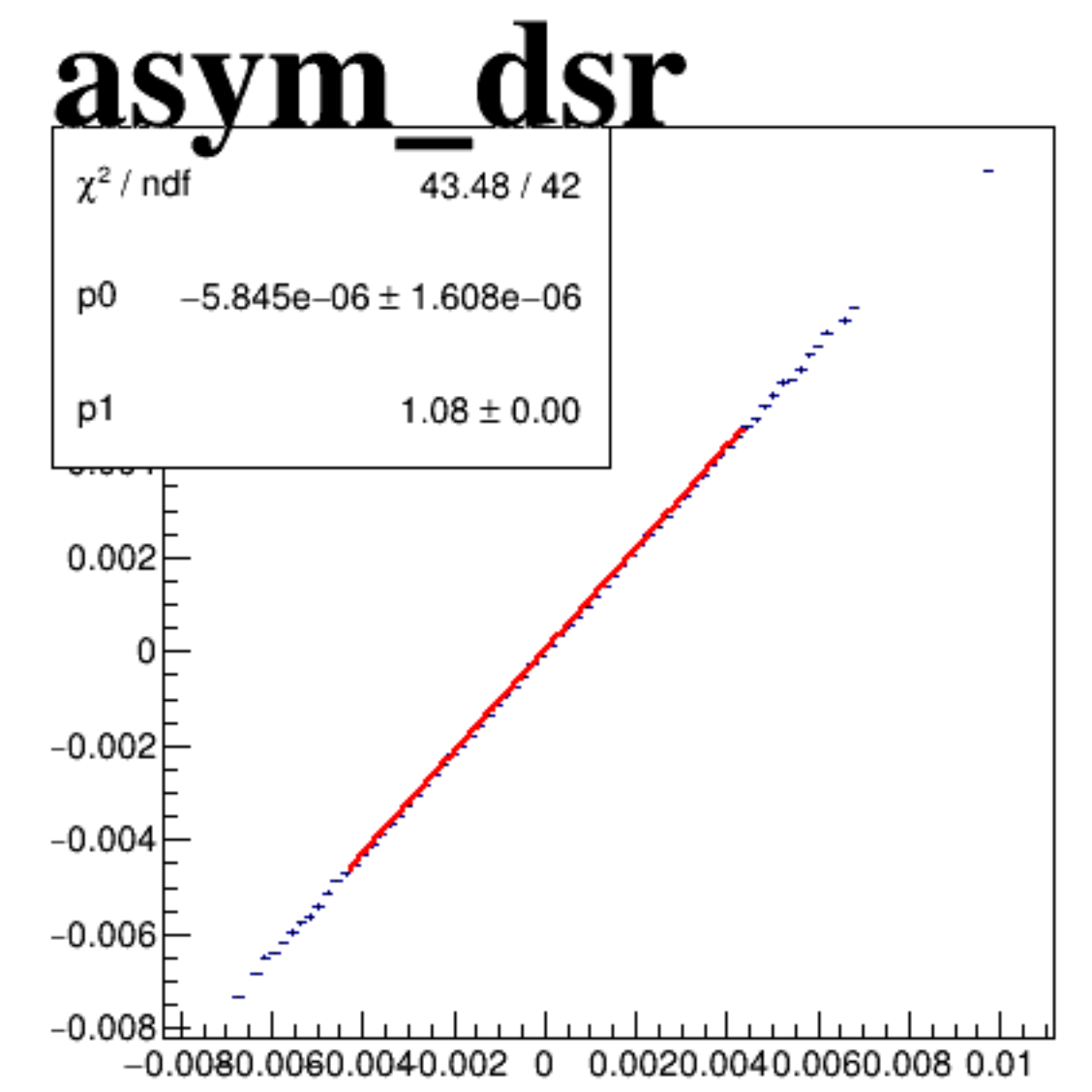
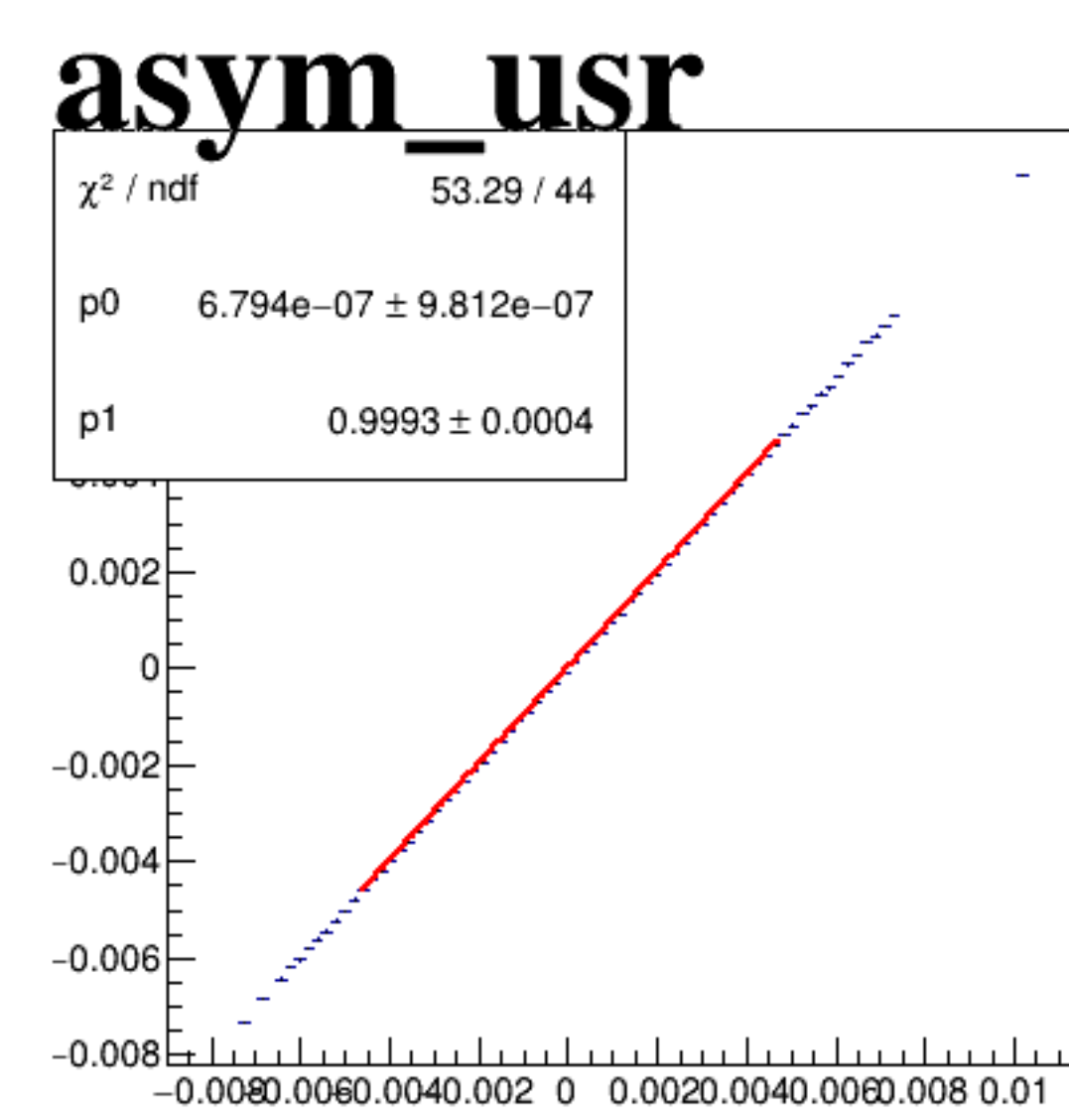
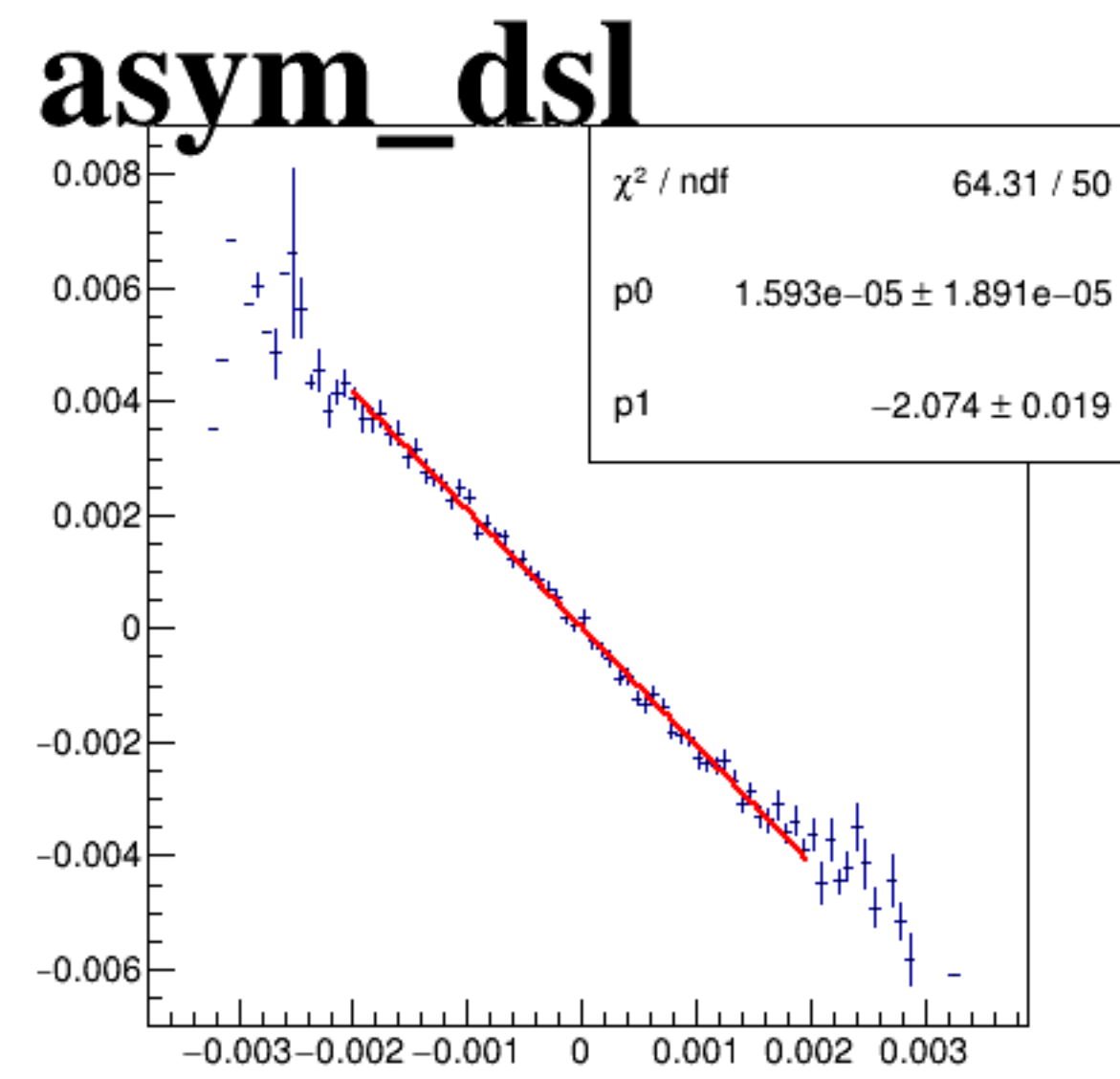
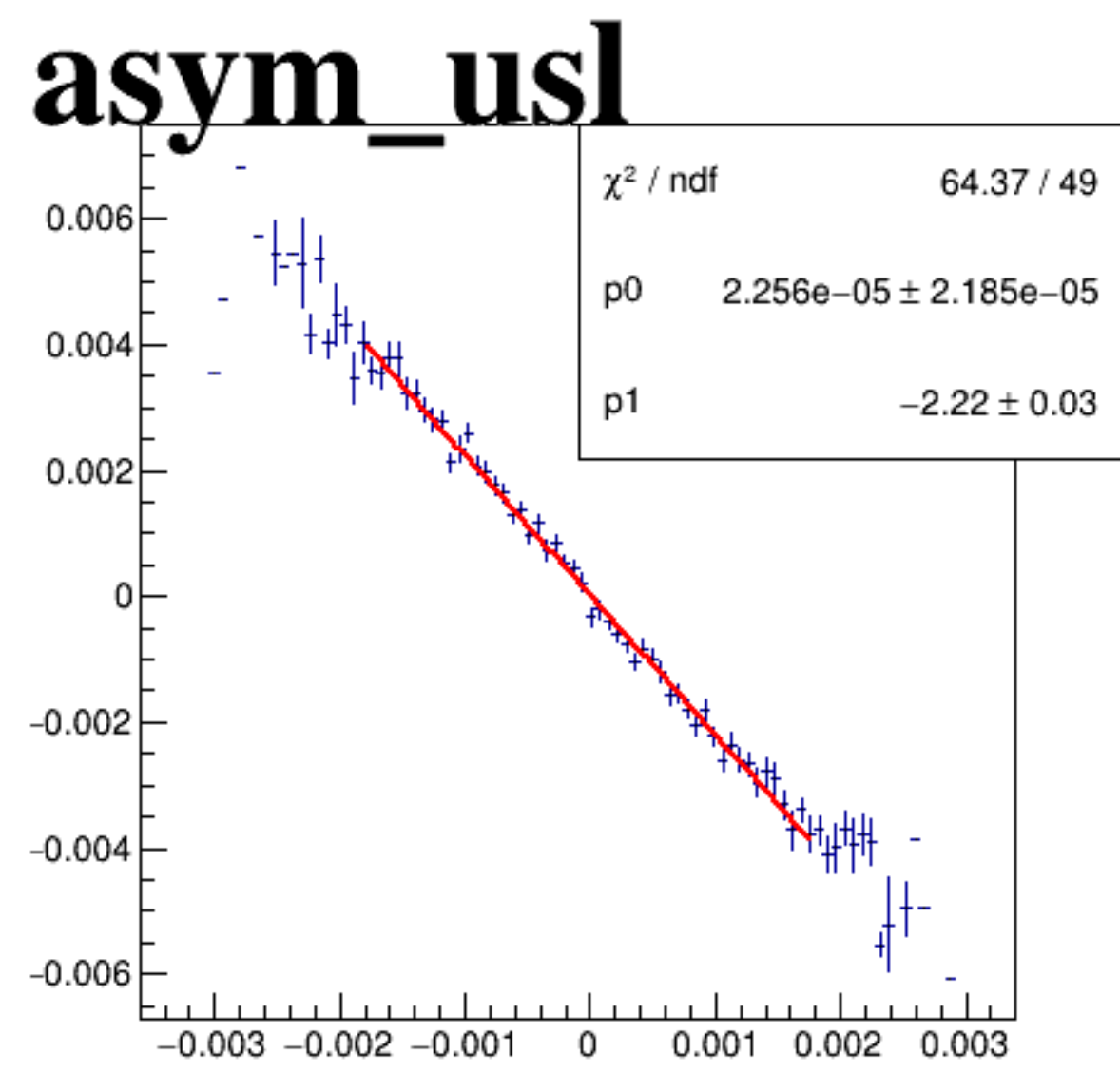
asym_usl



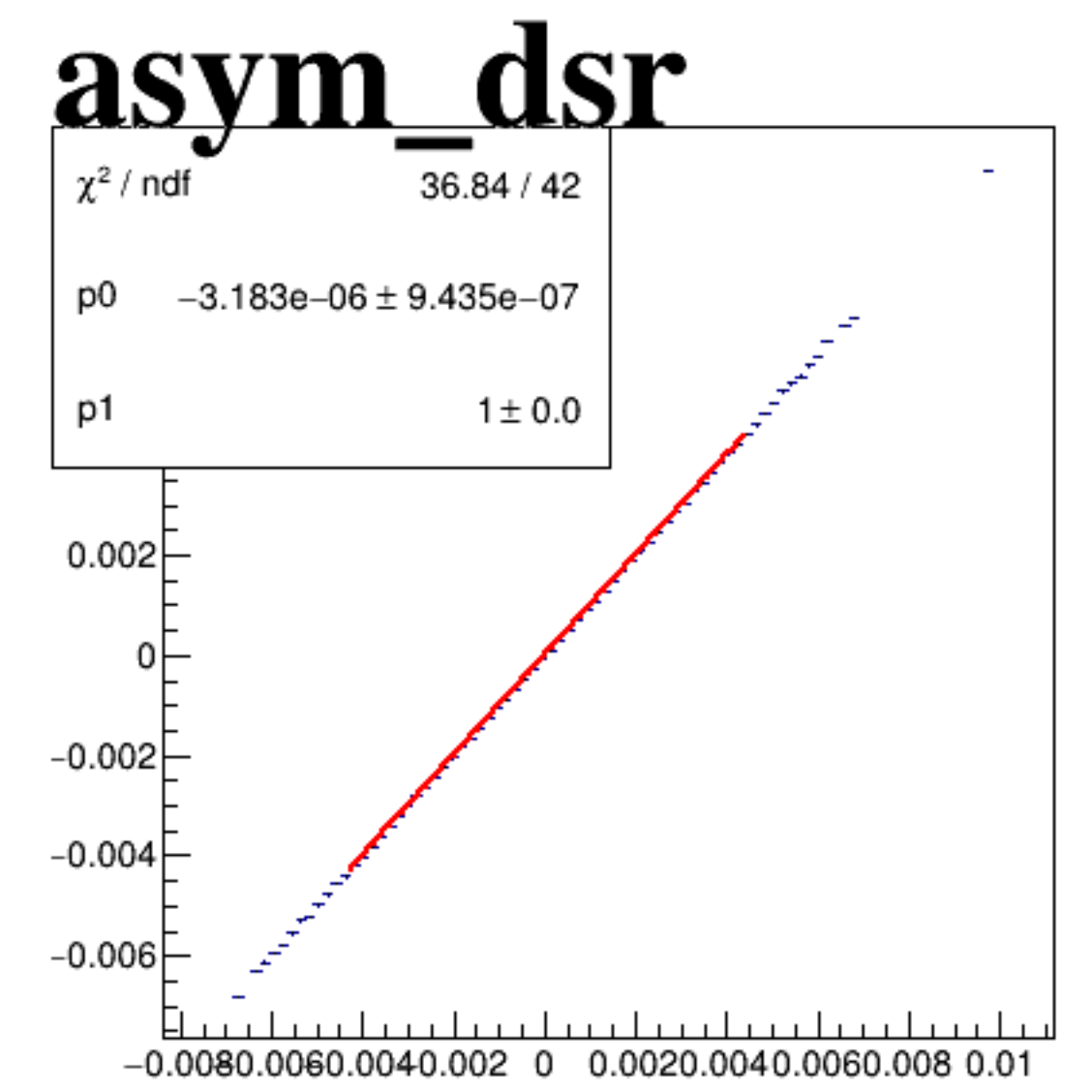
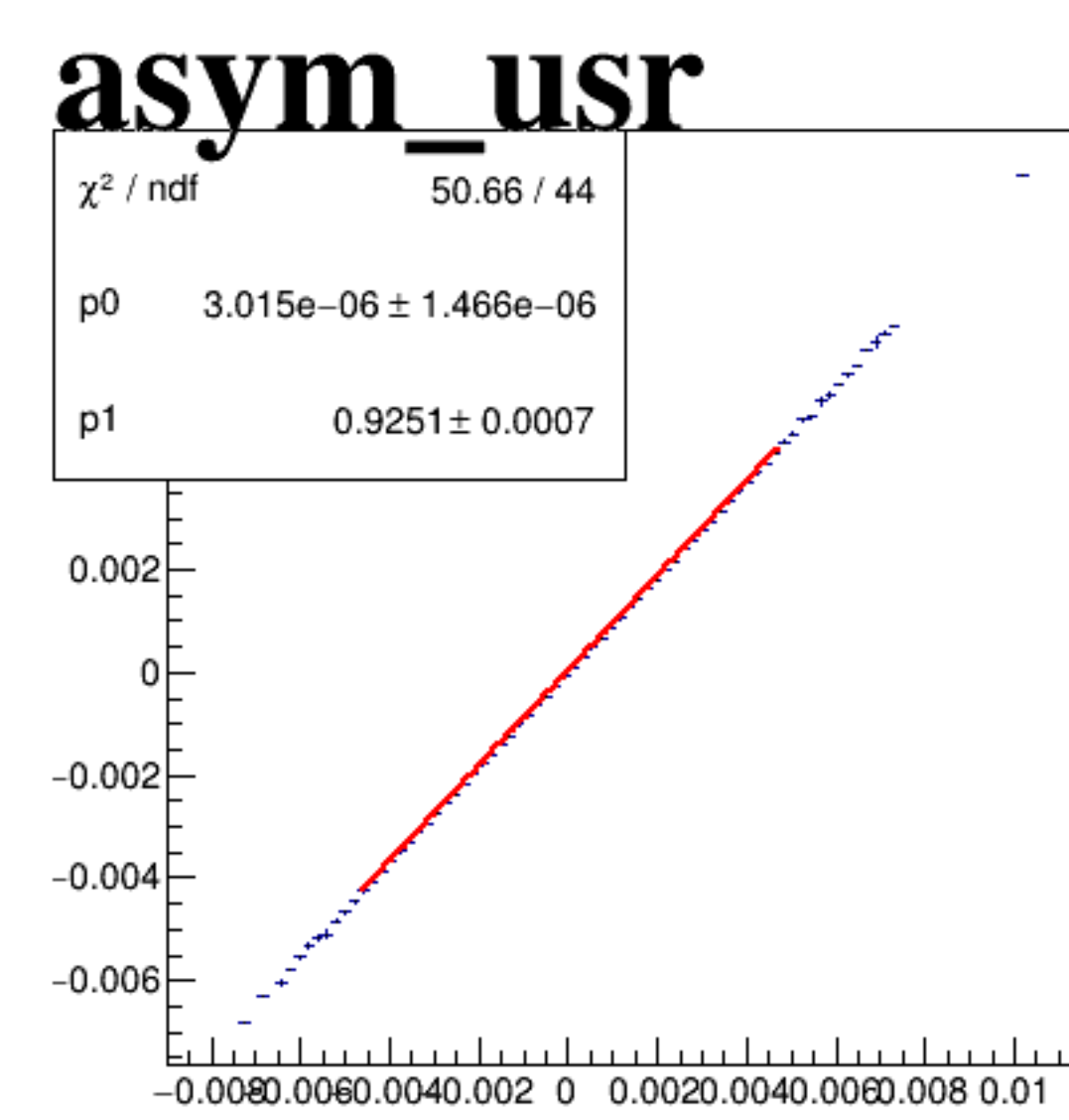
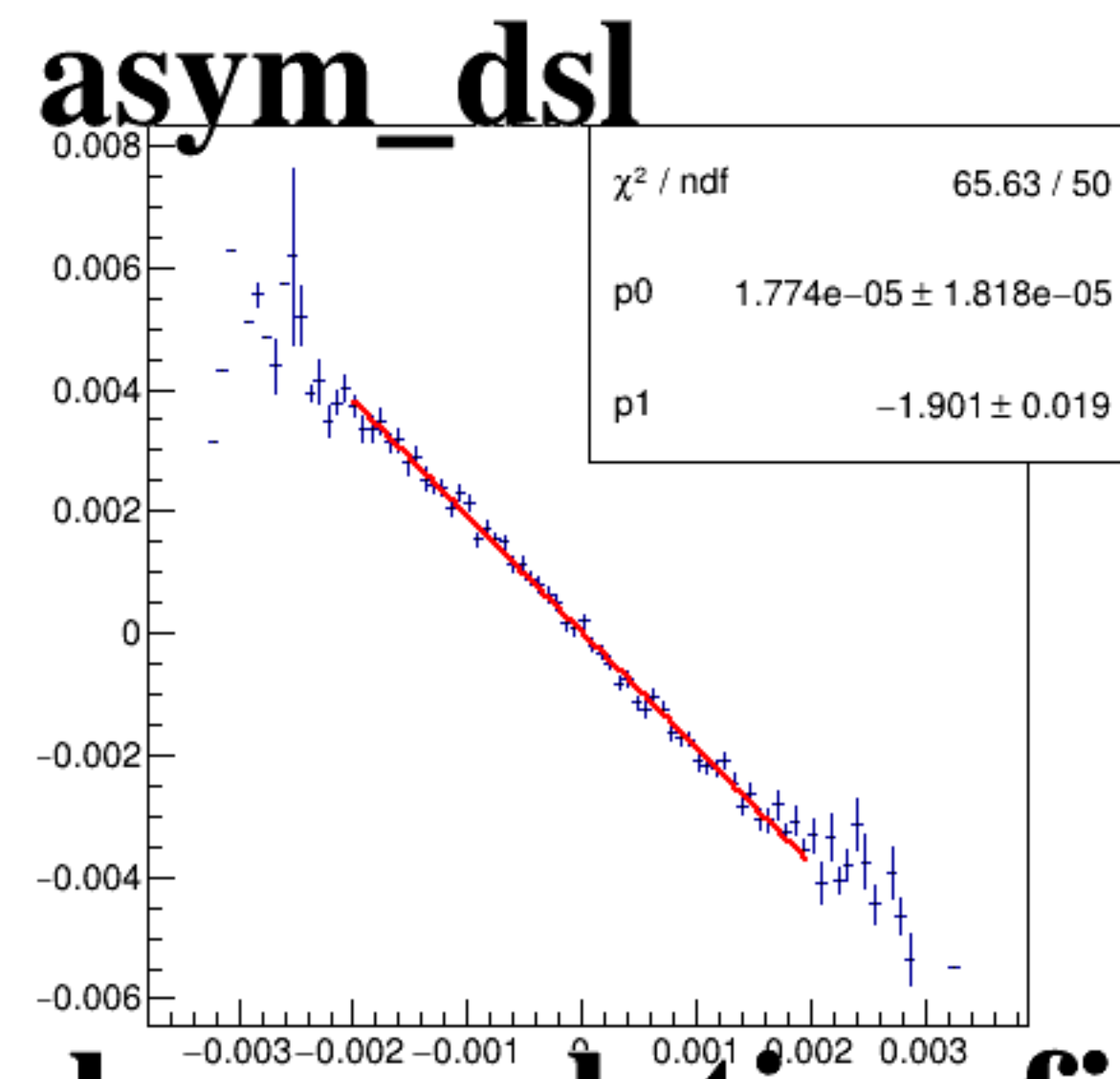
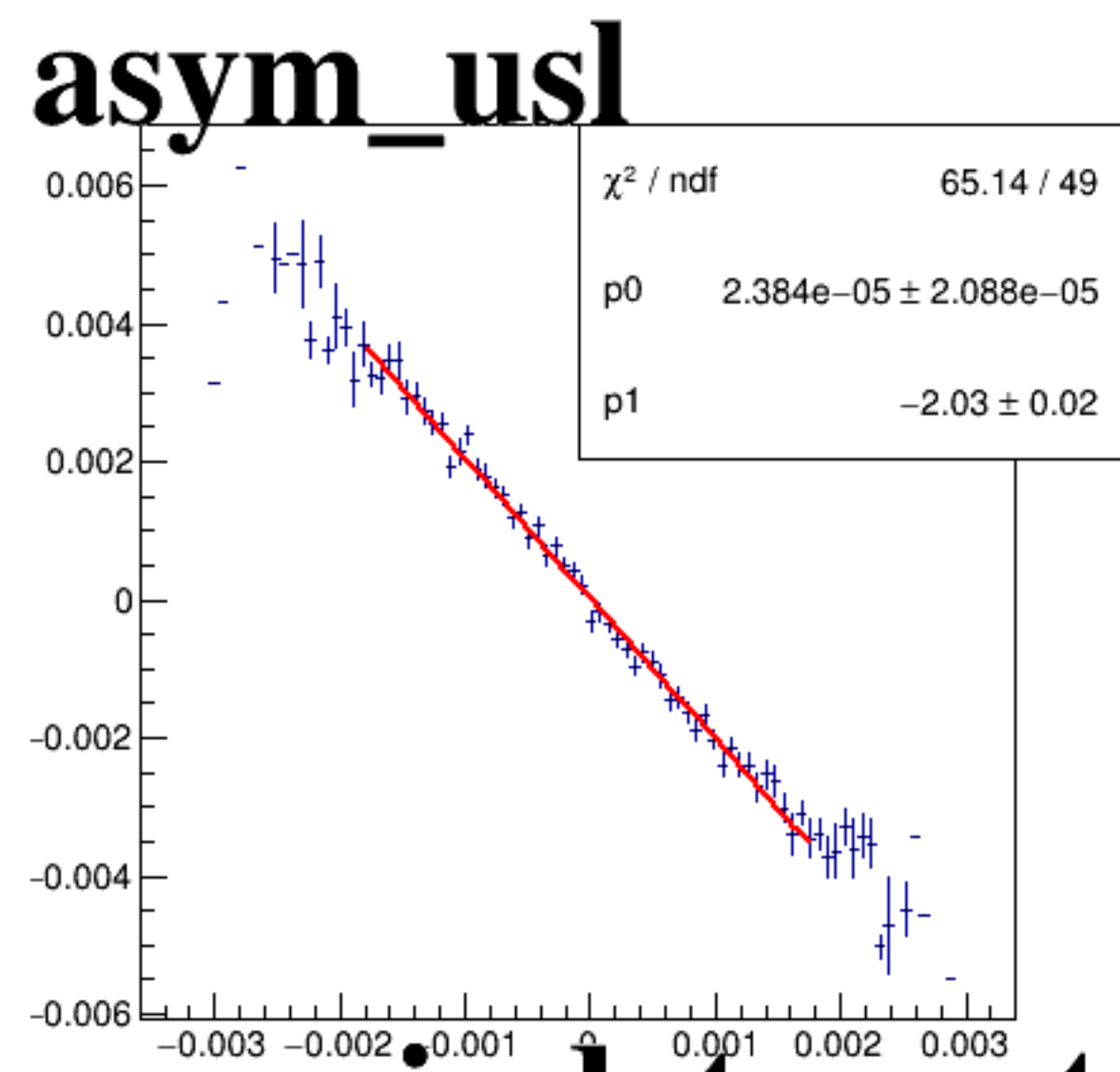
asym_dsl



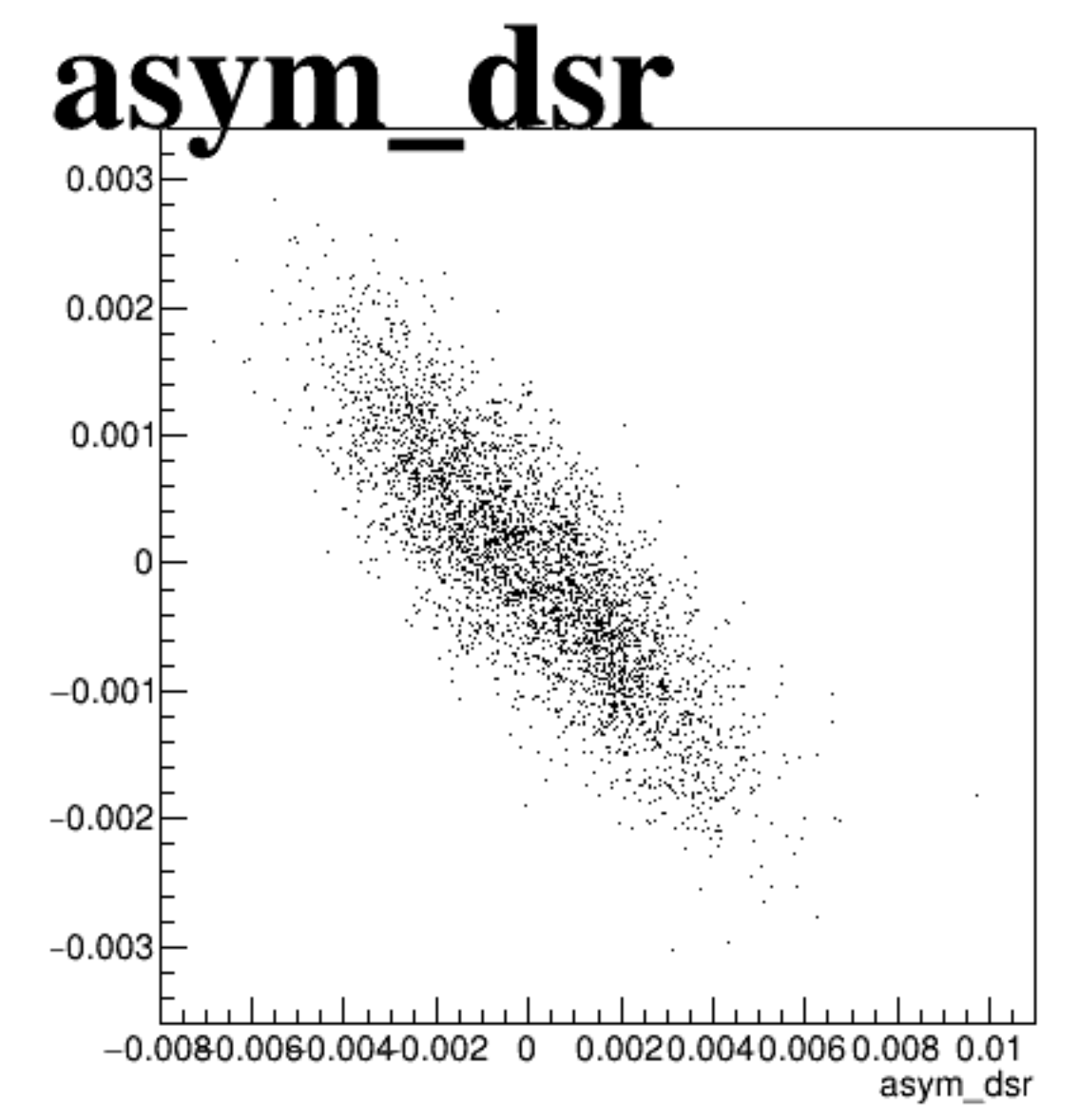
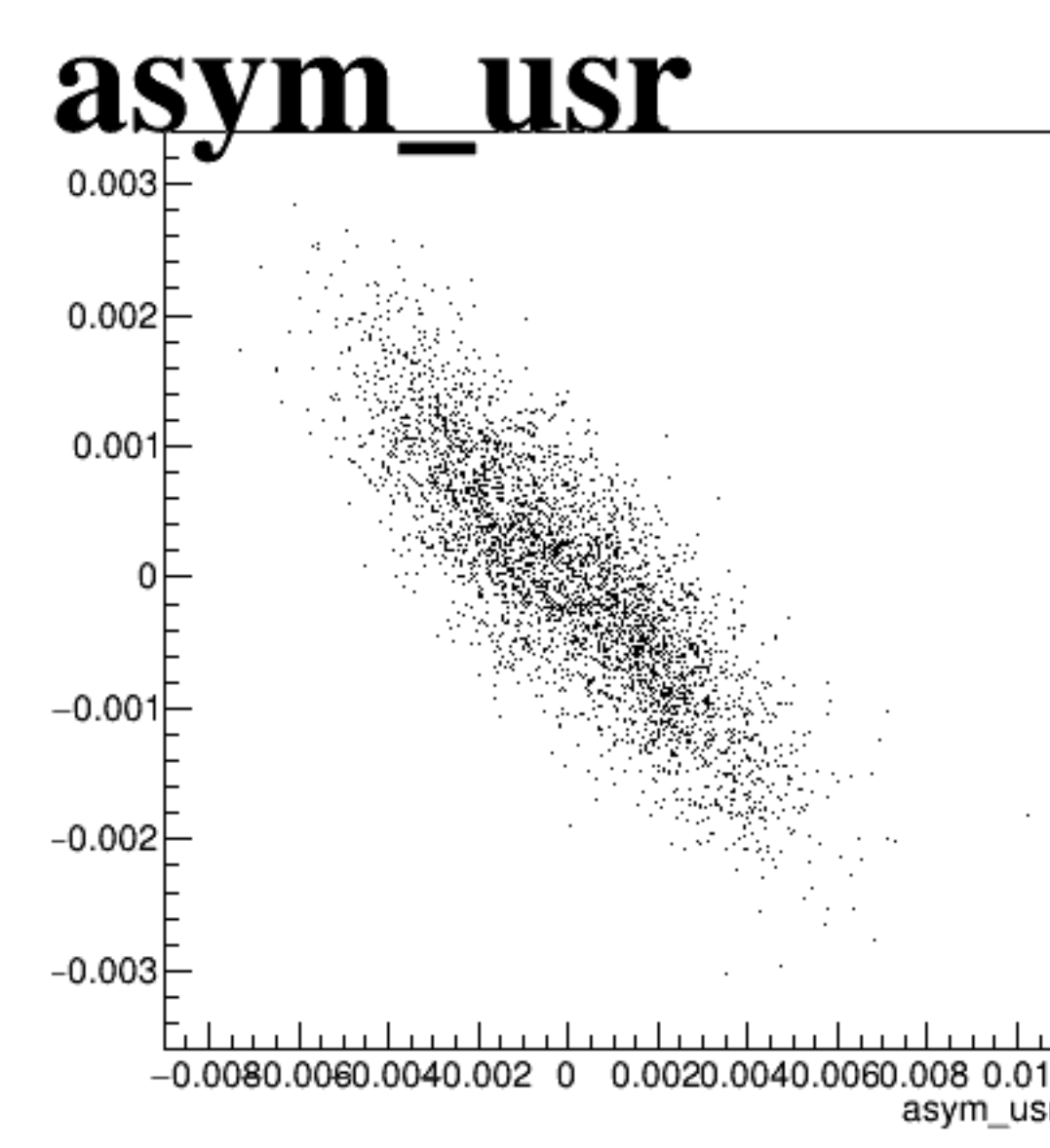
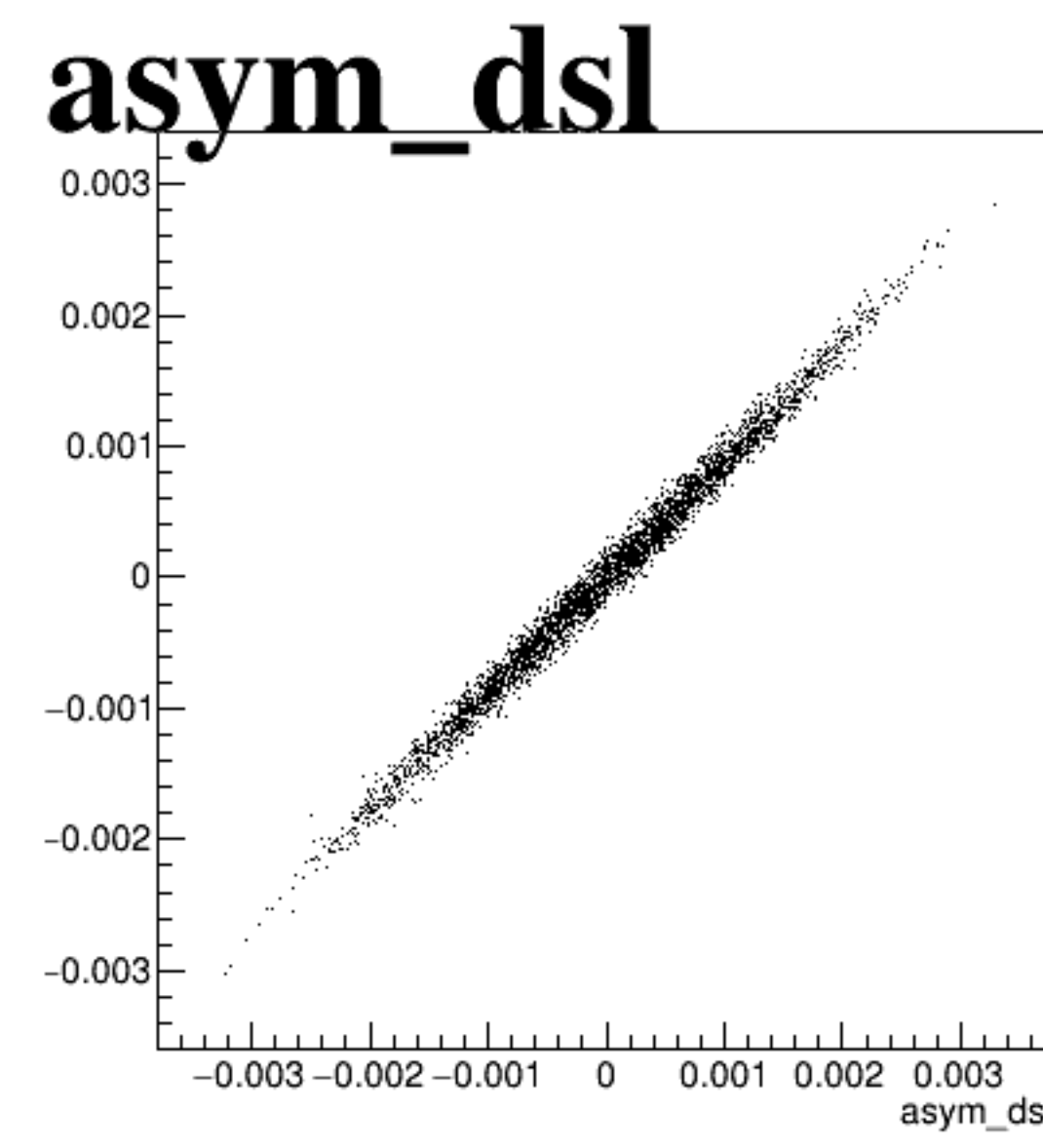
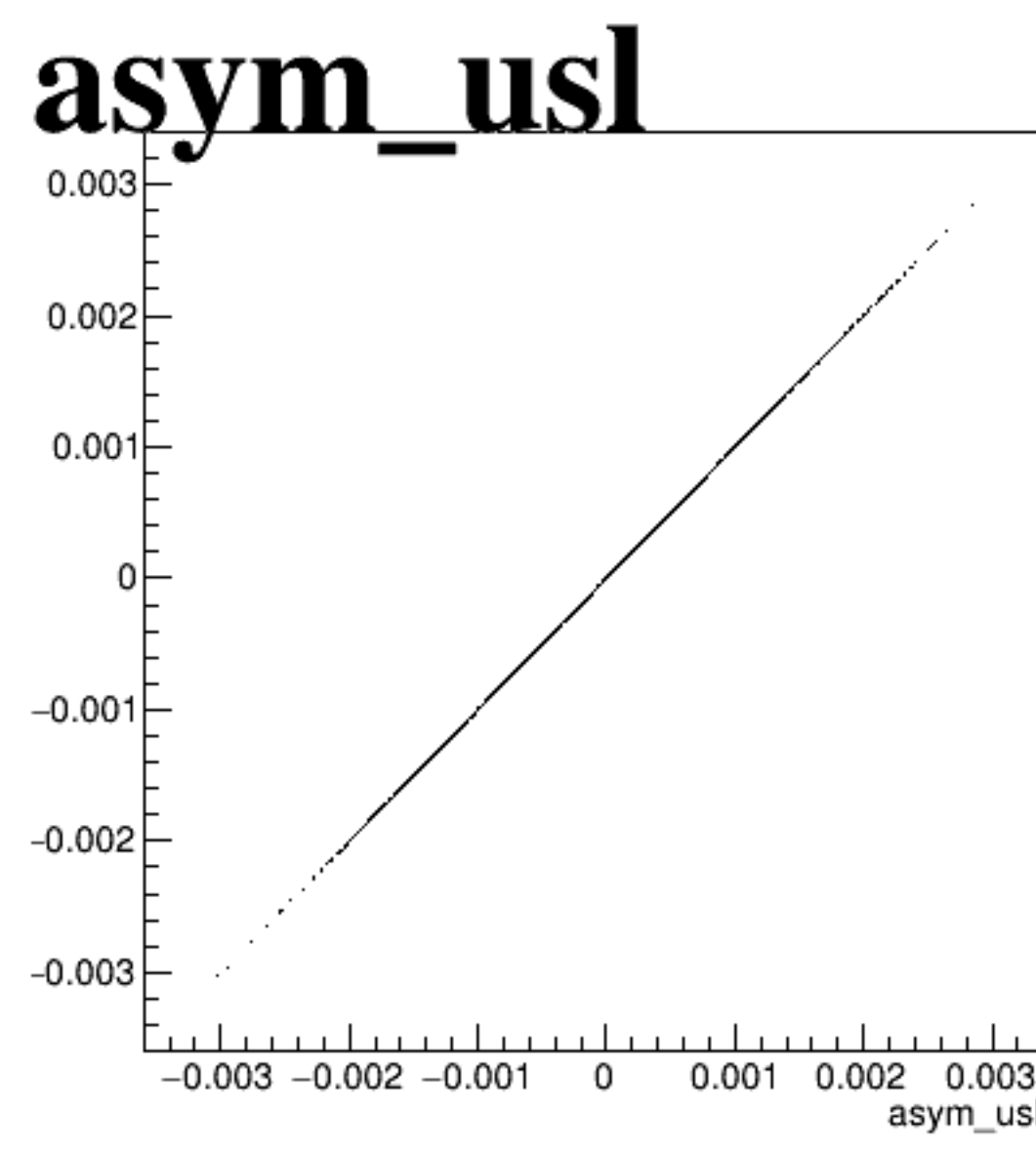
asym_usr



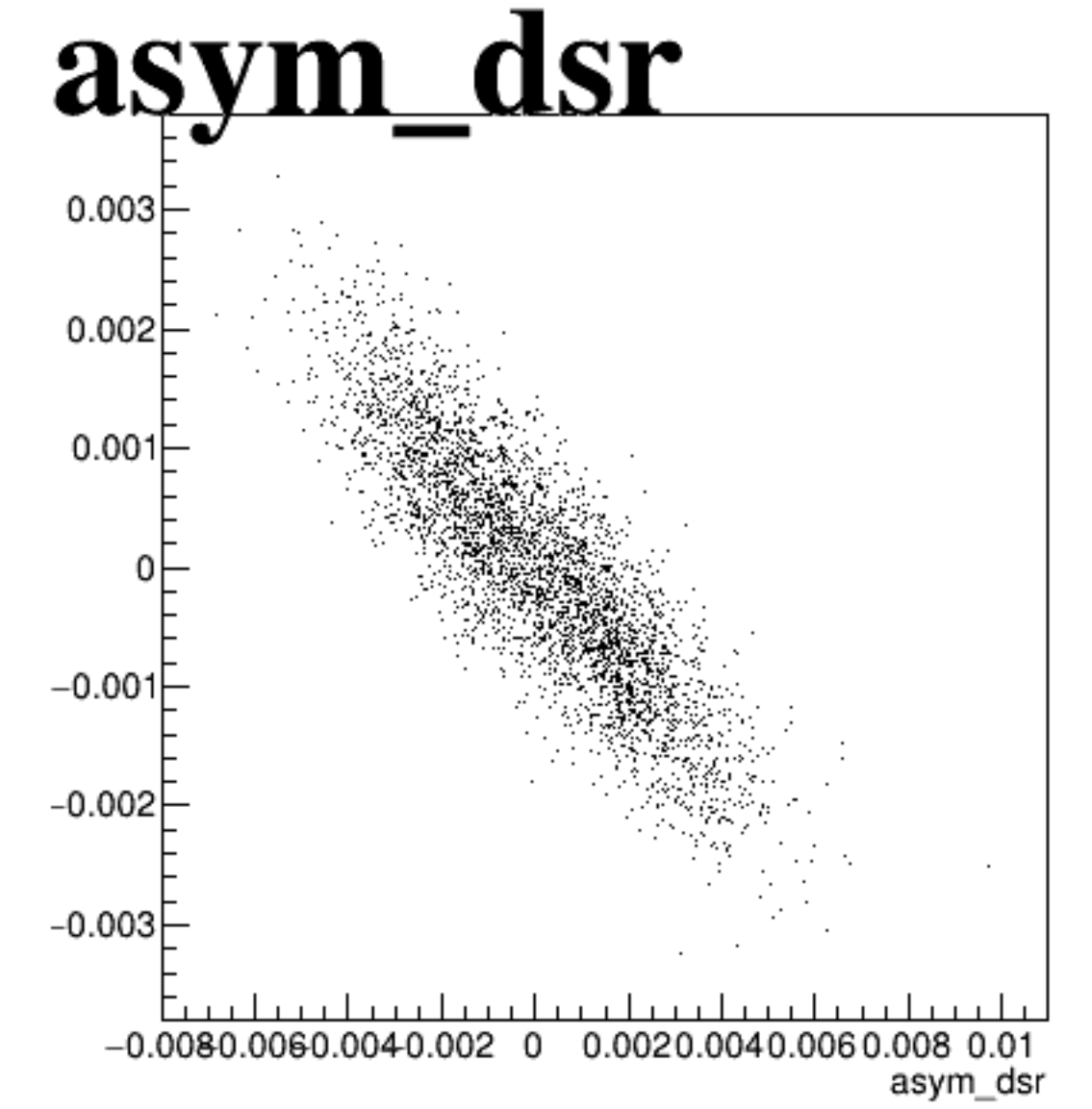
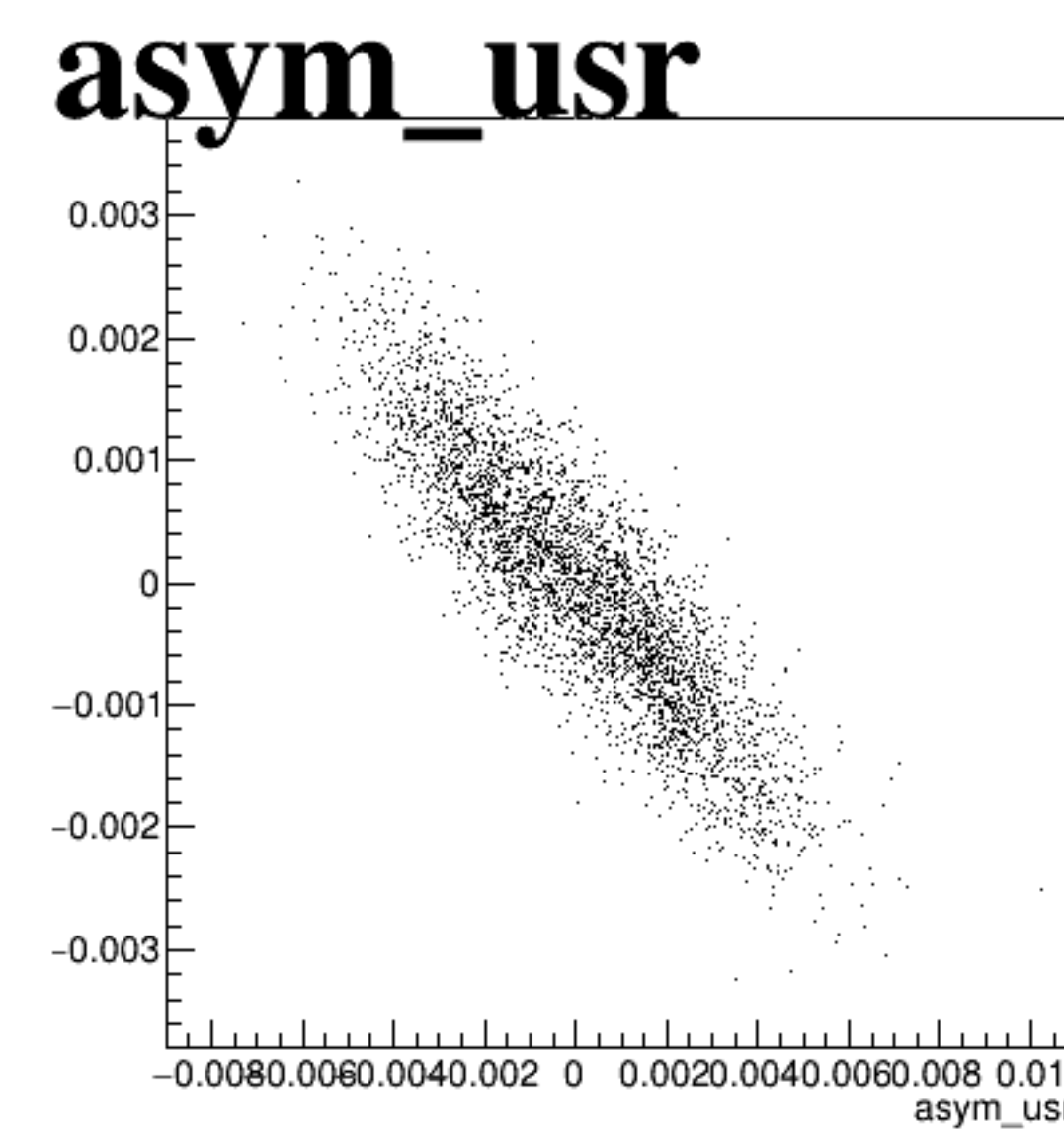
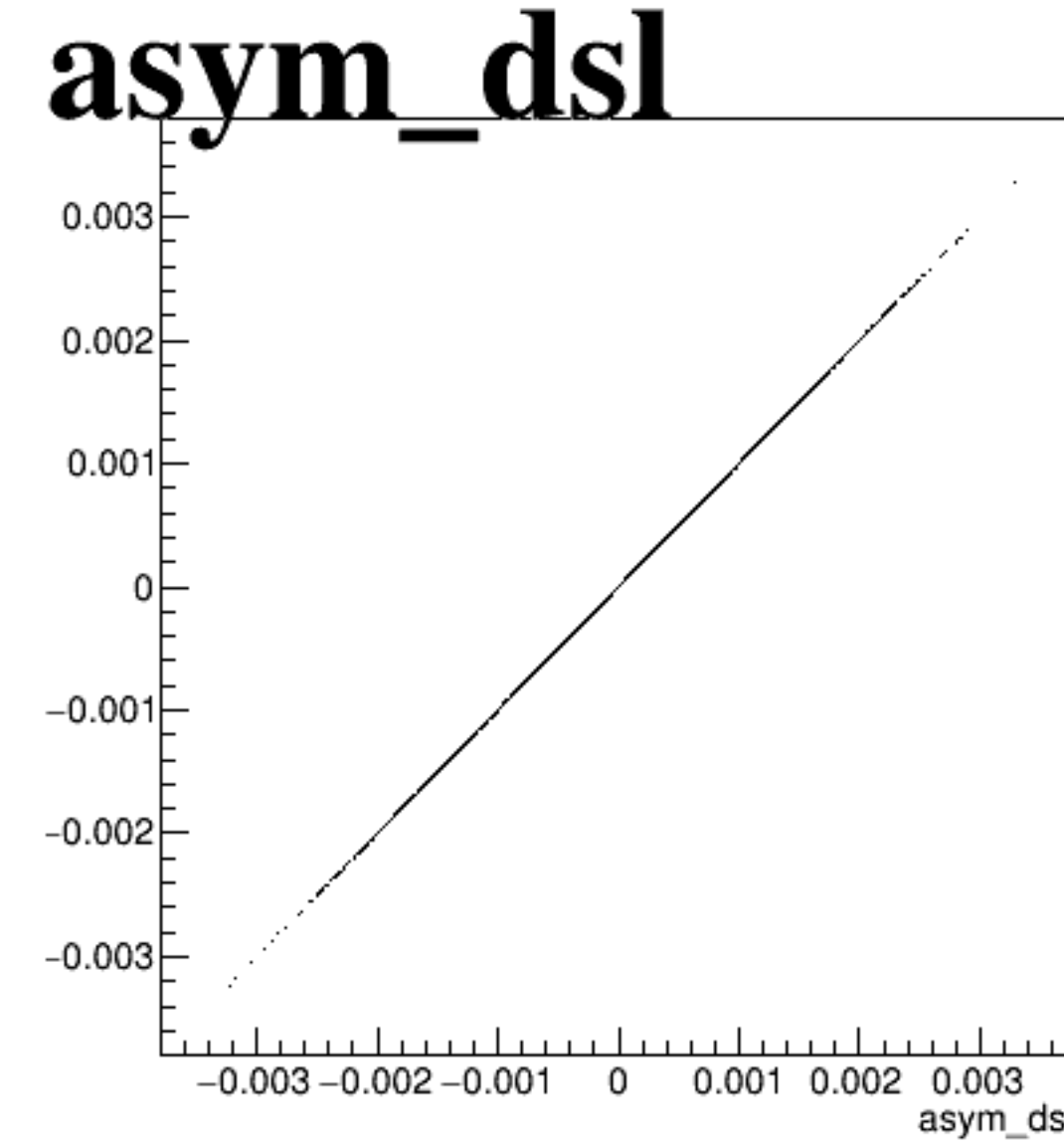
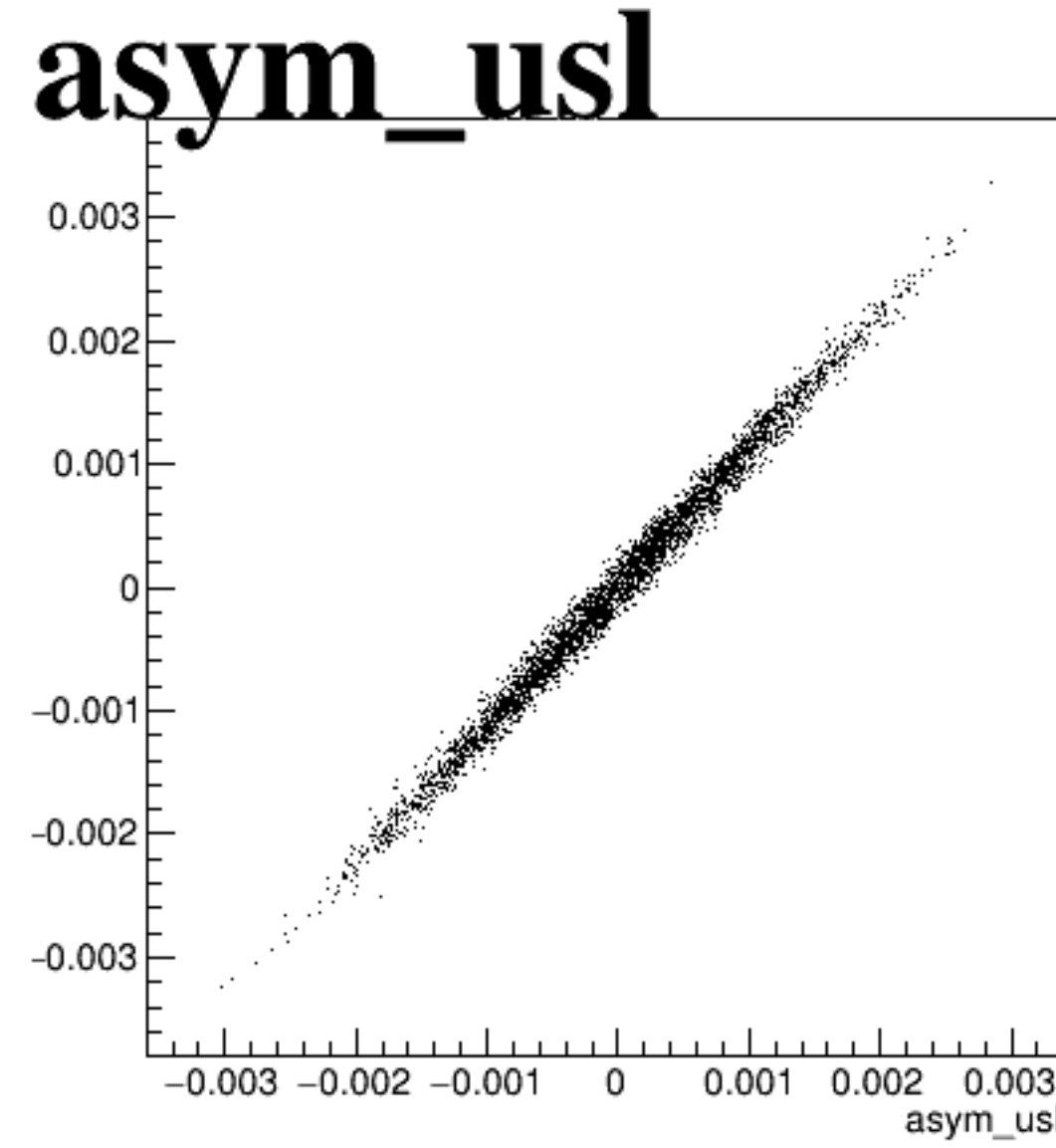
asym_dsr



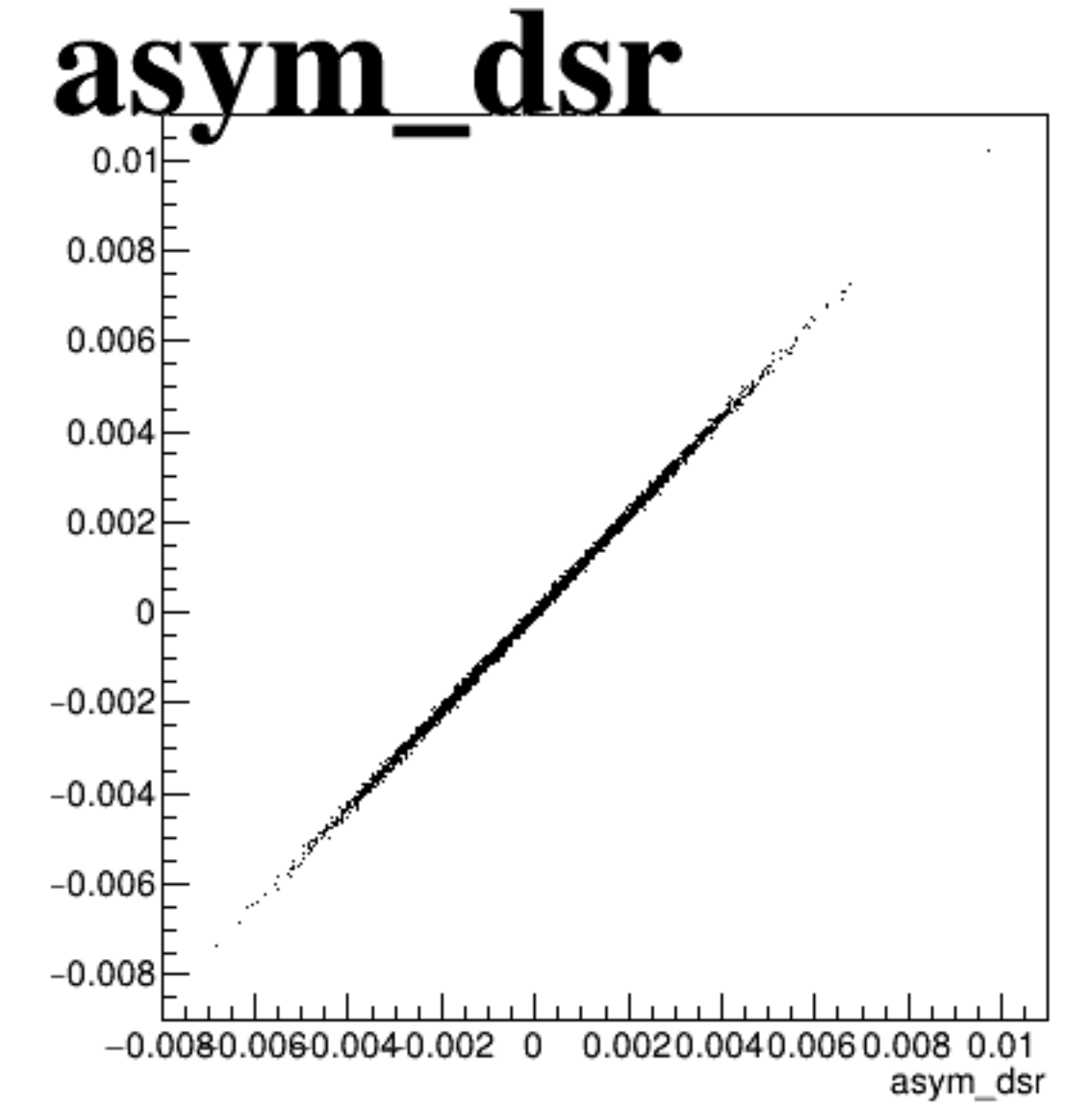
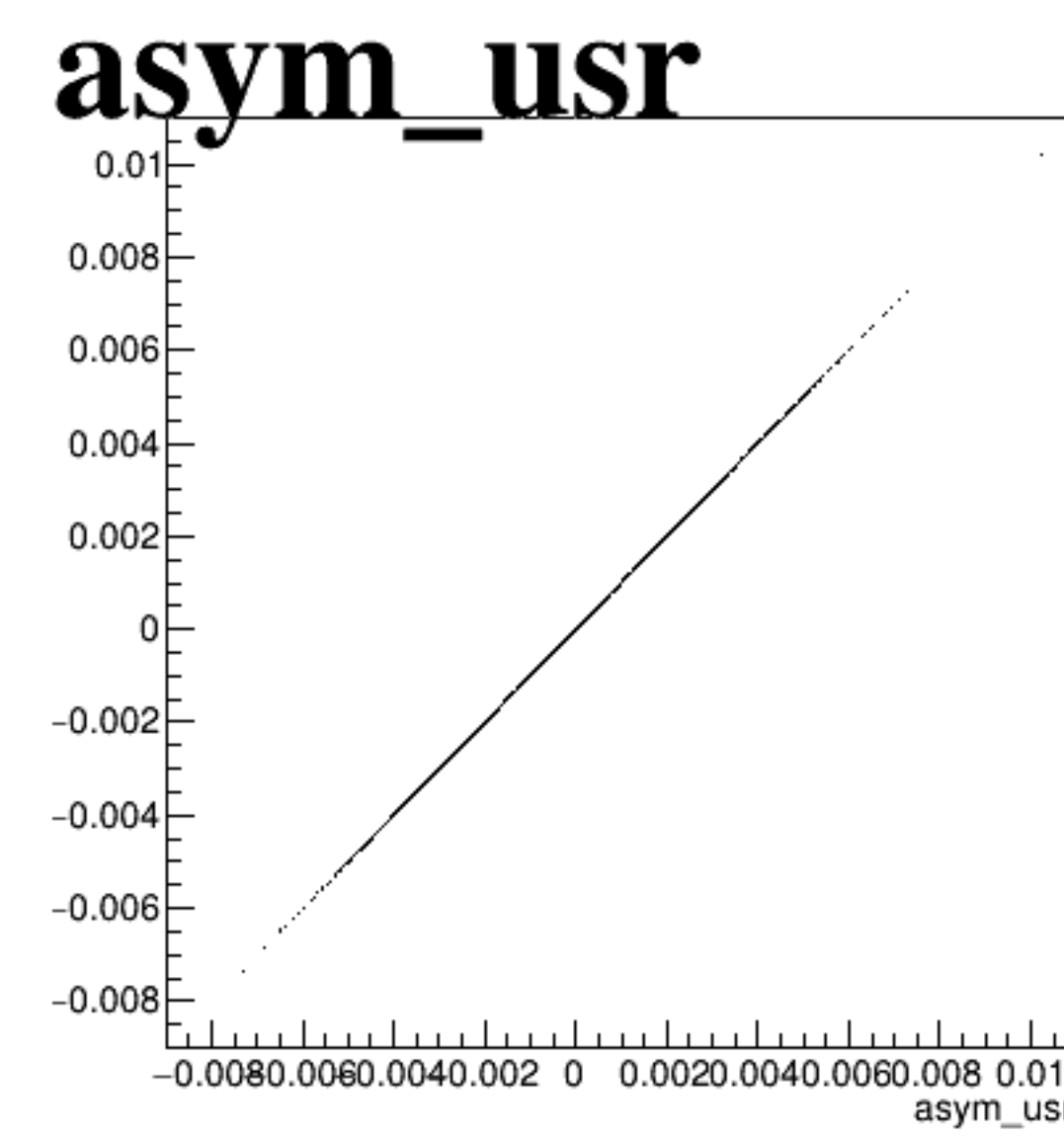
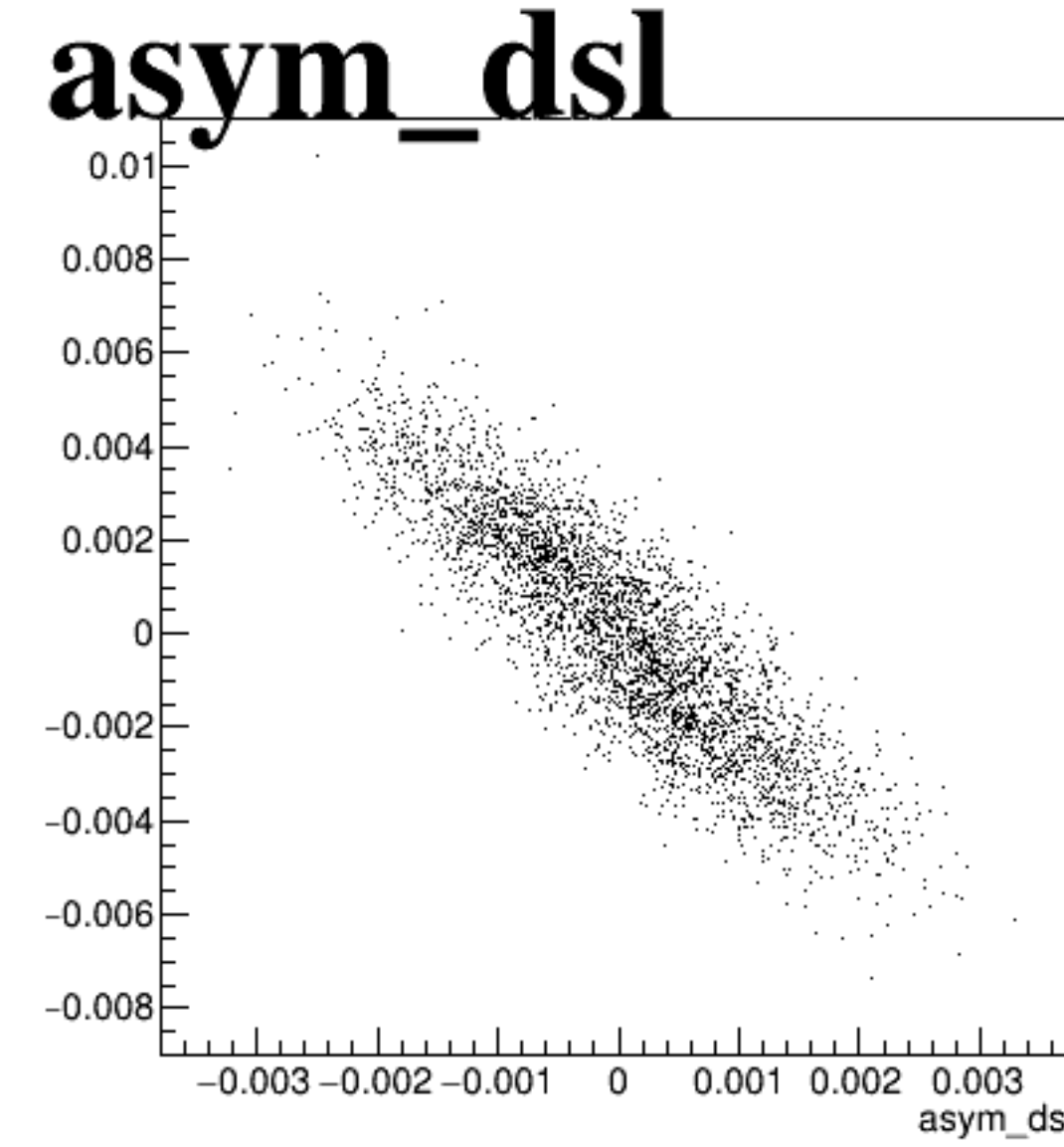
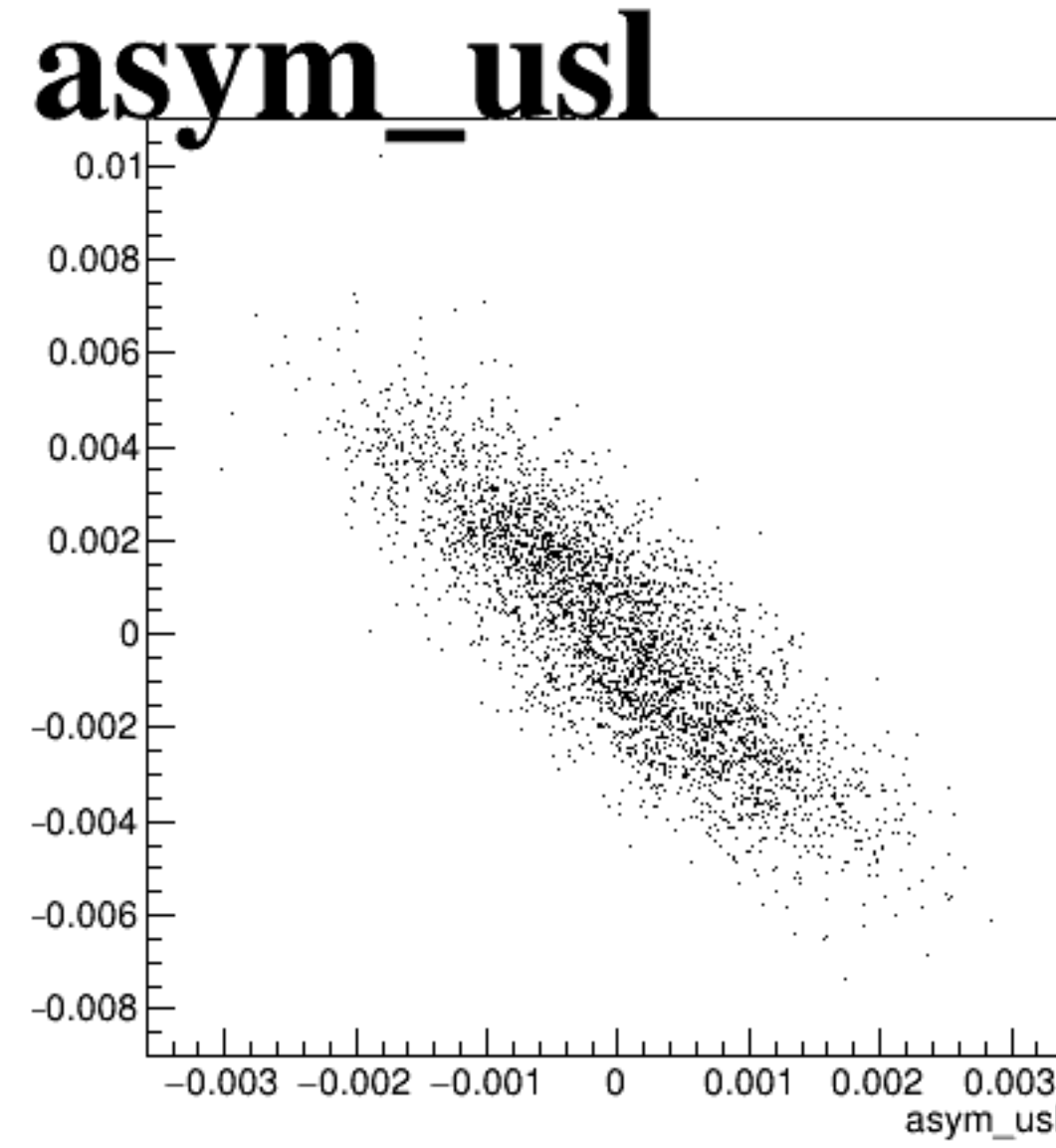
asym_usl



asym_dsl



asym_usr



asym_dsr

