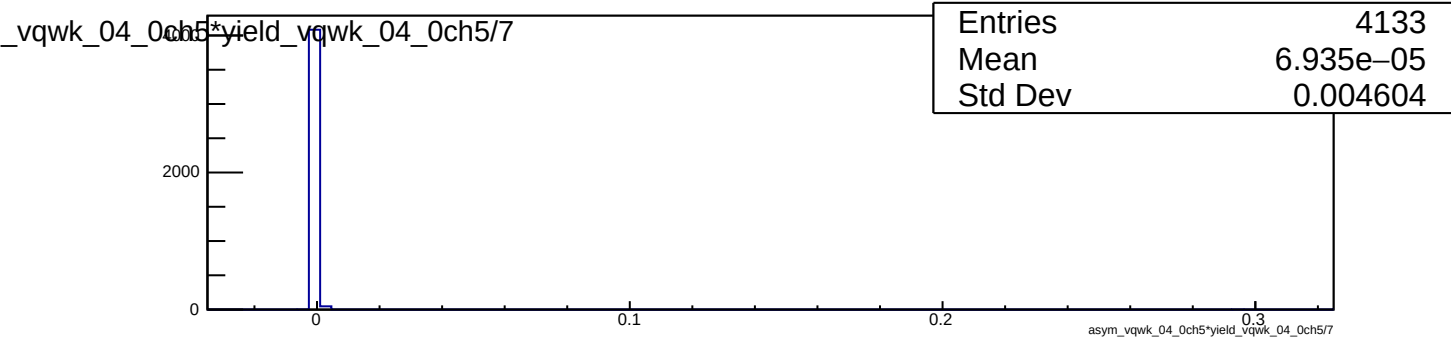
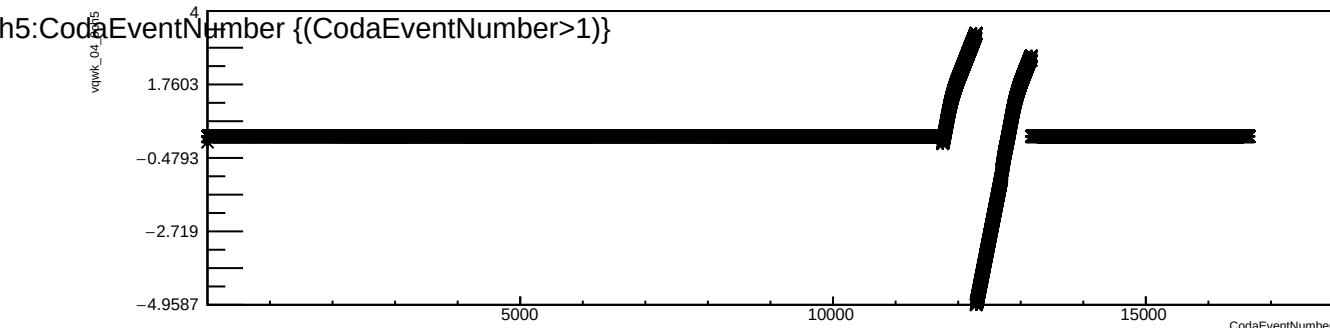
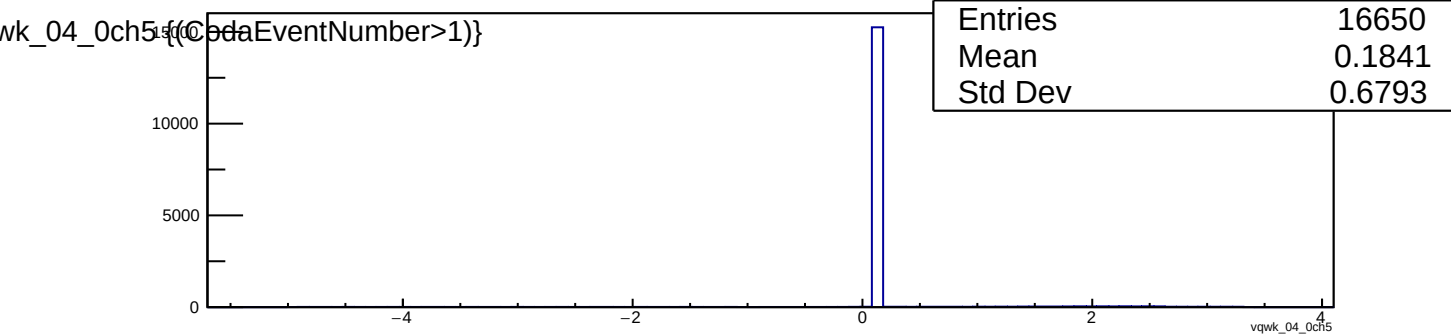
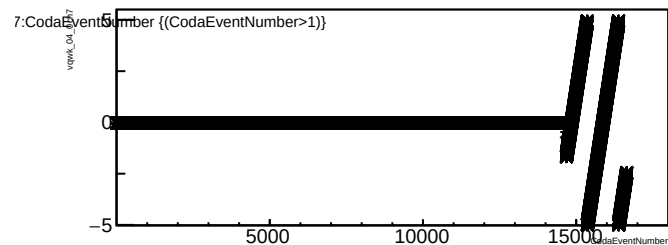
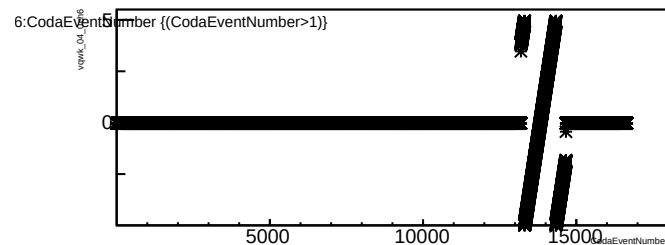
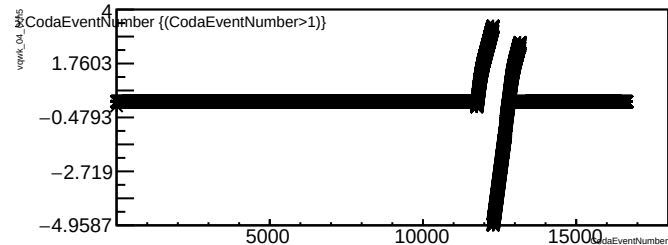
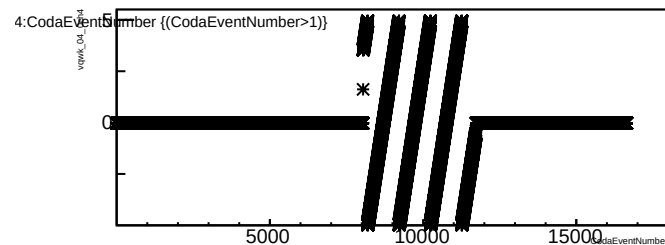
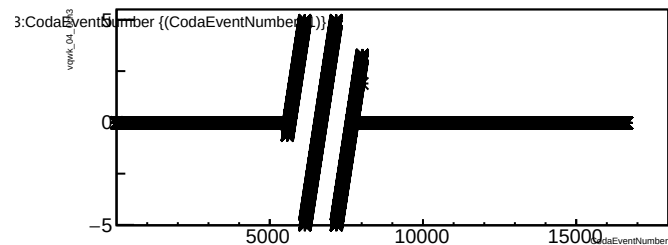
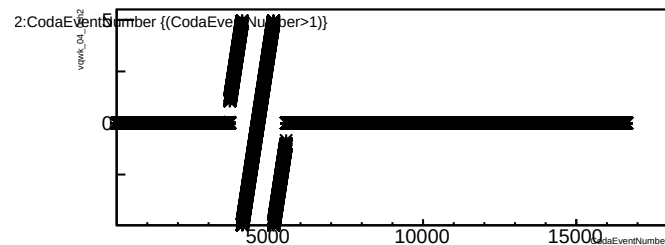
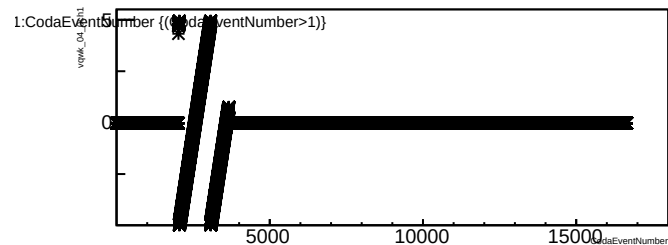
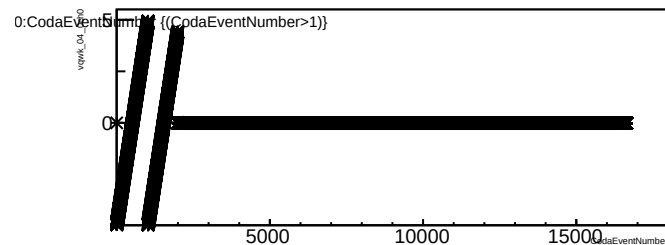


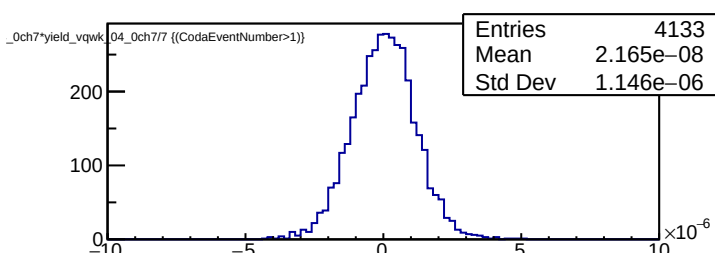
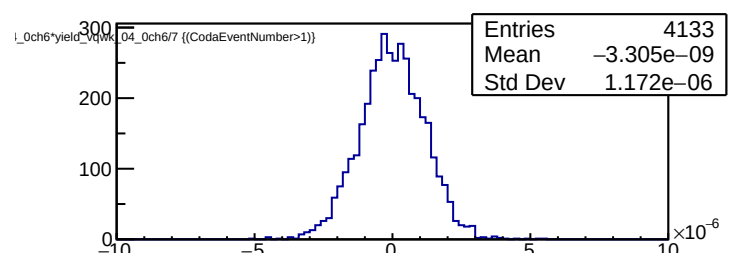
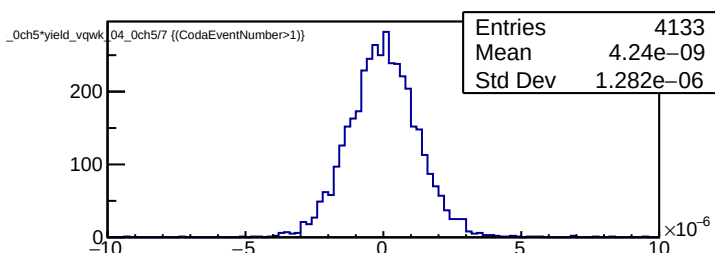
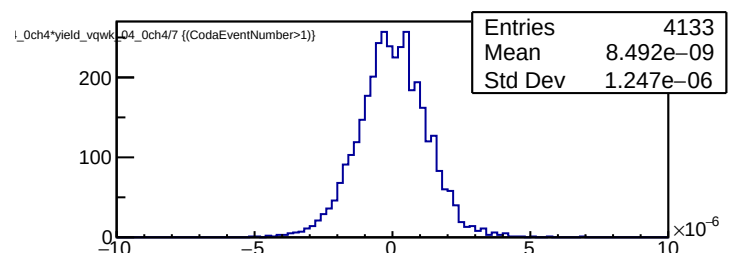
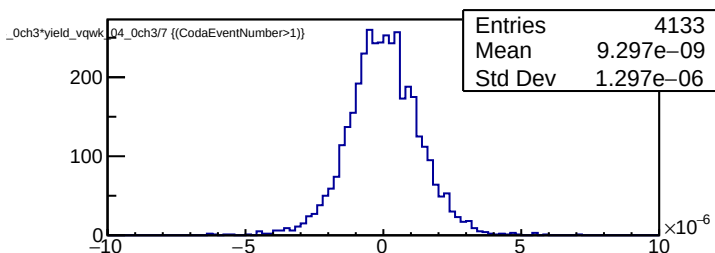
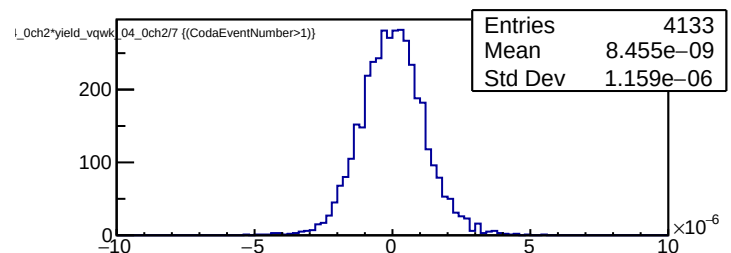
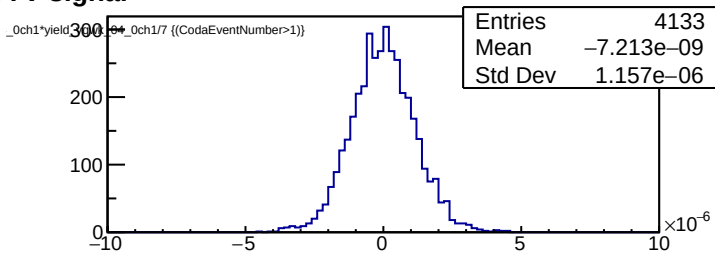
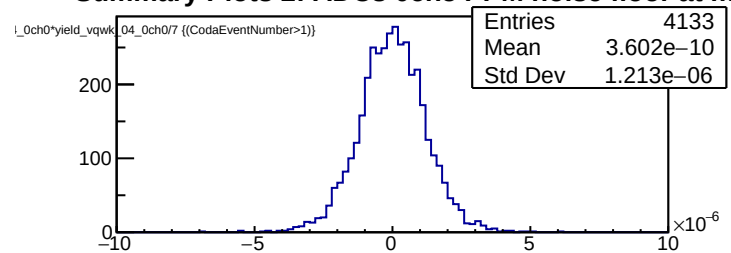
# Summary Plots 0: vqwk\_04\_0chs test



# Summary Plots 1: ADC vs event number



# Summary Plots 2: ADCs 0ch5 PPM noise floor at max 7V signal



Summary Plots 3: (PPM noise floor at max 7V signal) w.r.t. vqwk\_6\_04\_0ch5

