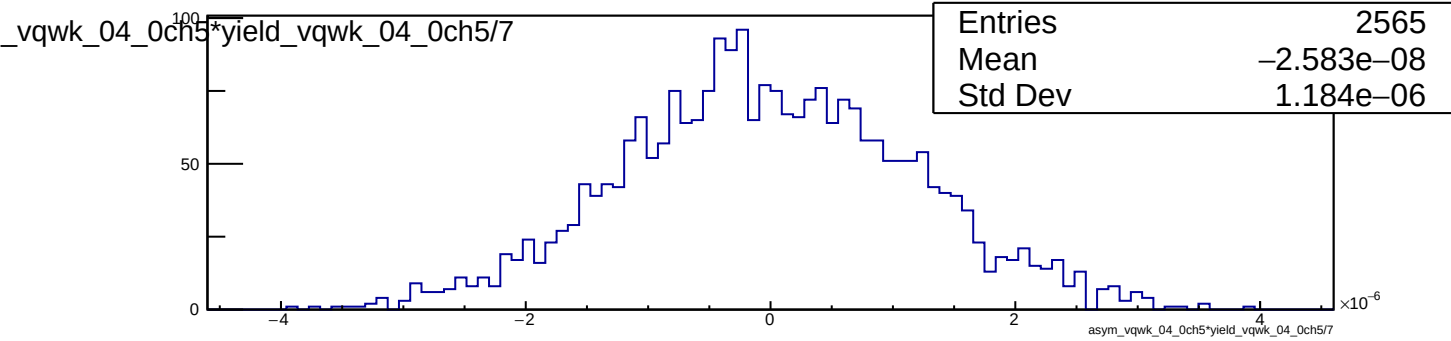
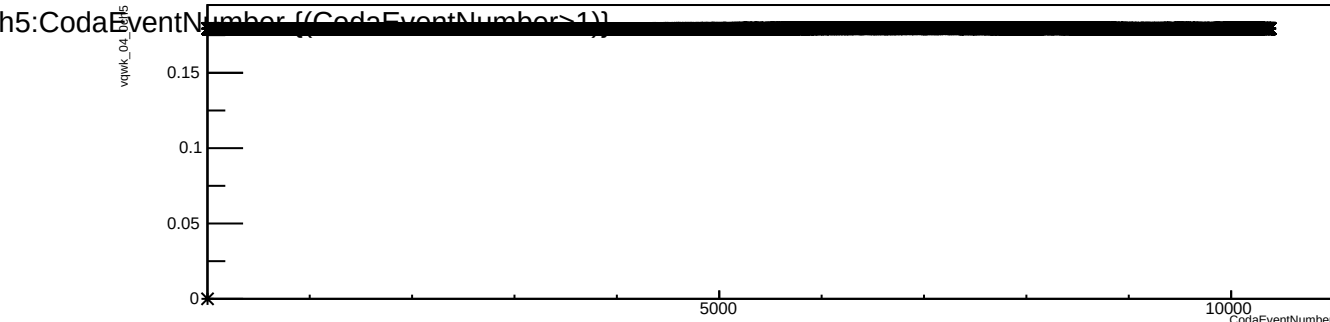
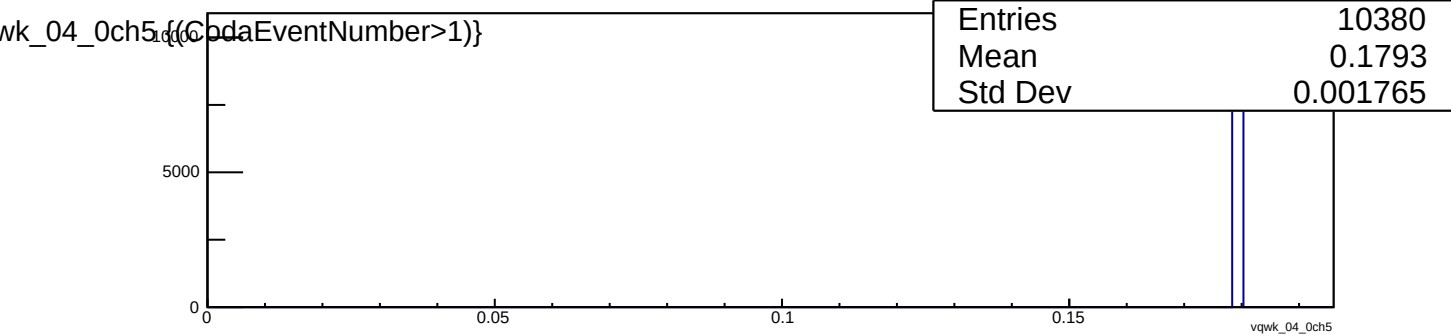
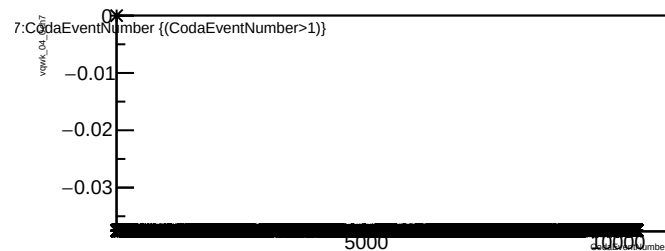
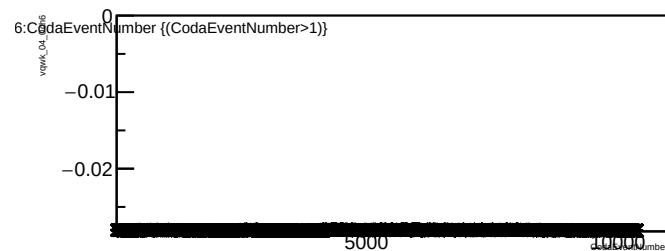
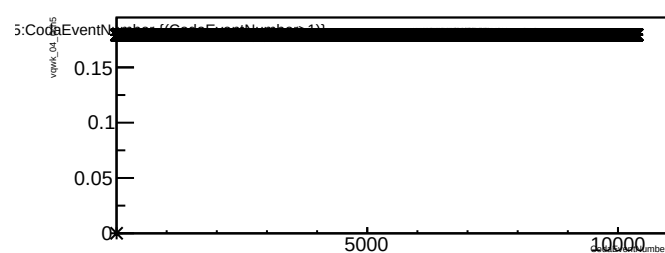
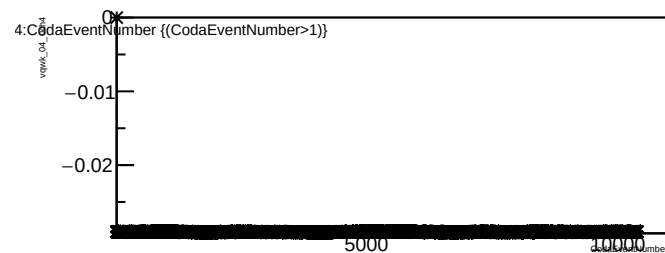
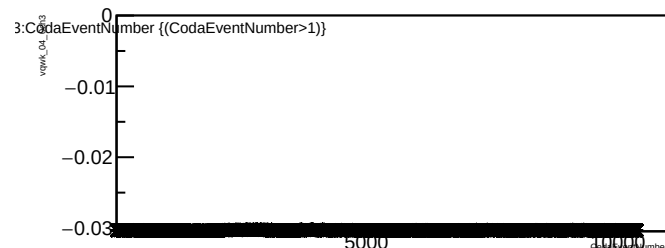
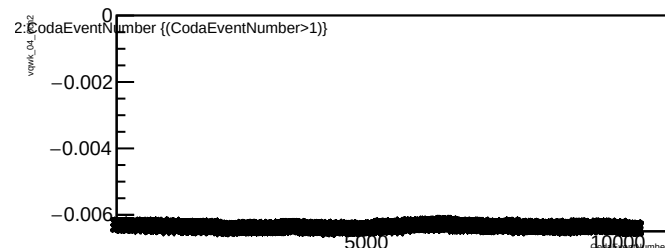
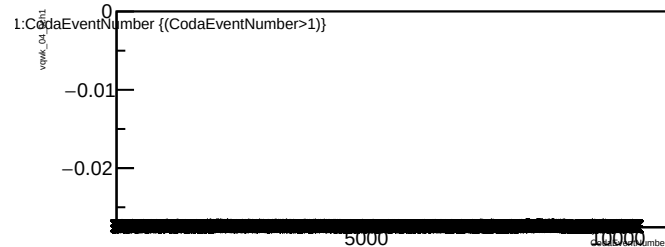
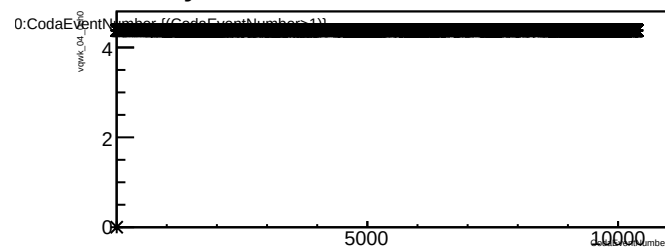


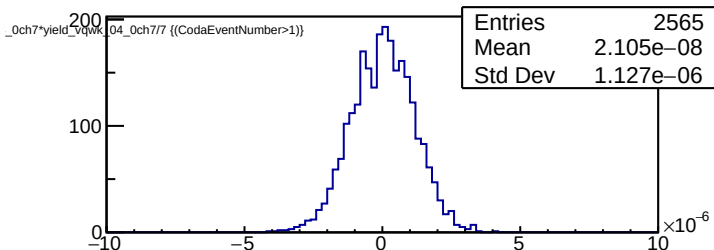
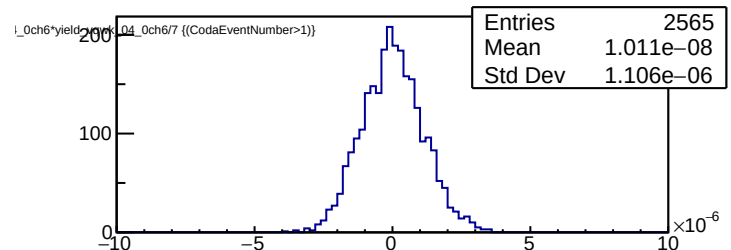
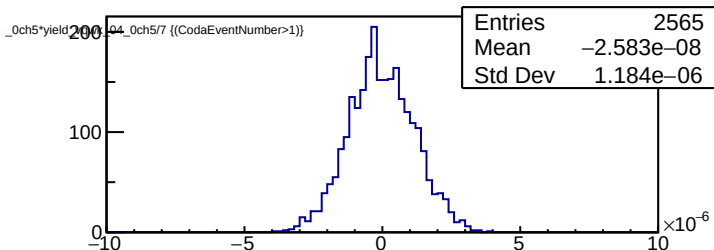
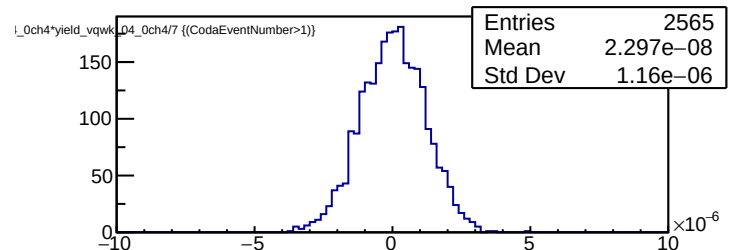
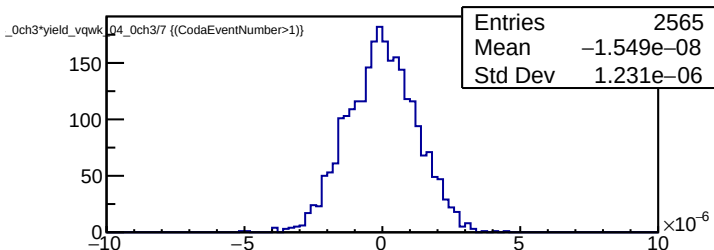
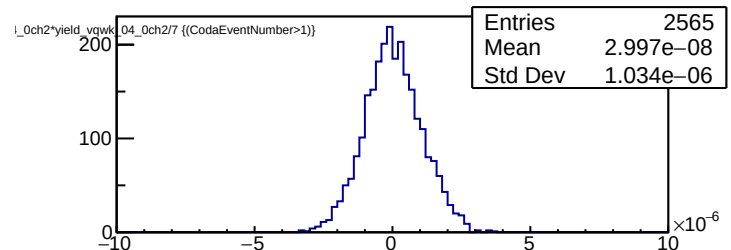
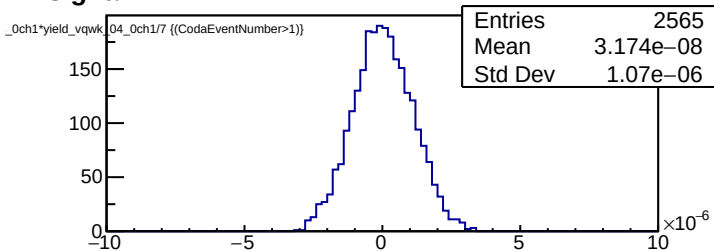
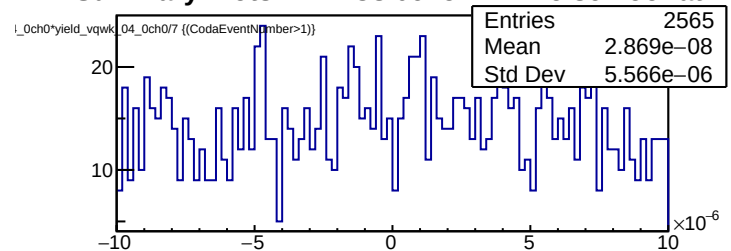
Summary Plots 0: vqwk_04_0chs test



Summary Plots 1: ADC vs event number



Summary Plots 2: ADCs 0ch5 PPM noise floor at max 7V signal



Summary Plots 3: (PPM noise floor at max 7V signal) w.r.t. vpwk_04_0ch5

