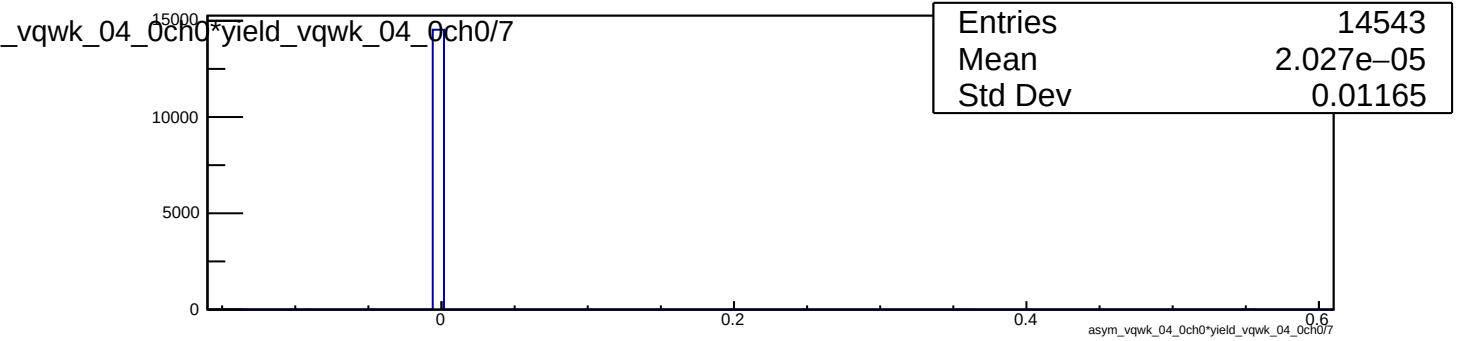
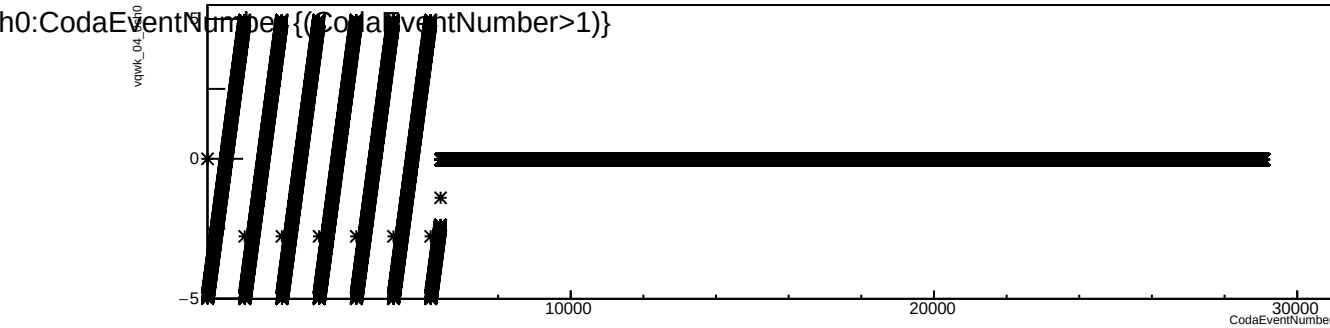
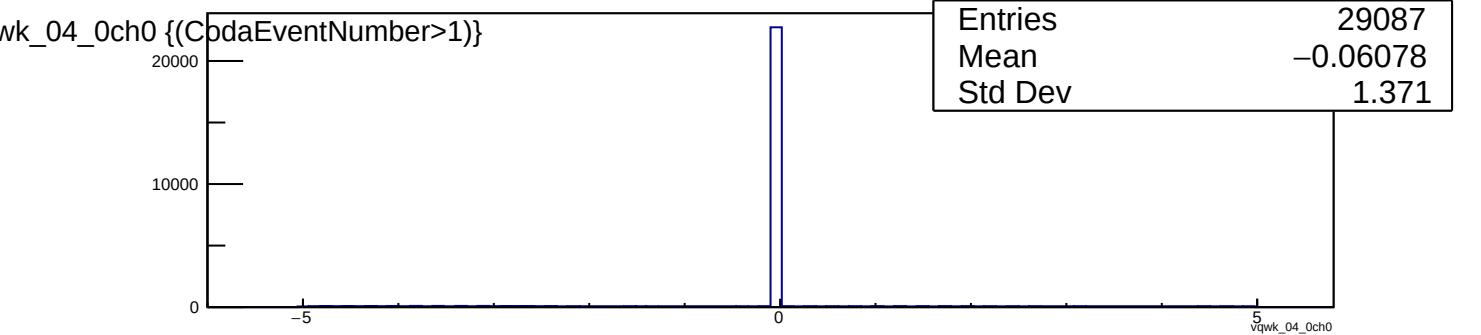
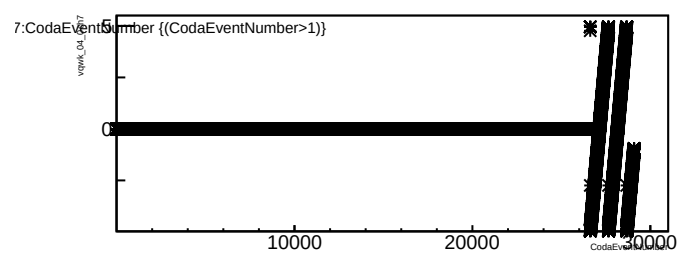
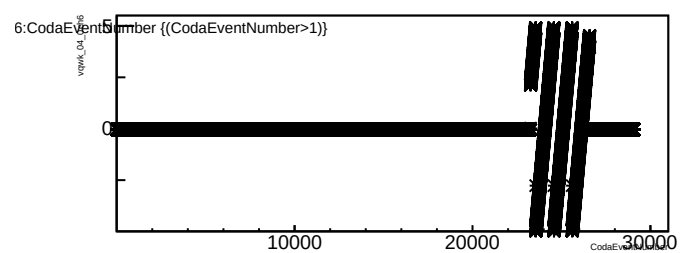
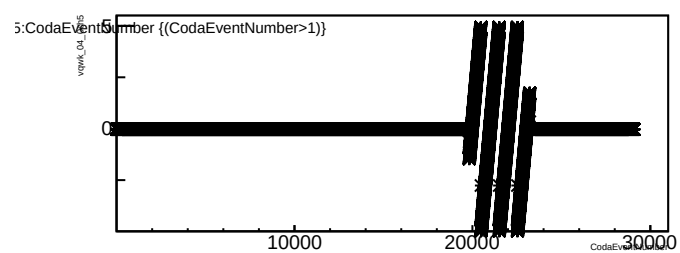
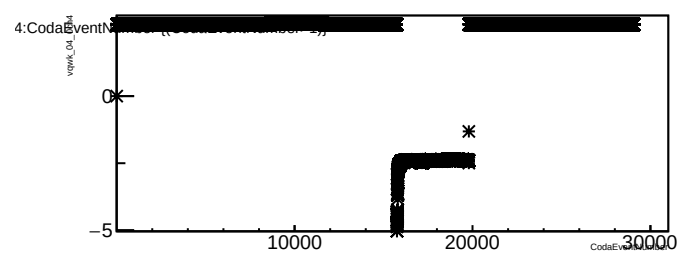
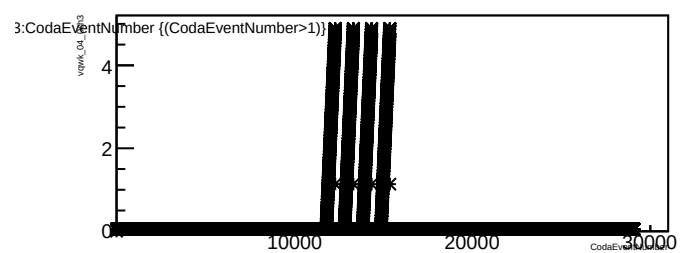
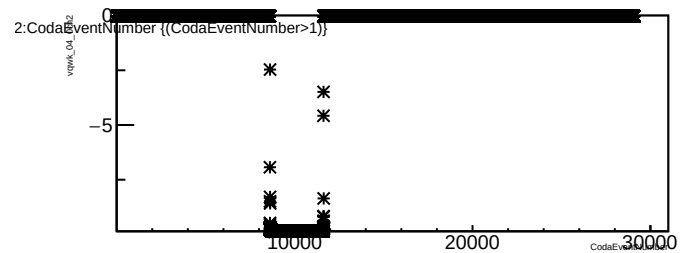
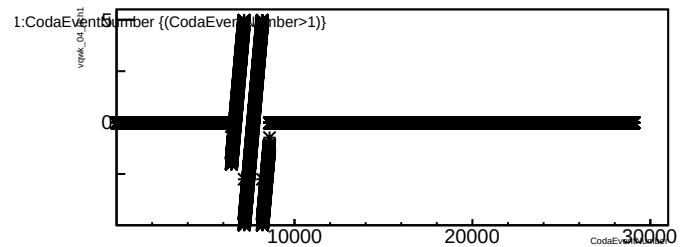
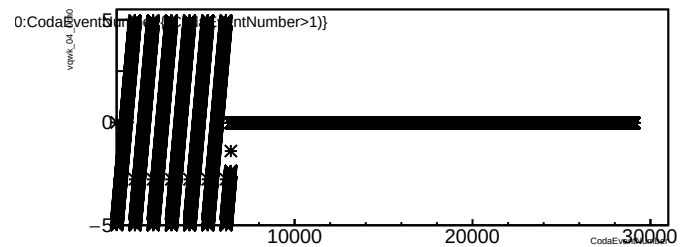


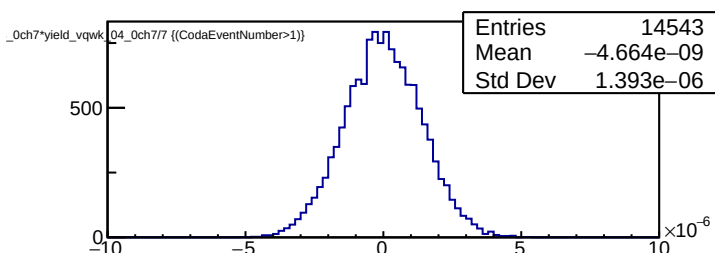
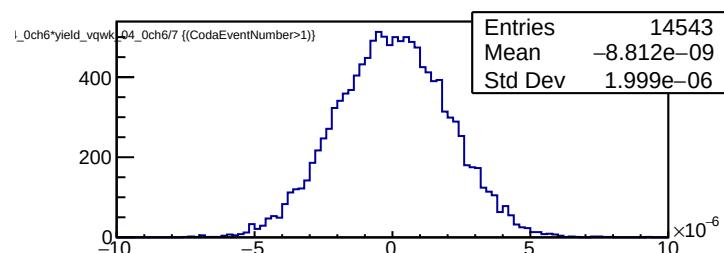
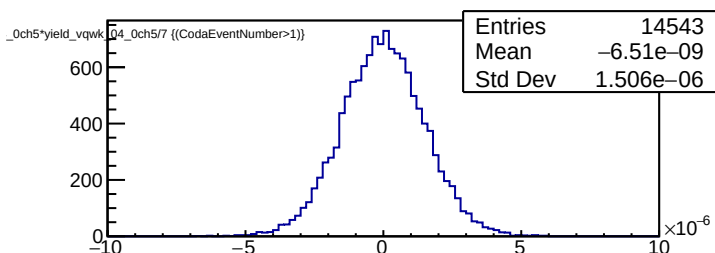
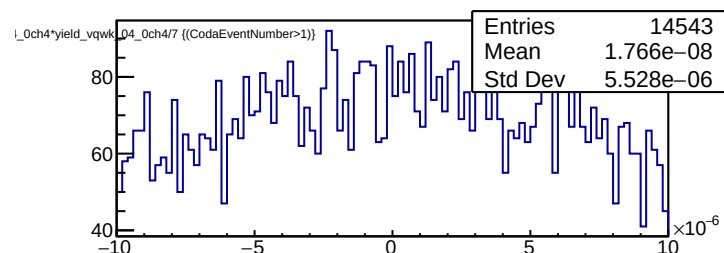
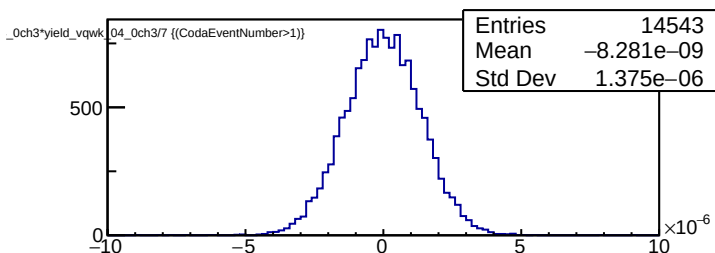
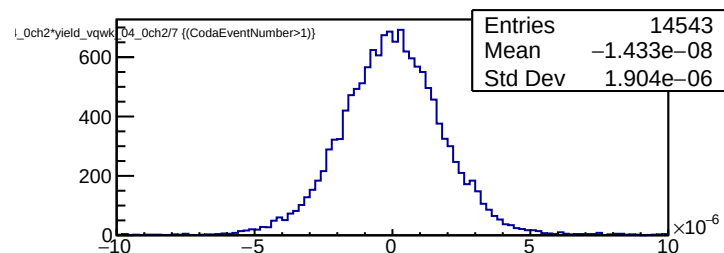
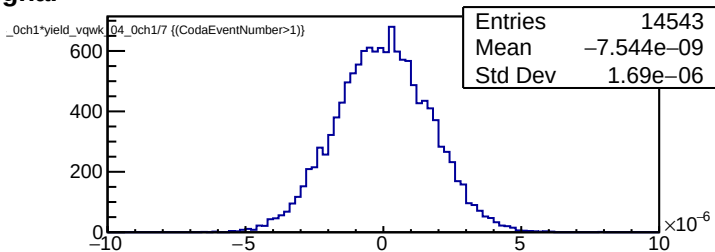
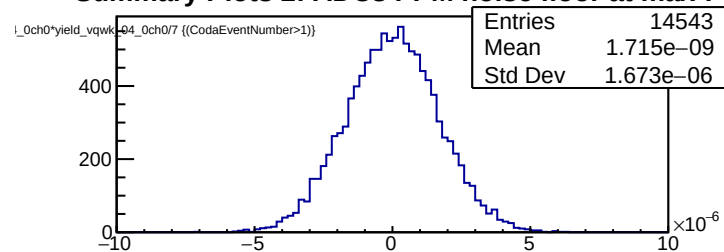
Summary Plots 0: vqwk_04_0chs test



Summary Plots 1: ADC vs event number



Summary Plots 2: ADCs PPM noise floor at max 7V signal



Summary Plots 3: (PPM noise floor at max 7V signal) w.r.t. v_{qwk}.04_0ch 0

